

MOS FIELD EFFECT TRANSISTOR 2SK3358

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK3358 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

★

- Low on-state resistance $R_{DS(on)1} = 30 \ m\Omega \ MAX. \ (V_{GS} = 10 \ V, \ I_{D} = 28 \ A)$
- $R_{DS(on)2} = 40 \text{ m}\Omega \text{ MAX.} (V_{GS} = 4.5 \text{ V}, \text{ ID} = 20 \text{ A})$
- Low Ciss: Ciss = 3200 pF TYP.
 - Built-in gate protection diode

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

| Drain to Source Voltage (Vcs = 0 V) | VDSS | 100 | V |
|---|----------|-------------|----|
| Gate to Source Voltage ($V_{DS} = 0 V$) | VGSS(AC) | ±20 | V |
| Gate to Source Voltage ($V_{DS} = 0 V$) | VGSS(DC) | +20, -10 | V |
| Drain Current (DC) | D(DC) | ±55 | А |
| Drain Current (Pulse) ^{Note1} | D(pulse) | ±165 | А |
| Total Power Dissipation (Tc = 25°C) | Рт | 100 | W |
| Total Power Dissipation ($T_A = 25^{\circ}C$) | Рт | 1.5 | W |
| Channel Temperature | Tch | 150 | °C |
| Storage Temperature | Tstg | –55 to +150 | °C |
| Single Avalanche Current Note2 | las | 39 | А |
| Single Avalanche Energy Note2 | Eas | 152 | mJ |
| | | | |

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1 %

2. Starting T_{ch} = 25 °C, R_G = 25 Ω , V_{GS} = 20 V \rightarrow 0 V

THERMAL RESISTANCE

| Channel to Case | Rth(ch-C) | 1.25 | °C/W |
|--------------------|-----------|------|------|
| Channel to Ambient | Rth(ch-A) | 83.3 | °C/W |

ORDERING INFORMATION

| PART NUMBER | PACKAGE | |
|-------------|-----------|--|
| 2SK3358 | TO-220AB | |
| 2SK3358-S | TO-262 | |
| 2SK3358-Z | TO-220SMD | |



(TO-220AB)

(TO-262)







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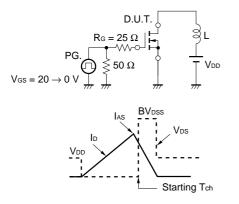
The mark **★** shows major revised points.

| CHARACTERISTICS | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|-------------------------------------|----------------------|---|------|------|------|------|
| Drain to Source On-state Resistance | RDS(on)1 | Vgs = 10 V, Id = 28 A | | 20 | 30 | mΩ |
| | RDS(on)2 | Vgs = 4.5 V, Id = 20 A | | 28 | 40 | mΩ |
| Gate to Source Cut-off Voltage | V _{GS(off)} | Vos = 10 V, Io = 250 μA | 1.5 | 2.0 | 2.5 | V |
| Forward Transfer Admittance | y _{fs} | Vds = 10 V, Id = 28 A | 17 | 35 | | S |
| Drain Leakage Current | loss | Vds = 100 V, Vgs = 0 V | | | 10 | μA |
| Gate to Source Leakage Current | lgss | $V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$ | | | ±10 | μA |
| Input Capacitance | Ciss | V _{DS} = 10 V | | 3200 | | pF |
| Output Capacitance | Coss | V _G s = 0 V | | 640 | | pF |
| Reverse Transfer Capacitance | Crss | f = 1 MHz | | 360 | | pF |
| Turn-on Delay Time | td(on) | ID = 28 A | | 40 | | ns |
| Rise Time | tr | $V_{GS(on)} = 10 V$ | | 300 | | ns |
| Turn-off Delay Time | td(off) | Vdd = 50 V | | 220 | | ns |
| Fall Time | tr | R _G = 10 Ω | | 230 | | ns |
| Total Gate Charge | QG | ID = 55 A | | 84 | | nC |
| Gate to Source Charge | QGS | Vdd = 80 | | 11 | | nC |
| Gate to Drain Charge | Qgd | $V_{GS(on)} = 10 V$ | | 31 | | nC |
| Body Diode Forward Voltage | VF(S-D) | IF = 55 A, VGs = 0 V | | 1.0 | | V |
| Reverse Recovery Time | trr | IF = 55 A, VGS = 0 V | | 160 | | ns |
| Reverse Recovery Charge | Qrr | $di/dt = 100 \text{ A}/\mu \text{s}$ | | 760 | | nC |

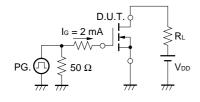
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

NEC

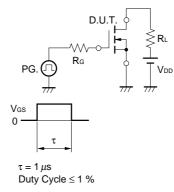
TEST CIRCUIT 1 AVALANCHE CAPABILITY

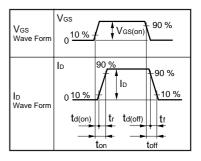


TEST CIRCUIT 3 GATE CHARGE



TEST CIRCUIT 2 SWITCHING TIME

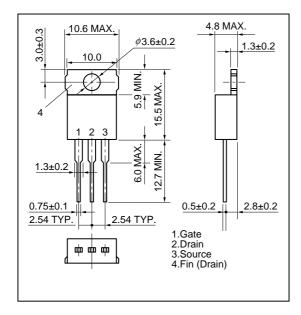




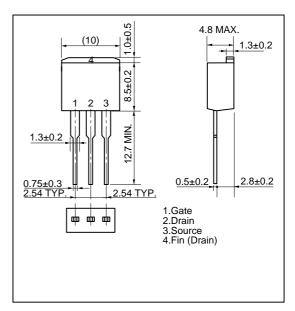
Preliminary Data Sheet D14322EJ1V0DS00

PACKAGE DRAWINGS (Unit : mm)

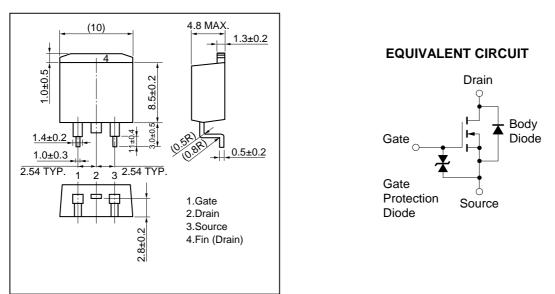
1)TO-220AB (MP-25)



2)TO-262 (MP-25 Fin Cut)



3)TO-220SMD (MP-25Z)



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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