

**HIGH ISOLATION VOLTAGE
DARLINGTON TRANSISTOR TYPE
SOP PHOTOCOUPLER**

-NEPOC™ Series-

DESCRIPTION

The PS2802-1 and PS2802-4 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon darlington-connected photo transistor in a plastic SOP for high density applications.

This package has shield effect to cut off ambient light.

FEATURES

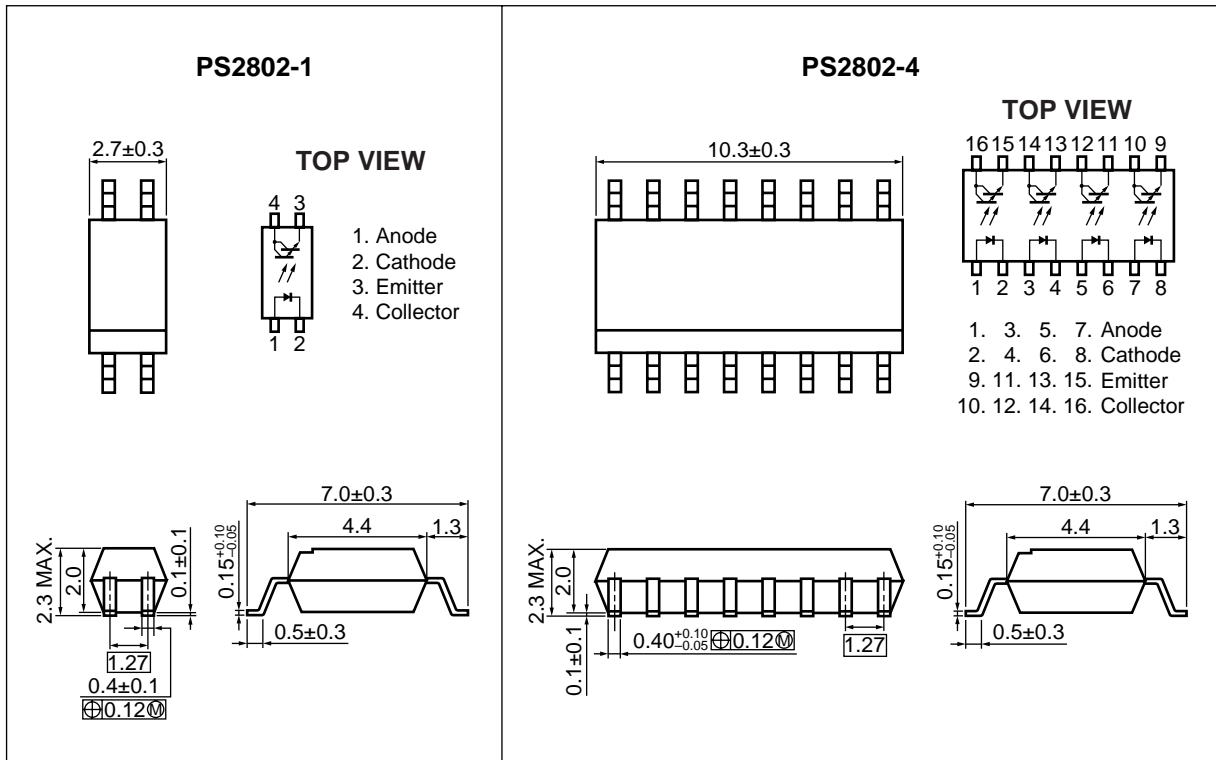
- High isolation voltage ($BV = 2\,500\text{ Vr.m.s.}$)
- Small and thin package (4,16-pin SOP, Pin pitch 1.27 mm)
- High current transfer ratio ($CTR = 2\,000\% \text{ TYP. @ } I_F = 1\text{ mA, } V_{CE} = 2\text{ V}$)
- UL approved: File No. E72422 (S)
- ★ VDE0884 approved (Option): PS2802-4 only
- Ordering number of taping product: PS2802-1-F3, F4, PS2802-4-F3, F4

APPLICATIONS

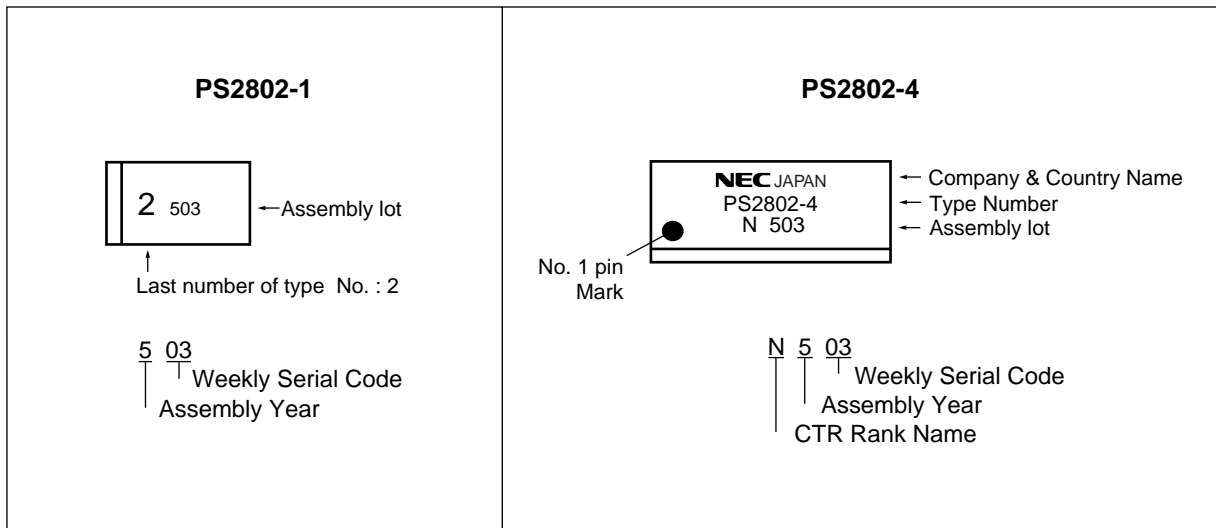
- Programmable logic controllers
- Measuring instruments
- Hybrid IC

The information in this document is subject to change without notice.

★ PACKAGE DIMENSIONS (in millimeters)



MARKING



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS2802-1	PS2802-4	
Diode	Forward Current (DC)	I _F	50		mA
	Reverse Voltage	V _R	6		V
	Power Dissipation Derating	ΔP _D /°C	0.6	0.8	mW/°C
	Power Dissipation	P _D	60	80	mW/ch
	Peak Forward Current ^{*1}	I _{FP}	1		A
Transistor	Collector to Emitter Voltage	V _{CEO}	40		V
	Emitter to Collector Voltage	V _{ECO}	6		V
	Collector Current	I _C	90	100	mA/ch
	Power Dissipation Derating	ΔP _C /°C	1.2		mW/°C
	Power Dissipation	P _C	120		mW/ch
Isolation Voltage ^{*2}		BV	2 500		Vr.m.s.
Operating Ambient Temperature		T _A	-55 to +100		°C
Storage Temperature		T _{stg}	-55 to +150		°C

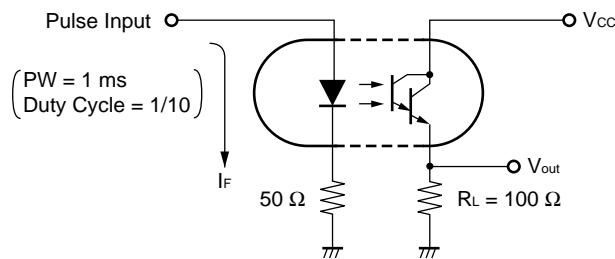
*1 PW = 100 μs, Duty Cycle = 1 %

*2 AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output

ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

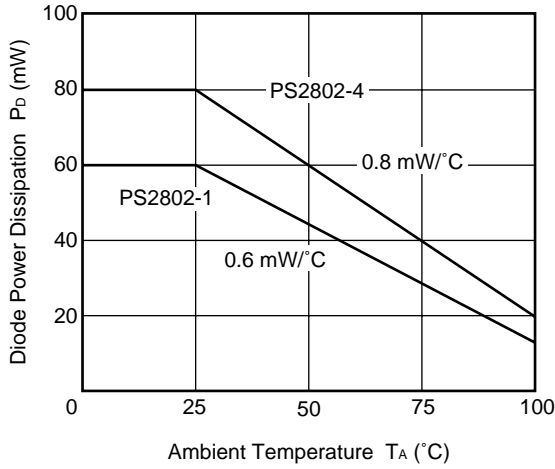
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V _F	I _F = 5 mA		1.1	1.4	V
	Reverse Current	I _R	V _R = 5 V			5	μA
	Terminal Capacitance	C _t	V = 0 V, f = 1.0 MHz		30		pF
Transistor	Collector to Emitter Dark Current	I _{CEO}	V _{CE} = 40 V, I _F = 0 mA			400	nA
Coupled	Current Transfer Ratio (I _c /I _F)	CTR	I _F = 1 mA, V _{CE} = 2 V	200	2 000		%
	Collector Saturation Voltage	V _{CE(sat)}	I _F = 1 mA, I _c = 2 mA			1.0	V
	Isolation Resistance	R _{I-O}	V _{I-O} = 1.0 kV _{DC}	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1.0 MHz		0.4		pF
	Rise Time *1	t _r	V _{CC} = 5 V, I _c = 2 mA, R _L = 100 Ω		200		μs
	Fall Time *1	t _f			200		

*1 Test circuit for switching time

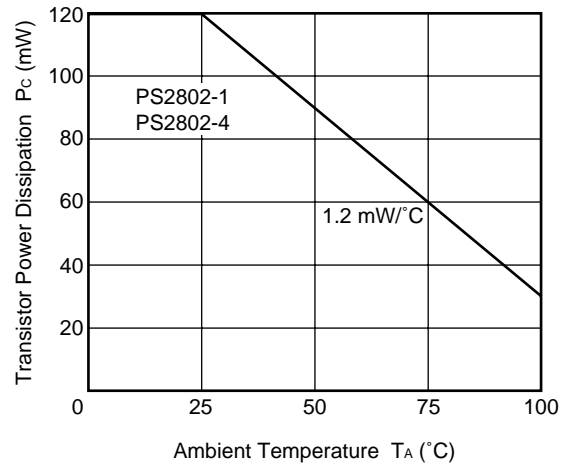


TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise specified)

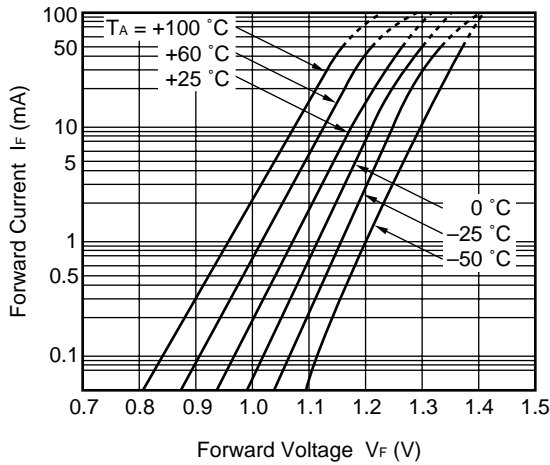
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



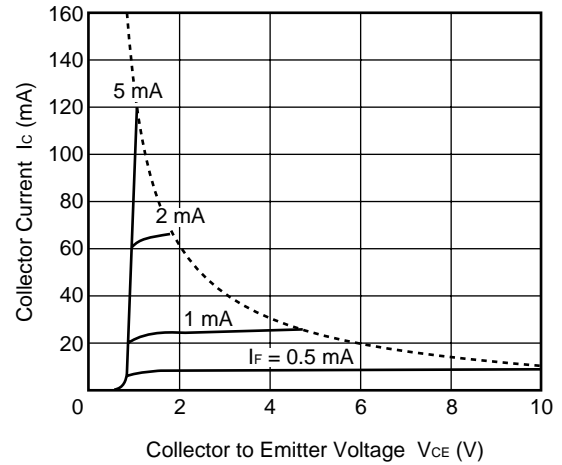
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



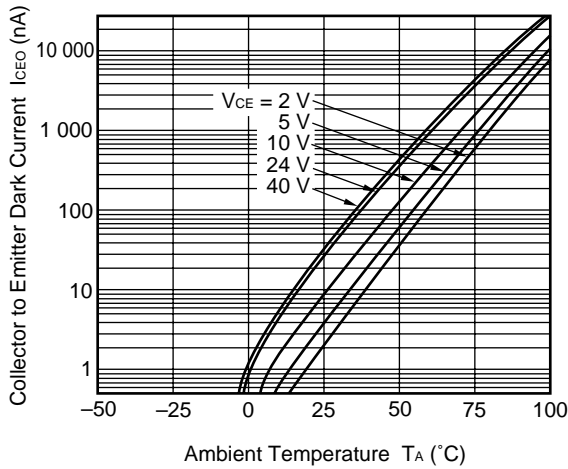
FORWARD CURRENT vs. FORWARD VOLTAGE



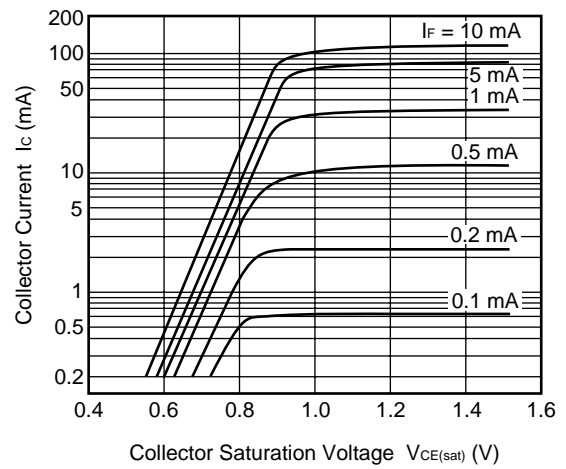
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



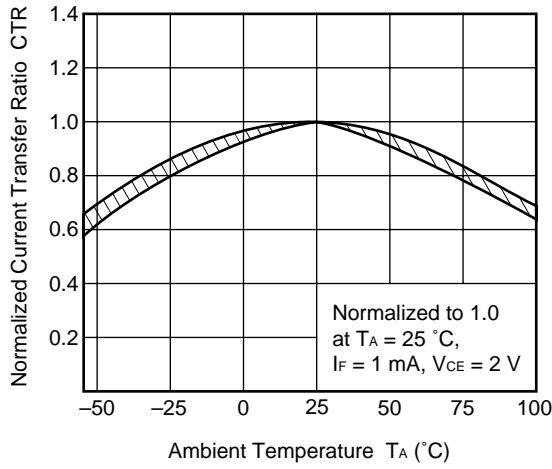
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



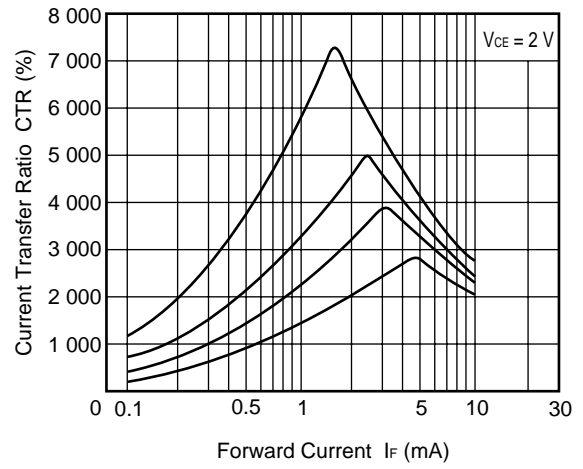
COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



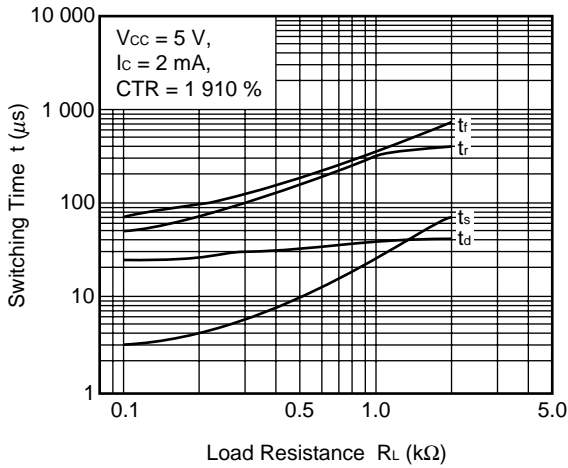
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



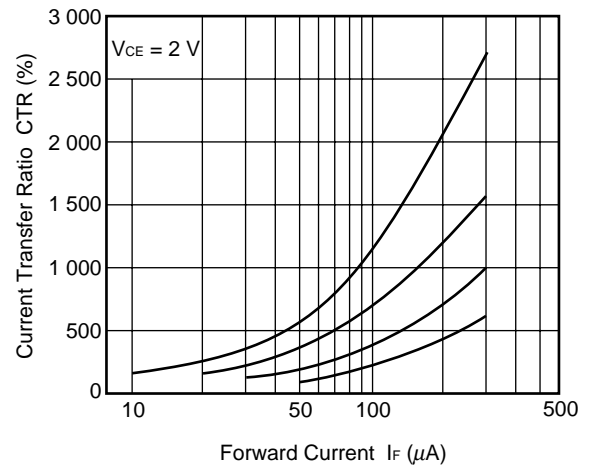
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



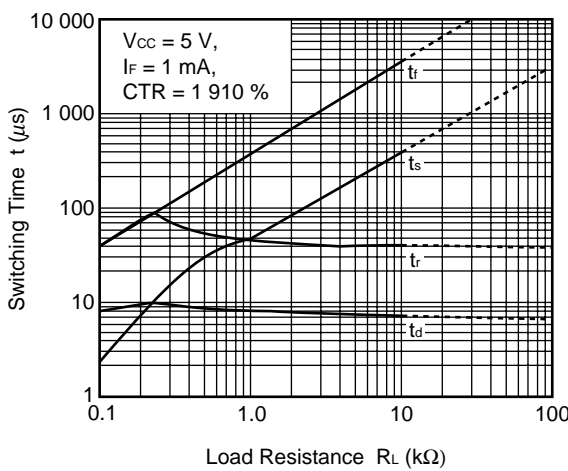
SWITCHING TIME vs. LOAD RESISTANCE



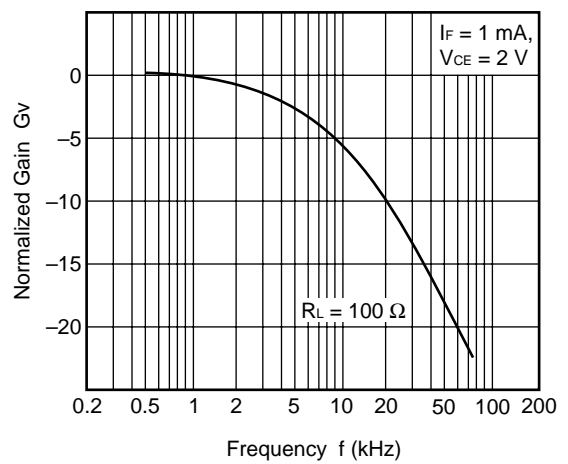
CURRENT TRANSFER RATIO vs. FORWARD CURRENT

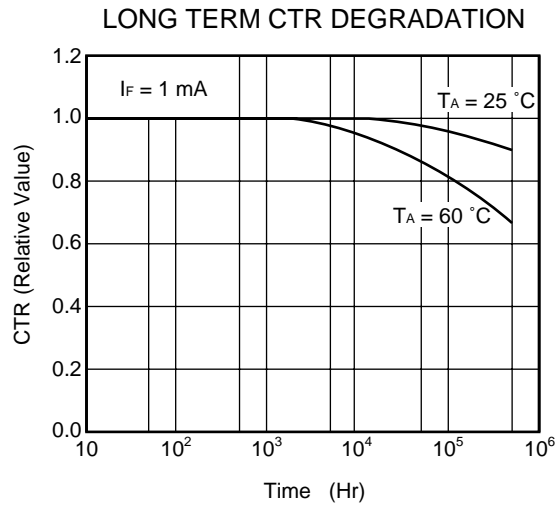


SWITCHING TIME vs. LOAD RESISTANCE



FREQUENCY RESPONSE



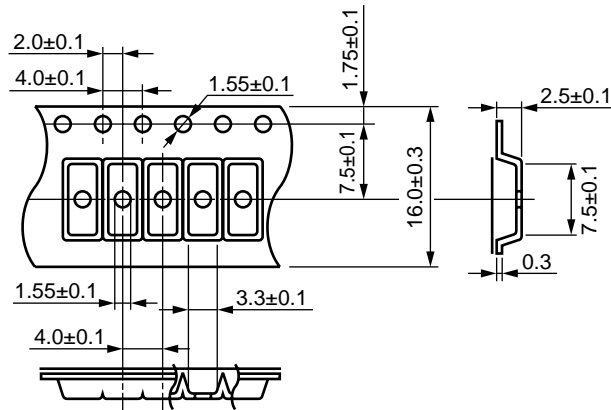


Remark The graphs indicate nominal characteristics.

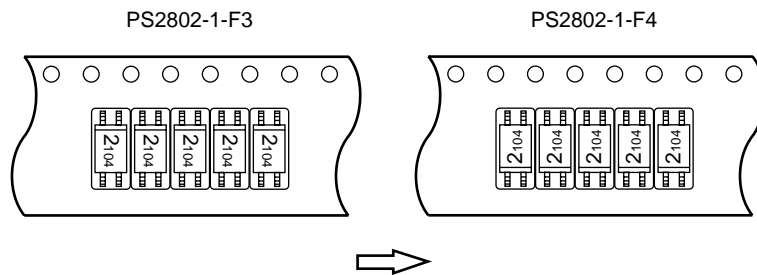
TAPING SPECIFICATIONS (in millimeters)

PS2802-1

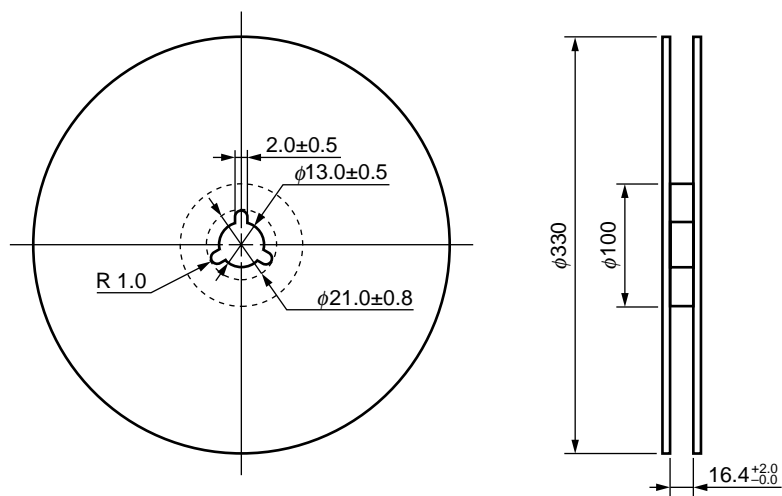
Outline and Dimensions (Tape)



Tape Direction



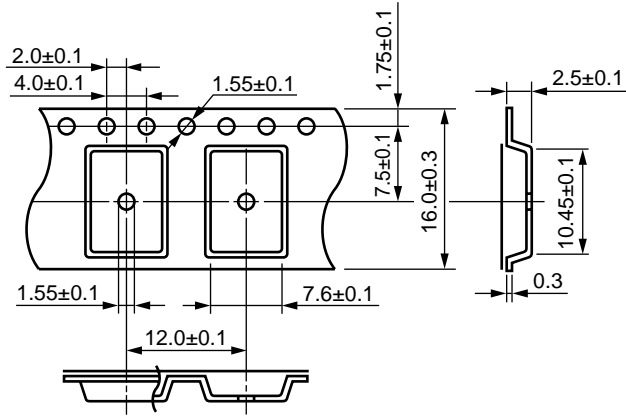
Outline and Dimensions (Reel)



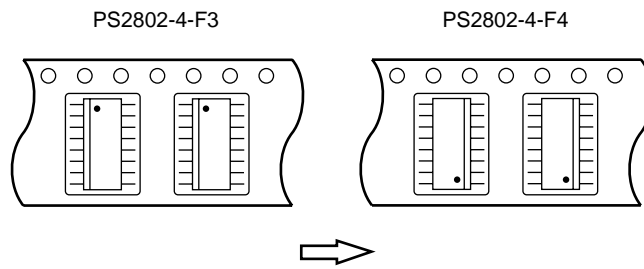
Packing: 3 500 pcs/reel

PS2802-4

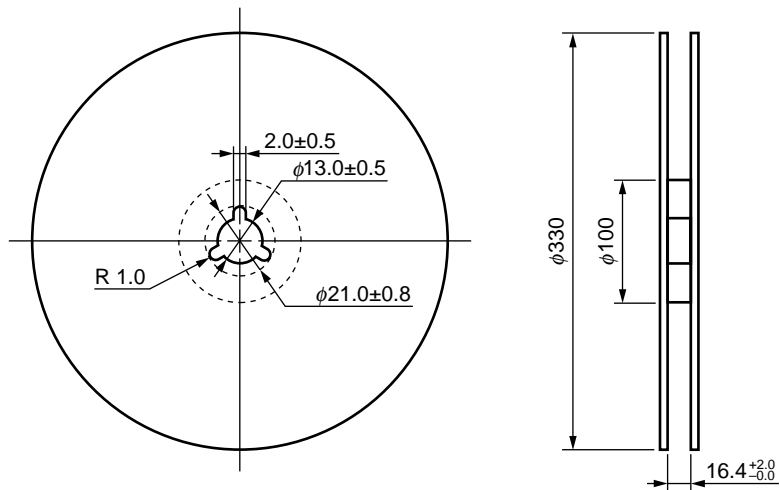
Outline and Dimensions (Tape)



Tape Direction



Outline and Dimensions (Reel)



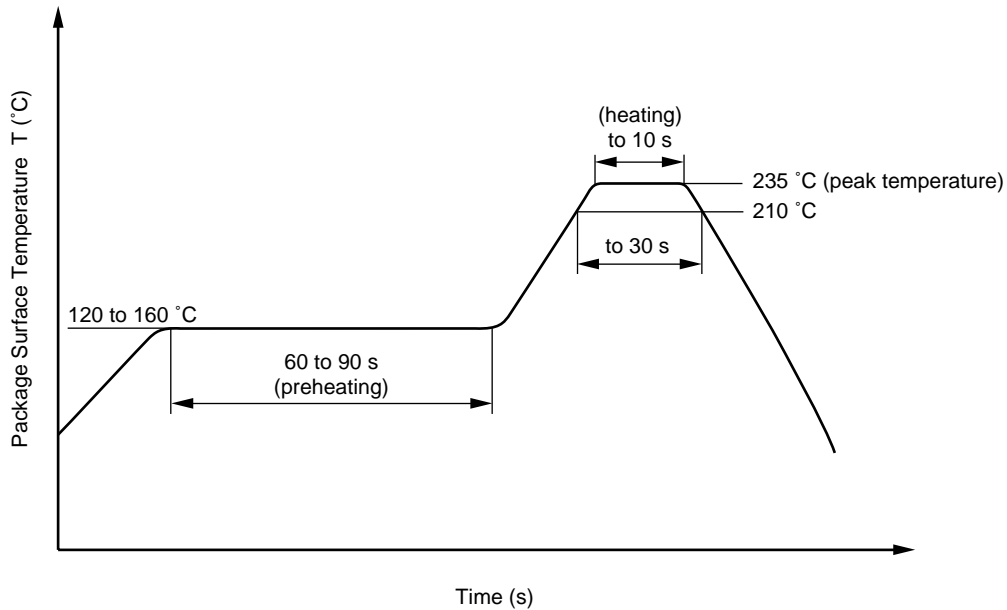
Packing: 2 500 pcs/reel

RECOMMENDED SOLDERING CONDITIONS

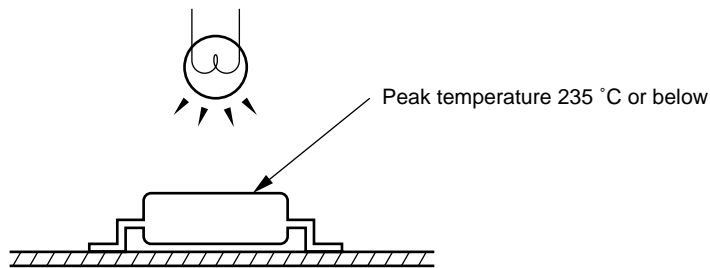
(1) Infrared reflow soldering

- Peak reflow temperature 235 °C (package surface temperature)
- Time of temperature higher than 210 °C 30 seconds or less
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

Recommended Temperature Profile of Infrared Reflow



★ **Caution** Avoid removing the residual flux with chlorine-based cleaning solvent after a reflow process.

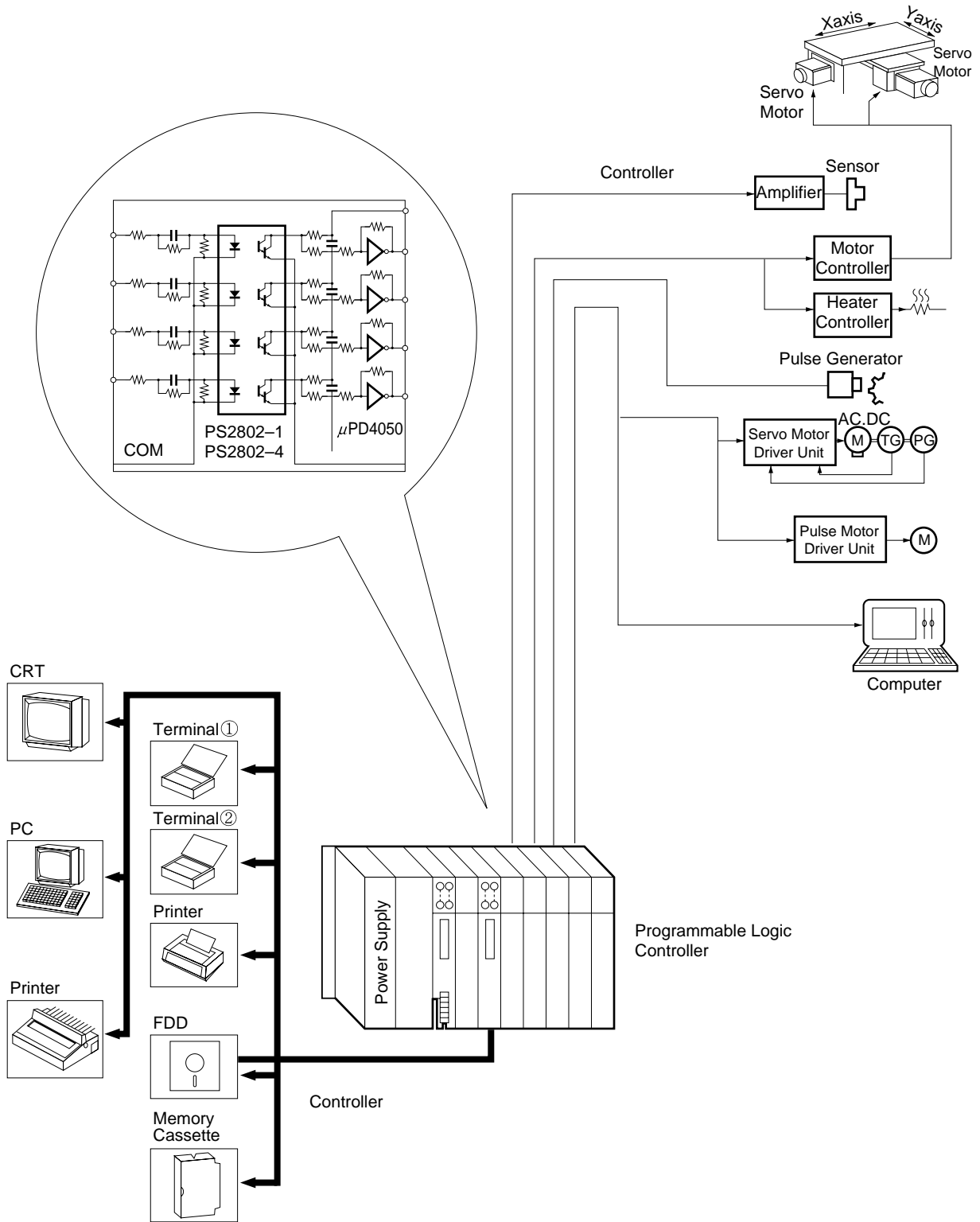


(2) Dip soldering

- Temperature 260 °C or below (molten solder temperature)
- Time 10 seconds or less
- Number of times One
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

PROGRAMMABLE LOGIC CONTROLLERS EXAMPLE

Purpose: In-out interface



CAUTION

Within this device there exists GaAs (Gallium Arsenide) material which is a harmful substance if ingested. Please do not under any circumstances break the hermetic seal.

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Anti-radioactive design is not implemented in this product.