

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA1811 is a switching device which can be driven directly by a 2.5-V power source.

The μ PA1811 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 2.5-V power source
- Low on-state resistance

$R_{DS(on)1} = 75 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -2.0 \text{ A)}$

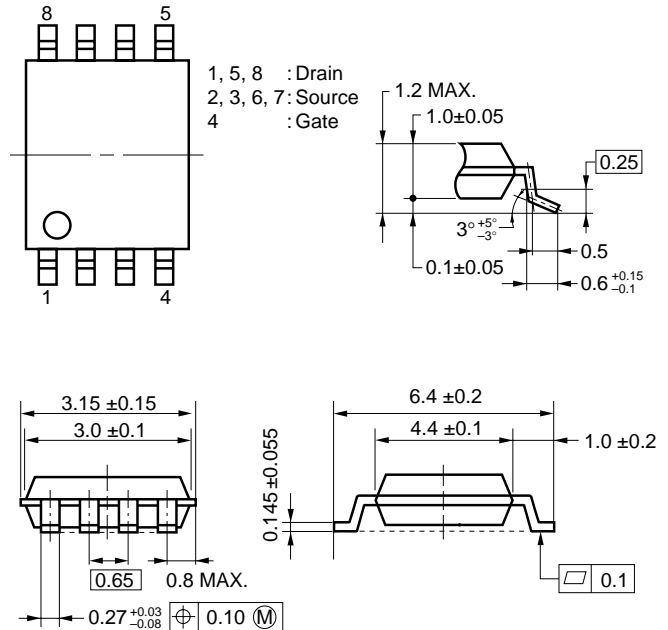
$R_{DS(on)2} = 80 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.0 \text{ V, } I_D = -2.0 \text{ A)}$

$R_{DS(on)3} = 120 \text{ m}\Omega \text{ MAX. (} V_{GS} = -2.5 \text{ V, } I_D = -2.0 \text{ A)}$

ORDERING INFORMATION

PART NUMBER	PACKAGE
μ PA1811GR-9JG	Power TSSOP8

PACKAGE DRAWING (Unit : mm)



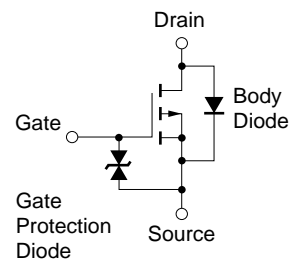
ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Drain to Source Voltage	V_{DSS}	-20	V
★ Gate to Source Voltage	V_{GSS}	-12/+6	V
Drain Current (DC)	$I_{D(DC)}$	±4.0	A
Drain Current (pulse)	$I_{D(pulse)}$	±16	A
Total Power Dissipation	P_T	2.0	W
Channel Temperature	T_{ch}	150	°C
Storage Temperature	T_{stg}	-55 to +150	°C

- Notes 1.** $PW \leq 10 \mu s$, Duty Cycle $\leq 1\%$
2. Mounted on ceramic substrate of $5000 \text{ mm}^2 \times 1.1 \text{ mm}$

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

EQUIVALENT CIRCUIT

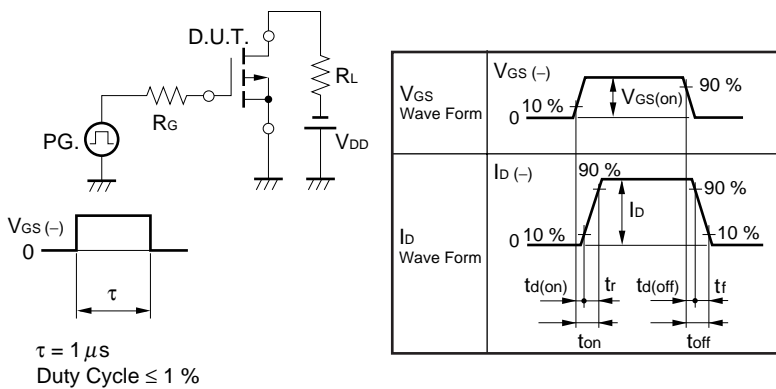


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 Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

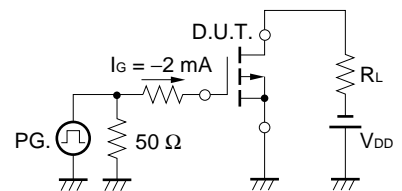
ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V			-10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±12 V, V _{DS} = 0 V			±10	μA
★ Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-0.5	-0.9	-1.5	V
★ Forward Transfer Admittance	y _{fs}	V _{DS} = -10 V, I _D = -2.0 A	2.5	6.8		S
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = -4.5 V, I _D = -2.0 A		42	75	mΩ
	R _{DS(on)2}	V _{GS} = -4.0 V, I _D = -2.0 A		46	80	mΩ
	R _{DS(on)3}	V _{GS} = -2.5 V, I _D = -2.0 A		73	120	mΩ
Input Capacitance	C _{iSS}	V _{DS} = -10 V		1160		pF
Output Capacitance	C _{oSS}	V _{GS} = 0 V		680		pF
Reverse Transfer Capacitance	C _{rSS}	f = 1 MHz		210		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = -10 V		40		ns
Rise Time	t _r	I _D = -2.0 A		100		ns
Turn-off Delay Time	t _{d(off)}	V _{GS(on)} = -4.0 V		90		ns
Fall Time	t _f	R _G = 5 Ω		60		ns
Total Gate Charge	Q _G	V _{DD} = -10 V		36		nC
Gate to Source Charge	Q _{GS}	I _D = -4.0 A		5		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = -4.0 V		16		nC
Diode Forward Voltage	V _{F(S-D)}	I _F = 4.0 A, V _{GS} = 0 V		0.74		V
Reverse Recovery Time	t _{rr}	I _F = 4.0 A, V _{GS} = 0 V		77		ns
Reverse Recovery Charge	Q _{rr}	di/dt = 100 A/μS		69		nC

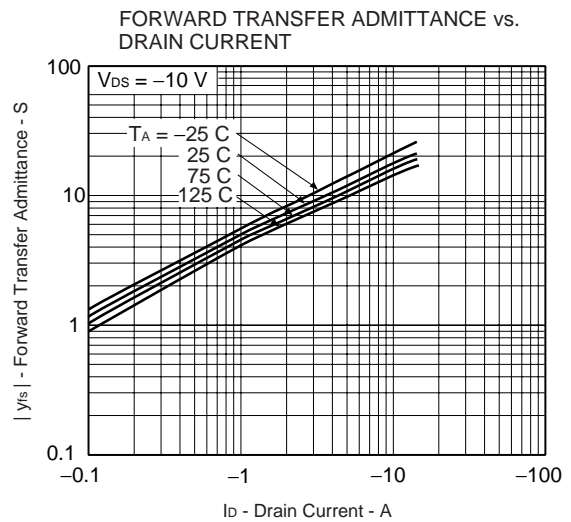
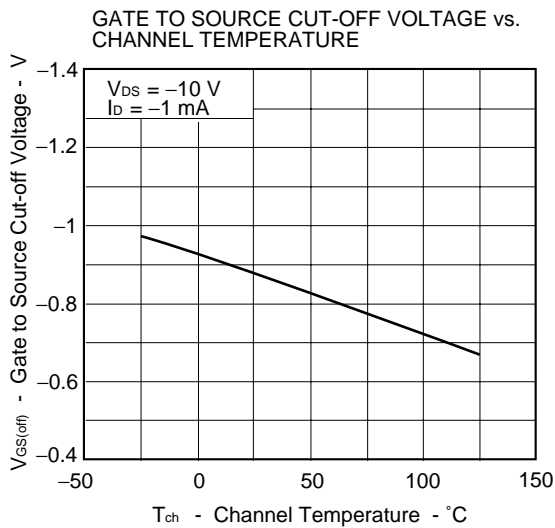
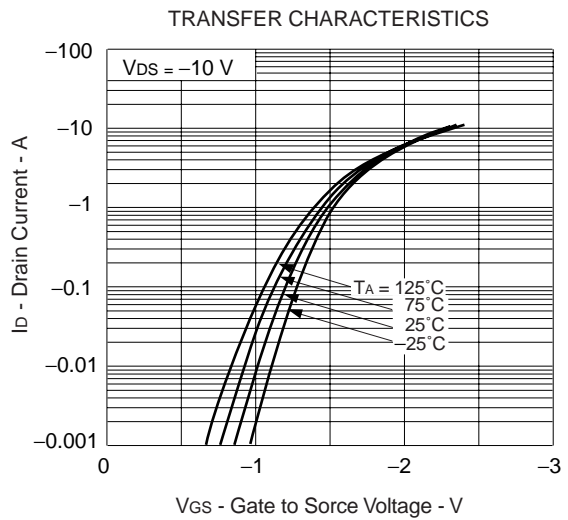
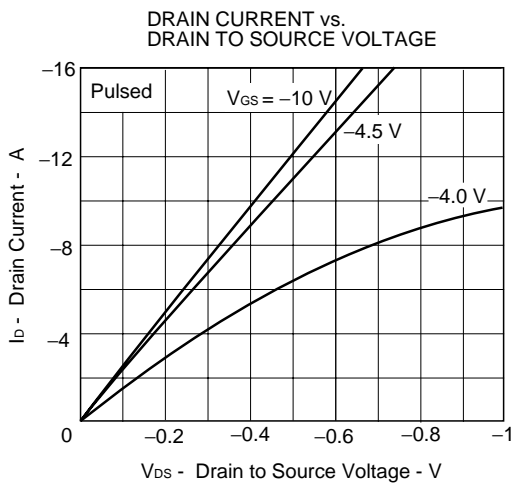
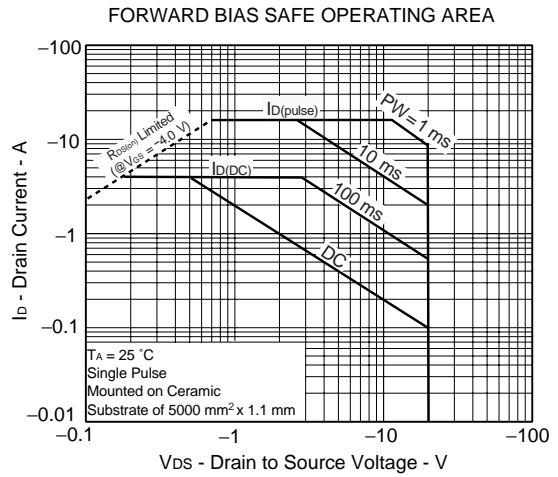
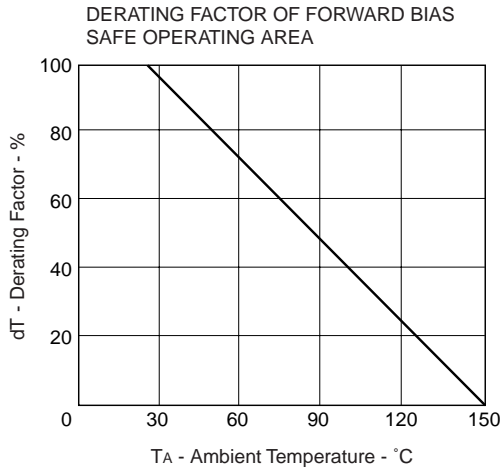
TEST CIRCUIT 1 SWITCHING TIME

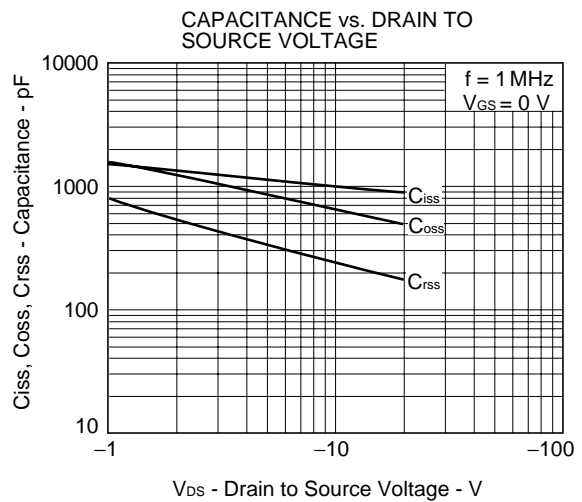
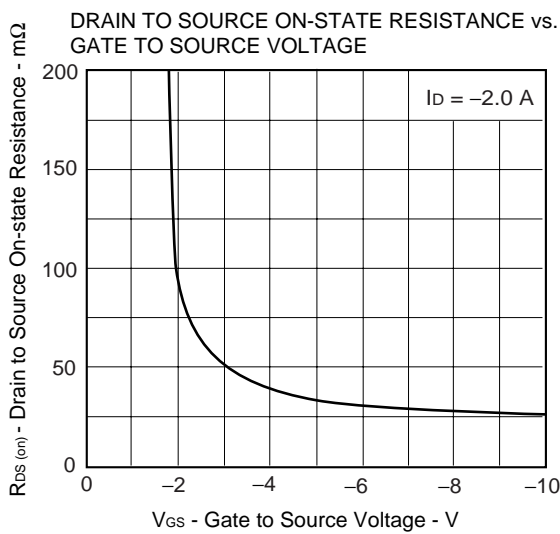
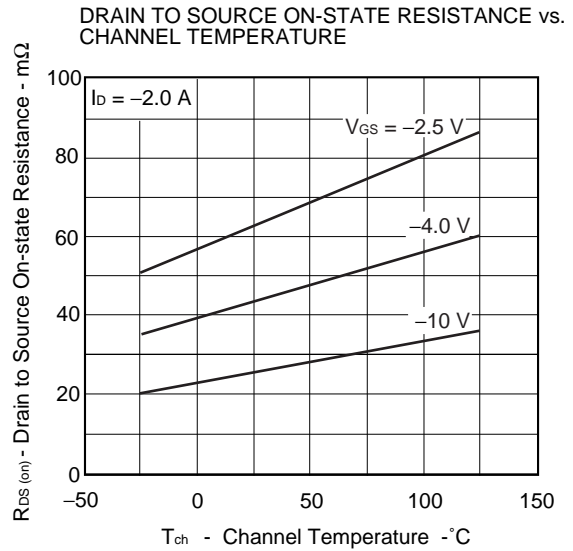
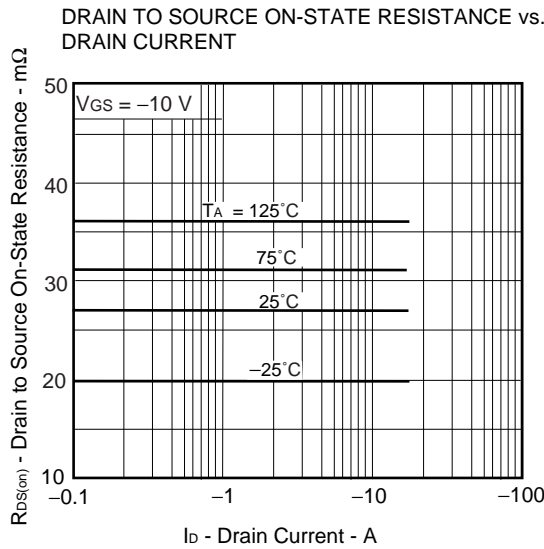
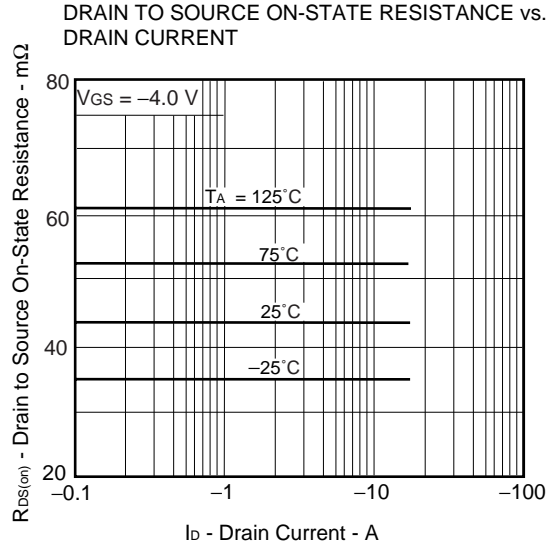
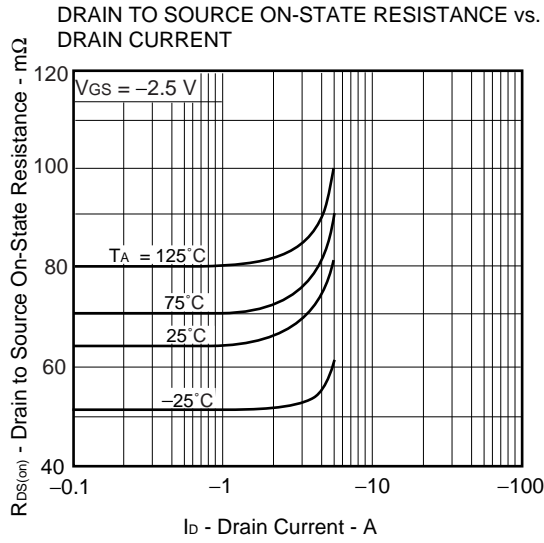


TEST CIRCUIT 2 GATE CHARGE

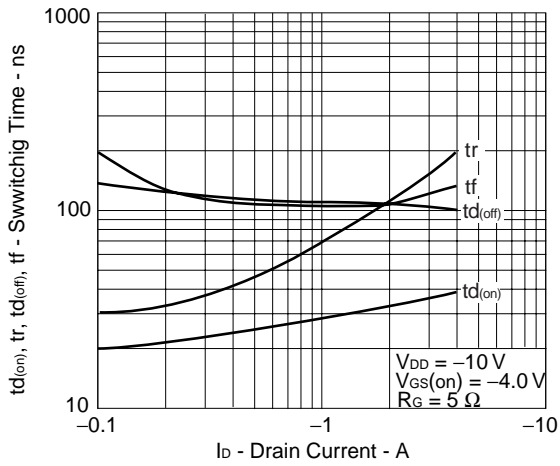


★ TYPICAL CHARACTERISTICS (T_A = 25 °C)

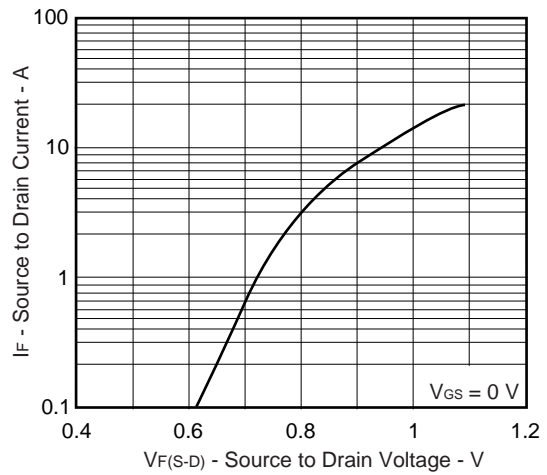




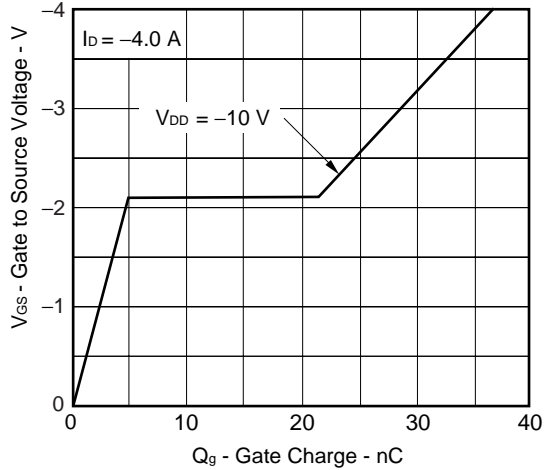
SWITCHING CHARACTERISTICS



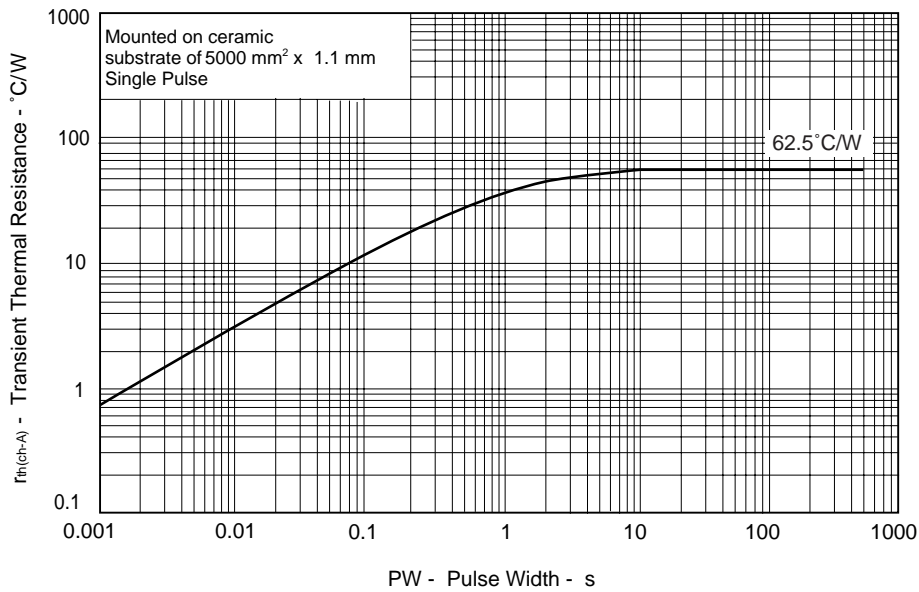
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT CHARACTERISTICS



TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



[MEMO]

[MEMO]

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