

# GaAs INTEGRATED CIRCUIT $\mu PG103B$

# WIDE-BAND AMPLIFIER

 $\mu$ PG103B is GaAs integrated circuit designed as wide band (50 MHz to 3GHz) amplifiers. This device is most suitable for the microwave communication system and the measurement equipment.

### FEATURES

- Ultra wide band : f = 50 MHz to 3 GHz
- Input/output impedance matched to 50  $\boldsymbol{\Omega}$
- Hermetic sealed ceramic package assures high reliability

#### ORDERING INFORMATION

PART NUMBER	PACKAGE		
μPG103B	T-31, 8 PIN CERAMIC		

#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 $^{\circ}$ C)

Drain Voltage	Vdd	+8	V
Gate Voltage	Vgg	-8	V
Input Voltage	Vin	-3 to +0.6	V
Input Power	Pin	+15	dBm
Total Power Dissipation*	Ptot	1.5	W
Operating Case Temperature	Topt	-65 to +125	°C
Storage Temperature	Tstg	-65 to +175	°C
* Tc ≤ 125 °C			

#### **RECOMMENDED OPERATING CONDITIONS (TA = 25 °C)**

Drain Voltage	Vdd	+5.0±0.5	V
Gate Voltage	Vgg	-5.0±0.5	V
Operating Case Temperature	Topt	-50 to +80	°C

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Current	lod	40	55	80	mA	RF OFF
Gate Current	lgg		1	2	mA	
Power Gain	Gp	12			dB	f = 0.05 to 2 GHz
Power Gain	Gp	10			dB	f = 2 to 3 GHz
Gain Flatness	⊿Gp		±1.5	±2.0	dB	f = 0.05 to 2 GHz
Gain Flatness	⊿G <sub>P</sub>		±2.0	±3.0	dB	f = 0.05 to 3 GHz
Noise Figure	NF		4.0	4.5	dB	f = 0.05 to 2 GHz
Noise Figure	NF		4.5	5.0	dB	f = 2 to 3 GHz
Input Return Loss	RLin	6	10		dB	f = 0.05 to 1 GHz
Input Return Loss	RLin	10	14		dB	f = 1 to 2 GHz
Input Return Loss	RLin	6	10		dB	f = 2 to 3 GHz
Output Return Loss	RLout	10	16		dB	
Isolation	Isol	30	40		dB	f = 0.05 to 3 GHz
Output Power at 1 dB Gain Compression Point	P₀ (1dB)	+7	+9		dBm	

## ELECTRICAL CHARACTERISTICS (TA = 25 °C, VDD = +5V, VGG = -5V)

## TYPICAL PERFORMANCE CURVES



out in

5000

0



# **TEST CIRCUIT**







# EQUIVALENT CIRCUIT

## OUTLINE DIMENSIONS (Unit : mm)



PACKAGE OUTLINE

## RECOMMENDED SOLDERING CONDITIONS

The following conditions (see table below) must be met when soldering this product.

Please consult with our sales offices in case other soldering process is used, or in case soldering is done under different conditions.

### TYPES OF SURFACE MOUNT DEVICE

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (C10535EJ7V0IF00).

 $\mu$ PG103B

Soldering process	Soldering conditions	Symbol
Infrared ray reflow	Peak package's surface temperature: 230 °C or below, Reflow time: 10 seconds or below (210 °C or higher), Number of reflow process: 1, Exposure limit <sup>*</sup> : None	
Partial heating method	Terminal temperature: 260 °C or below, Flow time: 10 seconds or below, Exposure limit <sup>*</sup> : None	

Exposure limit before soldering after dry-pack package is opened.
Storage conditions: 25 °C and relative humidity at 65 % or less.

Note Do not apply more than a single process at once, except for "Partial heating method".

**PRECAUTION** This IC must be handled with great care to prevent static discharge because its circuitry is composed of GaAs MES FET.

## Caution

The Grate Care must be taken in dealing with the devices in this guide. The reason is that the material of the devices is GaAs (Gallium Arsenide), which is designated as harmful substance according to the Japanese law concerned. Keep the Japanese law concerned and so on, especially in case of removal. [MEMO]

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Anti-radioactive design is not implemented in this product.

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