PRERIMINARY

# (•)HEADPHONE

# SRS Headphone 3D SURROUND PROCESSOR

#### ■GENERAL DESCRIPTION

The NJM2190 is a headphone surround processor based on SRS technology. It provides a realistic and spacious listening experience through standard headphones.

The features of low operating voltage, low output noise, and low operating current are very suitable for portable audio applications.

#### **■PACKAGE OUTLINE**



NJM2190D



#### **FEATURES**

Operating Voltage

●Low Operating Current

●Low Output Noise

●WIDTH Control

Bipolar Technology

Package Outline

(1.8 to 6.0V)

(1.3mA typ. at SRS mode)

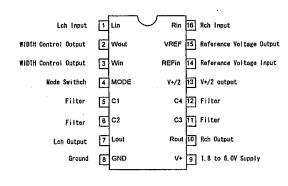
(12.0  $\mu$  Vrms typ. at SRS mode)

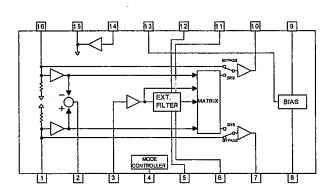


DIP16, DMP16, SSOP16

#### **■PIN CONFIGURATION**

#### **■BLOCK DIAGRAM**





SRS Headphone is a trademark of SRS Labs, Inc. SRS and the SRS symbol are registered trademarks of SRS Labs, Inc. SRS Headphone technology is incorporated under license from SRS Labs, Inc., and protected under patents either issued or pending in selected countries worldwide.

For further information, please contact:

SRS Labs, Inc.

2909 Daimler Street. Santa Ana. CA 92705 USA

Tel:714-442-1070 Fax:714-852-1099 http://www.srslabs.com

## ■ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	۸+	7		
Power Dissipation	P <sub>D</sub>	(D1P16) 500 (DMP16) 300 (SSOP16) 300	mW	
Operating Temperature Range	Topr	-20 to +75	°C	
Storage Temperature Range	Tstg	-40 to +125	°C	

# ■ ELECTRICAL CHARACTERISTICS (V+=3V, $V_{1N}$ =-26dBV(50mVrms), Ta=25°C, unless otherwise specified.)

				COND	ITION						
PARAMETER	SYMBOL		L	PUT R	OUTPUT	MODE	WIDTH VR(*1)	MIN	TYP	MAX	UNIT
Operating Voltage	V+		_	_		_	-	1.8	3. 0	6. 0	٧
Operating	I <sub>cc</sub>		0	0	_	BYPASS	_	_	0. 7	1. 0	
Current		No Signal	0	0	_	SRS	MIN	_	1. 3	1.8	mA
			0	0	-	SRS	MAX	-	1.3	1.8	
Reference Voltage	V <sub>REF</sub>	V <sup>+</sup> /2	_	_	_		_	1. 3	1.5	1. 7	٧
Maximum	V <sub>IM</sub>	f=1kHz THD=1%	V <sub>IN</sub>	0	L	DVDACC			0. 1		
Input Voltage		1110-1%	0	VIN	R	BYPASS		_	(1012)	<u></u>	
		f=100Hz THD=1% f=100Hz THD=1%	Λ <sup>IN</sup>	0	L	SRS	MIN	_	-11.8		
			0	VIN	R	SINS	MILA		(257)	_	
			VIN	0	L,	SRS	MAX	_	-15. 8	_	
			0	VIN	R	0110	IIIAA	_	(162)	-	dBV
		V+=1.8V f=1kHz	VIN	0	L	BYPASS	_	-6.7	-4. 7		(mVrms)
		THD=1%	0	۸ <sup>tы</sup>	R	Dil Acc		(462)	(582)		
		V+=1.8V f=100Hz	Λ <sup>1ν</sup>	0	L	SRS	MIN	_	-16.7	_	
		T=100H2 THD=1%	0	A <sup>IN</sup>	R	ONO	111111		(146)		
		V+=1. 8V f=100Hz	Λ <sup>IN</sup>	0	L	SRS	MAX	-22. 5	-20.5	_	
		THD=1%	0	VIN	R	0110		(75)	(94)		

# **ELECTRICAL CHARACTERISTICS** (V+=3V, V $_{\text{IN}}$ =-26dBV(50mVrms), Ta=25°C, unless otherwise specified.)

				COND	TION						
PARAMETER	SYMBOL	_	I NI L	PUT R	OUTPUT	MODE	WIDTH VR <sup>(*1)</sup>	MIN	TYP	MAX	UNIT
Maximum Input Voltage	V <sub>IM</sub>	V+=1.8V f=1kHz THD=1%	VIN	VIN	L R	SRS	MIN	_	-16. 7 (146)	-	
(*2)		V+=1. 8V f=1kHz THD=1%	VIN	۷ <sub>IN</sub>	L R	SRS	MAX	<b>1</b>	-16. 7 (146)	1	dBV
		V+=1.8V f=100Hz THD=1%	VIN	-V <sub>1N</sub>	L R	SRS	MIN	-	-22. 9 (72)	1	(mVrms)
		V+=1.8V f=100Hz THD=1%	V <sub>IN</sub>	-V <sub>1N</sub>	L R	SRS	MAX	-28. 5 (38)	-26. 5 (47)	_	
Output Noise	V <sub>NO</sub>		0	0	L R	BYPASS	_		-110 (3. 0)	-104 (6. 0)	
		Rg=0Ω A-Weighted	0	0	L R	SRS	MIN	<b>-</b>	-98 (12. 0)	_	dBV (μVrms)
			0	0	L R	SRS	MAX	_	-98 (12. 0)	-92 (24. 0)	
Total Harmonic Distortion	THD		V <sub>IN</sub>	0 V <sub>IN</sub>	L R	BYPASS	-	_	0. 02	_	
Diotor Eron		V+=1. 8V f=1kHz	V <sub>1N</sub>	0 V <sub>1N</sub>	L R	SRS	MIN		0. 10	-	%
		V <sub>IN</sub> O L SRS MAX		0. 25	0. 5						
BYPASS Gain	G <sub>VBYP</sub>	f=1kHz	V <sub>IN</sub>	0 V <sub>IN</sub>	L R	BYPASS	-	-1.0	0. 0	1.0	dB
L+R Gain	G <sub>L+R</sub>		VIN	V <sub>IN</sub>	L R	SRS	MIN	_	0. 0	-	
		f=1kHz	۸ <sup>IN</sup>	V <sub>IN</sub>	L R	SRS	MAX	-1.0	0.0	1.0	dB

 $\blacksquare ELECTRICAL \ \ CHARACTERISTICS \ \ (V^+=3V,V_{IN}=-26 dBV (50 mVrms), Ta=25 ^{\circ}C, \ unless \ otherwise \ specified.)$ 

				COND	ITION						
PARAMETER S	SYMBOL		I NI L	PUT R	OUTPUT	MODE	WIDTH VR(*1)	MIN	ТҮР	MAX	UNIT
L—R Gain (*2)	G <sub>L-R</sub>	5 10011-	٨٢٧	-V <sub>1N</sub>	L R	SRS	MIN	3. 7	5. 7	7. <b>7</b>	.ID
		f=100Hz		-V <sub>IN</sub>	L R	SRS	MAX	19. 3	21.3	23. 3	dB
Channel Separation	CS	f=1kHz	0 V <sub>IN</sub>	V <sub>1N</sub>	L R	BYPASS	-	60. 0	80. 0	<u></u>	dB
MODE Select Control Voltage	V <sub>IH</sub>	High Level	-	_	-	_	_	1.3	-	۷+	V
	V <sub>1L</sub>	Low Level	-	-	-	- ,	-	0. 0	-	0. 5	V .

- (\*1) Refer to application circuit 1.
- (\*2) The word '-V  $_{\rm I\,N}$ ' signifies opposite phase of 'V  $_{\rm I\,N}$ '.

#### ■MODE Switch

	MODE
BYPASS MODE	L
SRS MODE	Н

### **■ TERMINAL DESCRIPTION**

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
1 16	Lin Rin	Audio Input	150k S 50k VREF	V <sup>+</sup> /2
2	Wout	WIDTH Control Output	20k	V <sup>+</sup> /2
3	Win	WIDTH Control Input	150	V+/2
4	MODE	Mode Switch	150 150k	_

### **TERMINAL DESCRIPTION**

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
5	<b>C</b> 1	Capacitor Terminal 1 for Filter		V+/2
6	C2 .	Capacitor Terminal 2 for Filter	33%	V+/2
7 10	Lout Rout	Audio Output	10%	V+/2
8	GND	Ground		ov

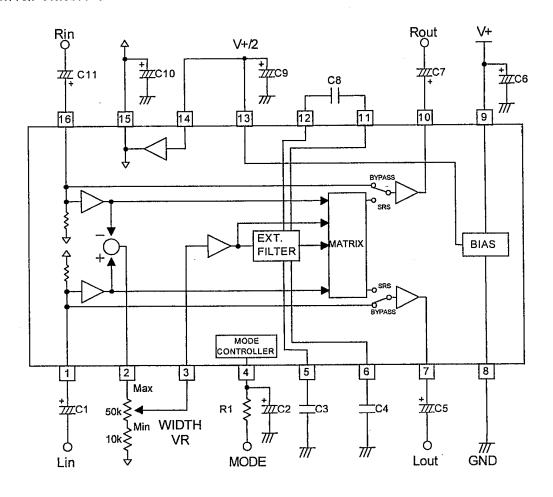
#### **MITERMINAL DESCRIPTION**

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
9	V+	Power Supply		<b>V</b> +
11	С3	Capacitor Terminal 3 for Filter	10k 10k 10k 200 W	V+/2
12	C4	Capacitor Terminal 4 for Filter		V+/2
13	V+/2	V+/2 Output	80k 80k	V <sup>+</sup> /2

### **TERMINAL DESCRIPTION**

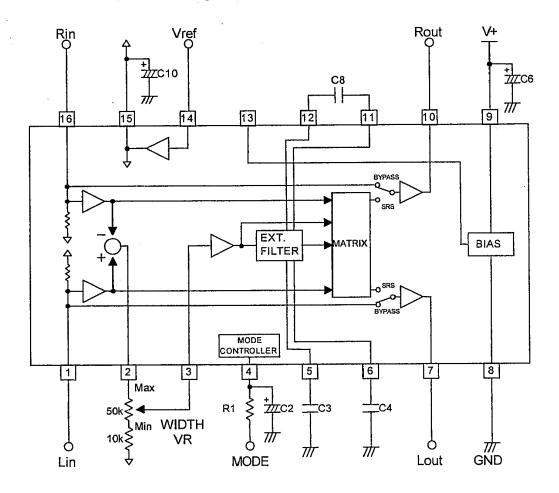
PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	VOLTAGE
14	REFin	Reference Voltage Input	150	V+/2
15	VREF	Reference Voltage Output	V+ V+ V+ 400 400 400 400 400 400 400 400 400 40	V+/2

### ■APPLICATION CIRCUIT 1



Parts No.	Value	Tolerance	Parts No.	Value	Tolerance
R1	22k Ω	±5%	C6	22 to 100 μ F	
C1	10 μ F	-	<b>C7</b>	10 μ F	-
C2	10 μ F	_	C8	4. 7nF	±5%
C3	3. 3nF	±5%	C9	1 to 10μF	
C4	0.1μF	±5%	C10	10 to 7μF	-
C5	10 μ F	_	C11	10 μ F	·

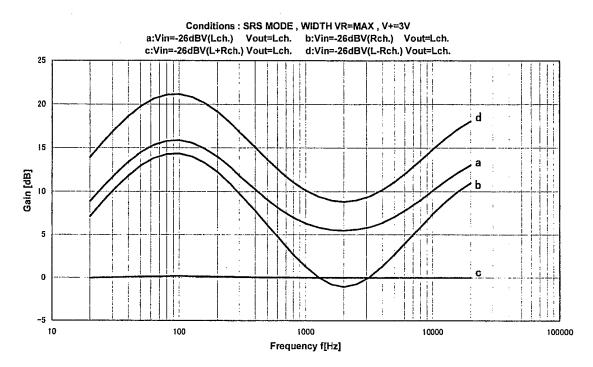
## ■APPLICATION CIRCUIT 2 (Without using internal V+/2.)



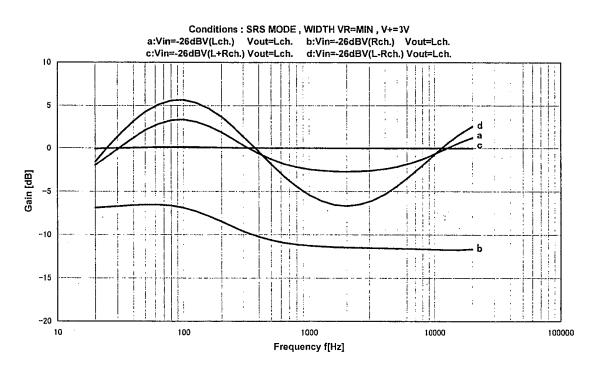
Parts No.	Value	Tolerance	Parts No.	Value	Tolerance
R1	22k Ω	±5%	C6	22 to 100 μ F	-
C2	10 μ F	_	C8	4. 7nF	±5%
C3	3. 3nF	±5%	C10	10 to 47 μF	-
C4	0.1μF	±5%			

#### **TYPICAL CHARACTERISTICS**

#### GAIN STURUCTURE (WIDTH VR: MAX)

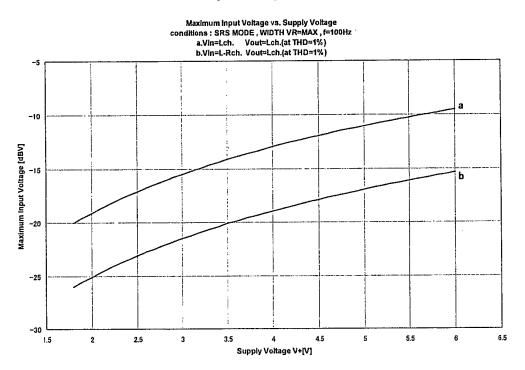


#### GAIN STRUCTURE (WIDTH VR:MIN)



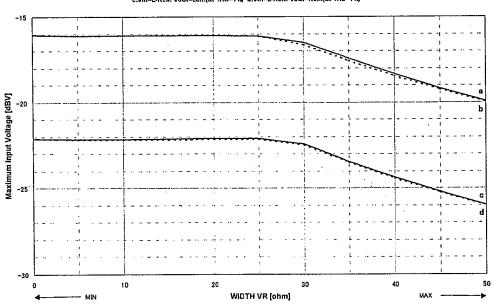
#### **TYPICAL CHARACTERISTICS**

#### Maximum Input Voltage vs. Supply Voltage



#### Maximum Input Voltage vs. WIDTH VR

Conditions: SRS MODE, V+=1.8V, f=100Hz
a:Vin=Lch. Vout=Lch.(at THD=1%) b:Vin=Lch. Vout=Rch.(at THD=1%) c:Vin=L-Rch. Vout=Rch.(at THD=1%) d:Vin=L-Rch. Vout=Rch.(at THD=1%)



# **MEMO**

[CAUTION]
The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.