

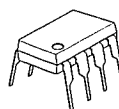
DUAL J-FET INPUT OPERATIONAL AMPLIFIER

GENERAL DESCRIPTION

These devices are low cost, high speed, dual JFET input operational amplifiers with an internally trimmed input offset voltage. They require low supply current yet maintain a large gain bandwidth product and fast slew rate. In addition, well matched high voltage JFET input devices provide very low input bias and offset currents.

These amplifiers may be used in applications such as high speed integrators, fast D/A converters, sample and hold circuits and many other circuits requiring low input offset voltage, low input bias current, high input impedance, high slew rate and wide bandwidth. The devices also exhibit low noise and offset voltage drift.

PACKAGE OUTLINE



NJM353D

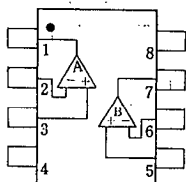


NJM353M

FEATURES

- Operating Voltage (±5V ~ ±18V)
- J-FET Input
- Low Input Bias Current (50pA typ.)
- High Slew Rate (13V/μs typ.)
- Wide Unity Gain Bandwidth (4MHz typ.)
- Package Outline DIP8, DMP8
- Bipolar Technology

PIN CONFIGURATION

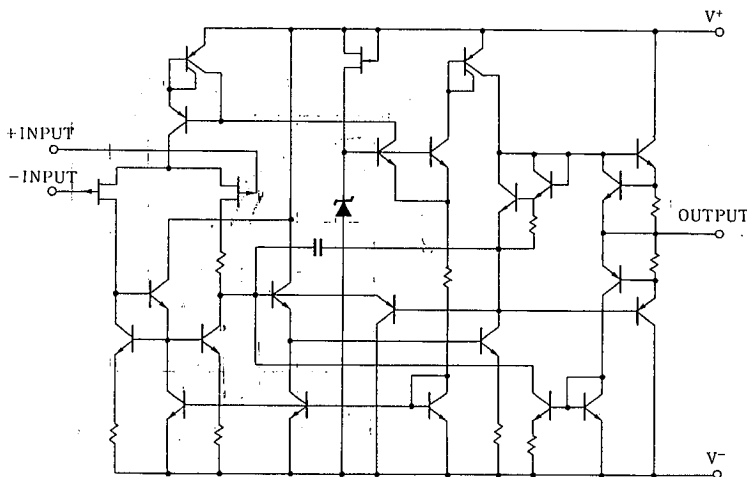


NJM353D
NJM353M

PIN FUNCTION

1. A OUTPUT
2. A-INPUT
3. A+INPUT
4. V-
5. B+INPUT
6. B-INPUT
7. B OUTPUT
8. V+

EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ /V ⁻	±18	V
Differential Input Voltage	V _{ID}	±30	V
Input Voltage	V _{IC}	±15	V
Power Dissipation	P _D	(DIP8) 500	mW
		(DMP8) 300	mW
Operating Temperature Range	T _{opr}	-40 ~ +85	°C
Storage Temperature Range	T _{stg}	-40 ~ +125	°C

(note) For supply voltage less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

■ ELECTRICAL CHARACTERISTICS

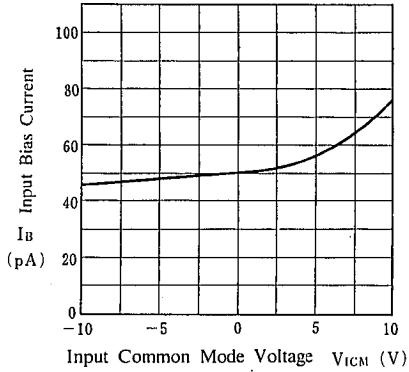
(Ta=25°C, V⁺/V⁻=±15V)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	R _S =10kΩ	—	5	10	mV
Average TC of Input Offset Voltage	ΔV _{IO} /ΔT	R _S =10kΩ	—	10	—	μV/°C
Input Offset Current	I _{IO}		—	25	100	pA
Input Bias Current	I _B		—	50	200	pA
Input Resistance	R _{IN}		—	10 ¹²	—	Ω
Large-signal Voltage Gain	A _V	R _L =2kΩ, V _O =±10V	88	100	—	dB
Maximum Peak-to-peak Output Voltage Swing	V _{OM}	R _L =10kΩ	±12	±13.5	—	V
Input Common Mode Voltage Range	V _{ICM}		±11	+15, -12	—	V
Common Mode Rejection Ratio	CMR	R _S ≤10kΩ	70	100	—	dB
Supply Voltage Rejection Ratio	SVR		70	100	—	dB
Operating Current	I _{CC}		—	3.6	6.5	mA
Channel Separate	CS	f=1Hz~20kHz	—	120	—	dB
Slew Rate	SR		—	13	—	V/μs
Unity Gain Bandwidth	f _T		—	4	—	MHz
Equivalent Input Noise Voltage	e _n	R _S =100Ω, f=1kHz	—	16	—	nV/√Hz
Equivalent Input Noise Current	i _n	f=1kHz	—	0.01	—	pA/√Hz

■ TYPICAL CHARACTERISTICS

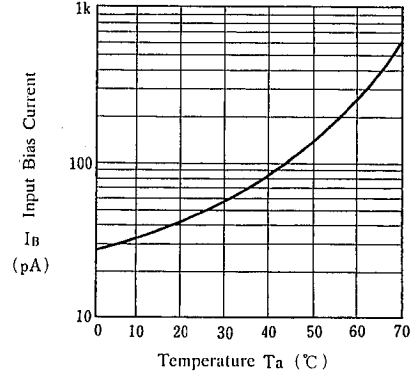
Input Bias Current

($V^+/V^- = \pm 15V$, $T_a = 25^\circ C$)



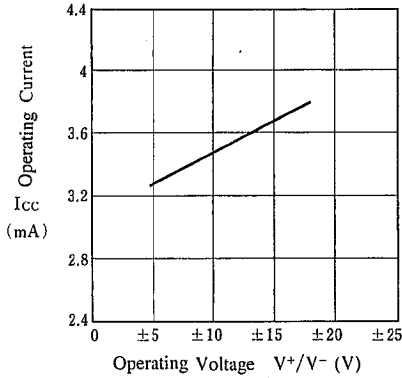
Input Bias Current

($V_{ICM} = 0$, $V^+/V^- = \pm 15V$)



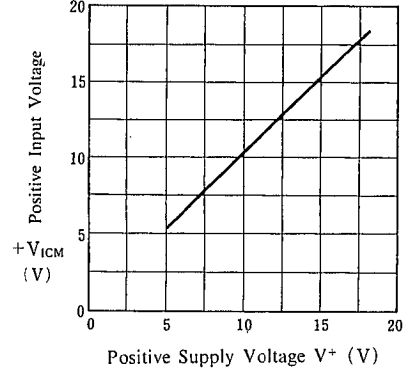
Operating Current vs. Operating Voltage

($T_a = 25^\circ C$)



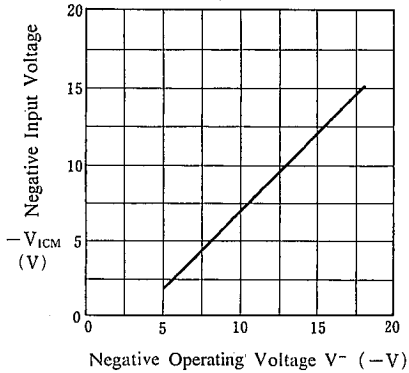
Positive Input Voltage

($T_a = 25^\circ C$)

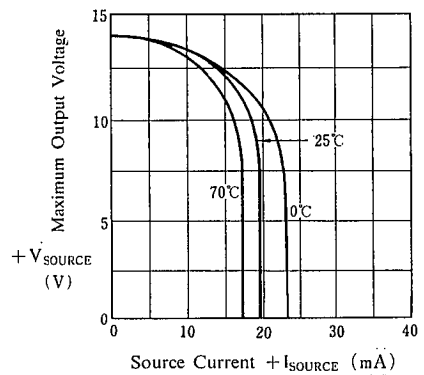


Negative Input Voltage

($T_a = 25^\circ C$)

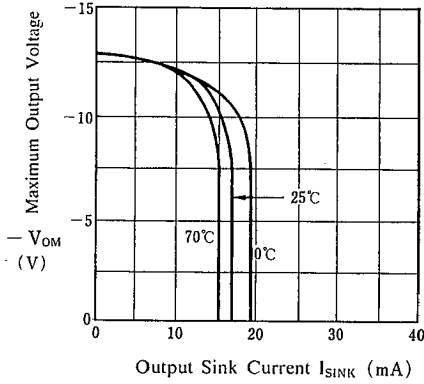


Positive Current Limit



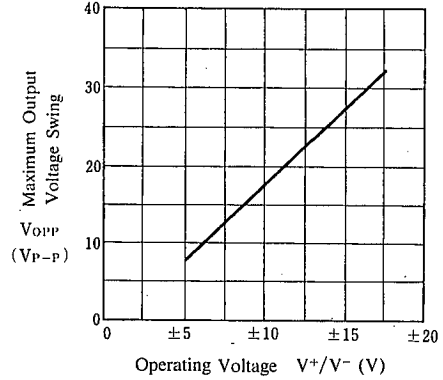
TYPICAL CHARACTERISTICS

Negative Current Limit



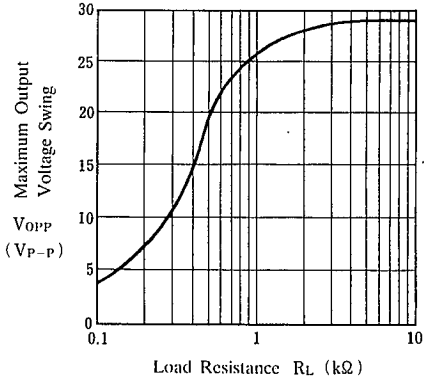
Maximum Output Voltage Swing vs. Operating Voltage

($R_L=2k\Omega$, $T_a=25^\circ C$)



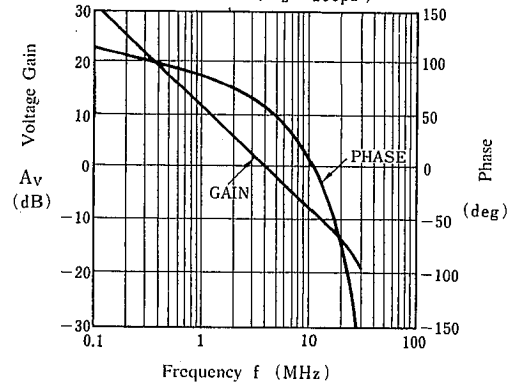
Maximum Output Voltage Swing vs. Load Resistance

($V^+/V^- = \pm 15V$, $T_a=25^\circ C$)



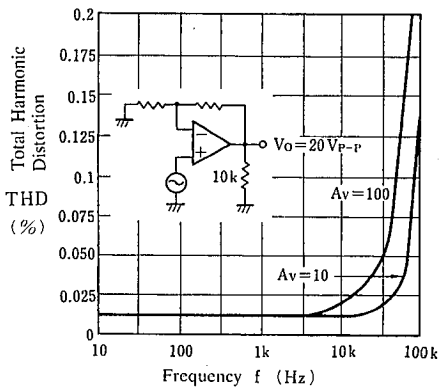
Voltage Gain, Phase vs. Frequency

($V^+/V^- = \pm 15V$, $R_L=2k\Omega$, $C_L=100pF$)



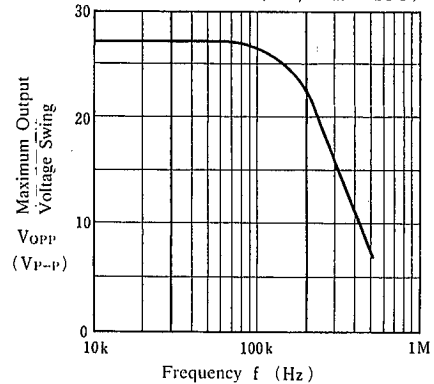
Total Harmonic Distortion vs. Frequency

($V^+/V^- = \pm 15V$, $T_a=25^\circ C$)

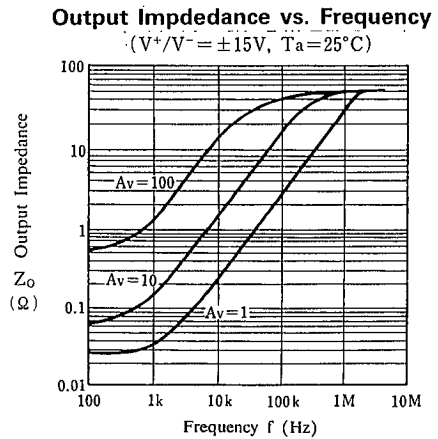
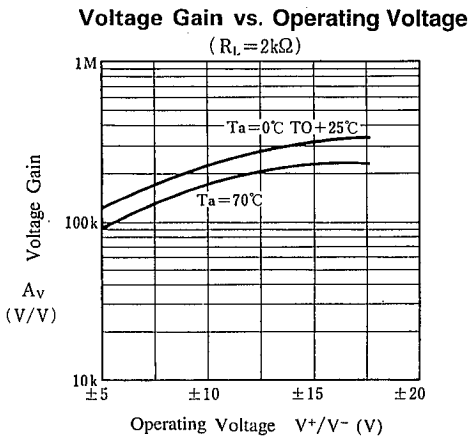
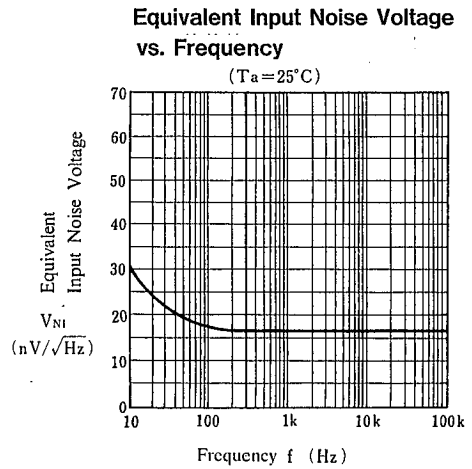
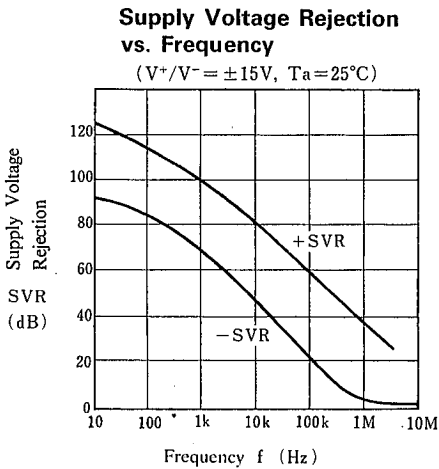
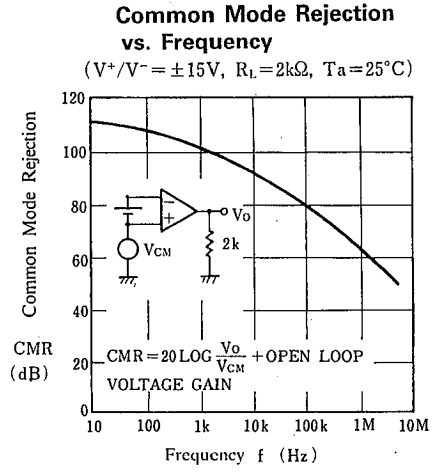
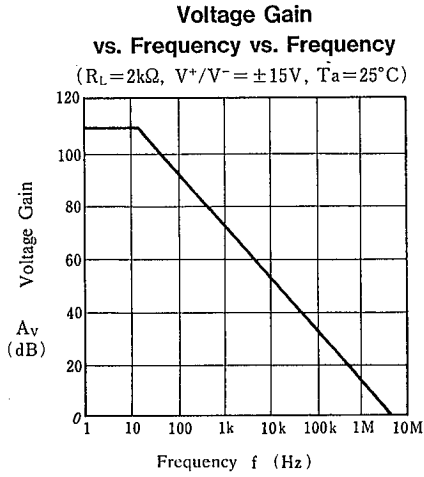


Maximum Output Voltage Swing vs. Frequency

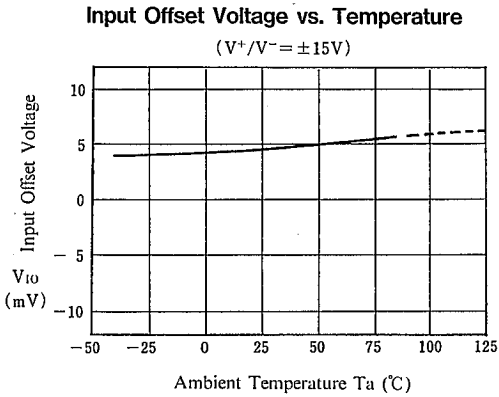
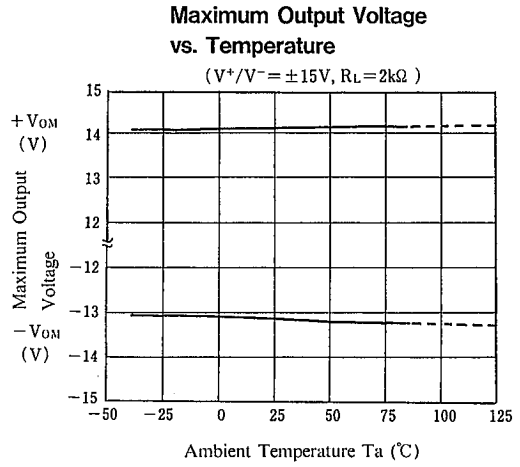
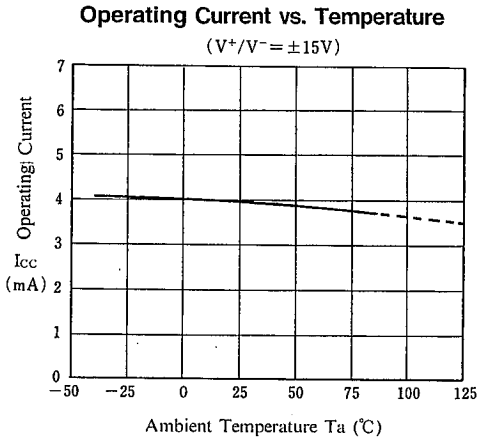
($V^+/V^- = \pm 15V$, $R_L=2k\Omega$, $T_a=25^\circ C$, $A_v=1$, $< 1\% \text{ DIST}$)



■ TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



MEMO

[CAUTION]

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