

LOW VOLTAGE C-MOS OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJU7021, 22 and 24 are single, dual and quad C-MOS Operational Amplifiers operated on a single-power-supply, low voltage and low operating current.

The minimum operating voltage is 3V and the output stage permits output signals to swing between both of the supply rails.

The input bias current is as low as less than 1pA, consequently the very small signal around the ground level can be amplified.

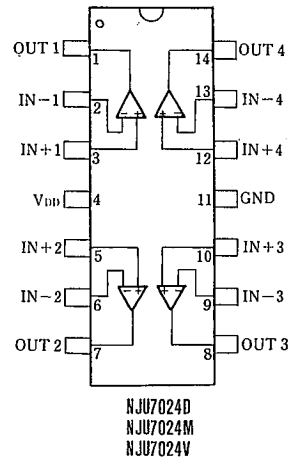
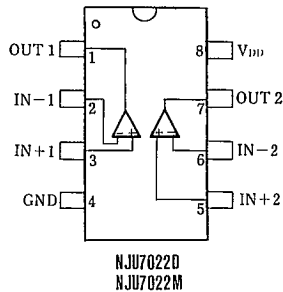
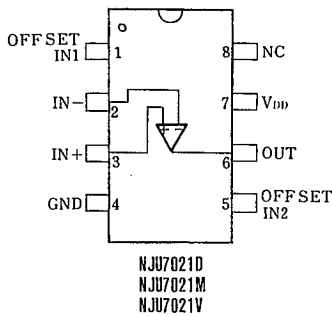
Furthermore, the operating current is also as low as 150 μ A(typ) per circuit, therefore it can be applied especially to battery operated items.

■ FEATURES

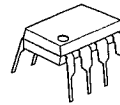
- Single-Power-Supply
- Wide Operating Voltage ($V_{DD}=3\sim 16V$)
- Wide Output Swing Range ($V_{OM}=9.98V$ typ. at $V_{DD}=10V$)
- Low Operating Current (150 μ A/circuit)
- Low Bias Current ($I_B=1pA$)
- Internal Compensation Capacitor
- External Offset Null Adjustment (Only NJU7021)
- Package Outline

DIP/DMP/SSOP 8 (NJU7021)
DIP/DMP 8 (NJU7022)
DIP/DMP/SSOP 14 (NJU7024)
- C-MOS Technology

■ PIN CONFIGURATION



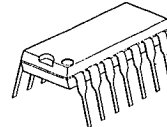
■ PACKAGE OUTLINE



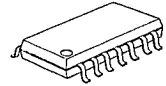
NJU7021D
NJU7022D



NJU7021M
NJU7022M



NJU7024D



NJU7024M

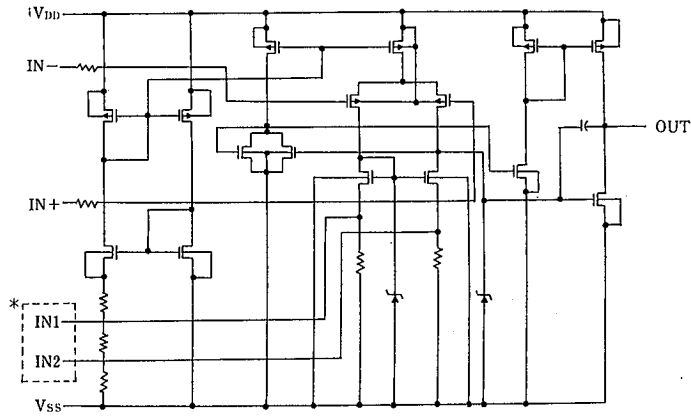


NJU7021V



NJU7024V

■ EQUIVALENT CIRCUIT



* IN1, IN2 are only for NJU7021(NJU7022/24 don't have these terminals).

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{DD}	18	V
Differential Input Voltage	V _{ID}	±18 *1	V
Common Mode Input Voltage	V _{IC}	-0.3~18	V
Power Dissipation	P _D	(DIP14) 700 (DIP8) 500 (DMP8,14) 300 (SSOP8,14) 300	mW
Operating Temperature	T _{opr}	-20~+75	°C
Storage Temperature	T _{stg}	-40~+125	°C

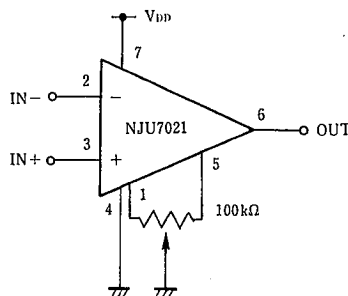
* 1) If the supply voltage (V_{DD}) is less than 18V, the input voltage must not over the V_{DD} level though 18V is limit specified.

■ ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{DD}=10V, R_L=∞)

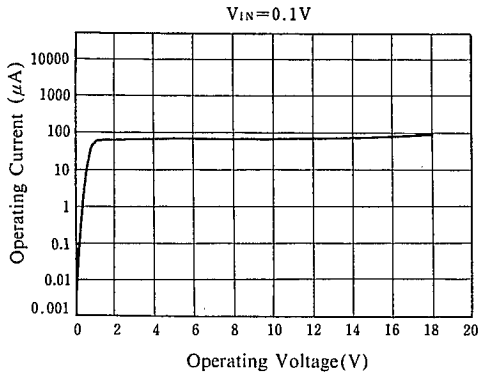
PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	R _S =50Ω			10	mV
Input Offset Current	I _{IO}			1		pA
Input Bias Current	I _{IB}			1		pA
Input Impedance	R _{IN}			1		TΩ
Large Signal Voltage Gain	A _v		80	95		dB
Input Common Mode Voltage Range	V _{ICM}		0~9			V
Maximum Output Swing Voltage	V _{OM}	R _L =1MΩ	9.80	9.98		V
Common Mode Rejection Ratio	CMR		60	75		dB
Supply Voltage Rejection Ratio	SVR		60	75		dB
Operating Current / Circuit	I _{DD}			150	300	μA
Slew Rate	SR			0.40		V/μs
Unity Gain Bandwidth	F _t	A _v =40dB C _L =10pF		0.4		MHz

■ OFFSET ADJUSTMENT CIRCUIT (ONLY FOR NJU7021)

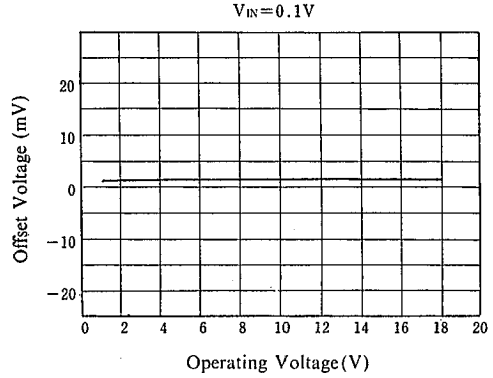


■ TYPICAL CHARACTERISTICS

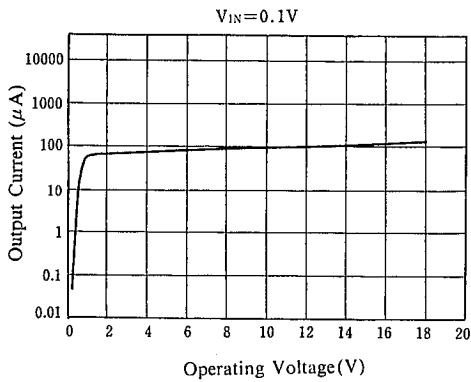
Operating Current vs. Operating Voltage



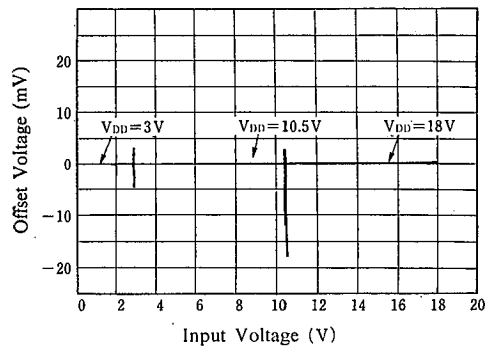
Offset Voltage vs. Operating Voltage



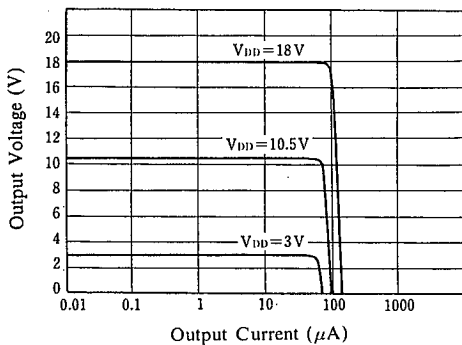
Output Current vs. Operating Voltage



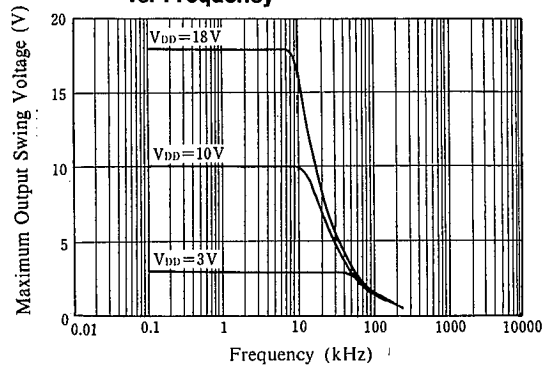
Offset Voltage vs. Input Voltage



Output Voltage vs. Output Current

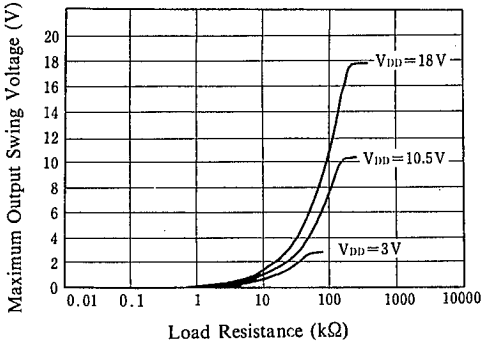


Maximum Output Swing Voltage vs. Frequency

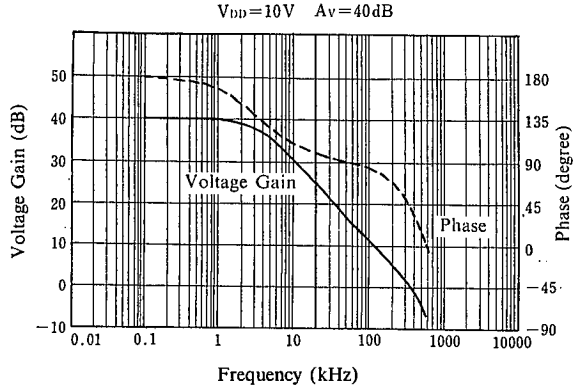


■ TYPICAL CHARACTERISTICS

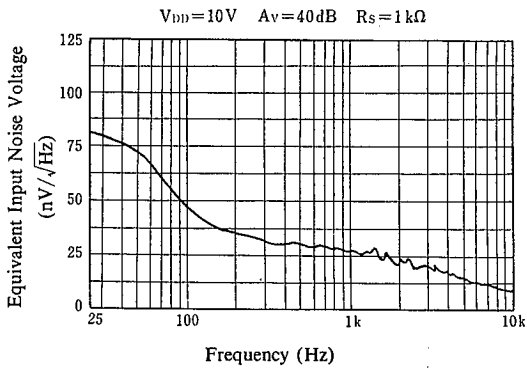
Maximum Output Swing Voltage vs. Frequency



Voltage Gain · Phase vs. Frequency



Equivalent Input Noise Voltage vs. Frequency



MEMO

[CAUTION]

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