54F/74F158A Quad 2-Input Multiplexer

General Description

The 'F158A is a high speed quad 2-input multiplexer. It selects four bits of data from two sources using the common Select and Enable inputs. The four outputs present the selected data in the inverted form. The 'F158A can also generate any four of the 16 different functions of two variables.

Features

■ Guaranteed 4000V minimum ESD protection

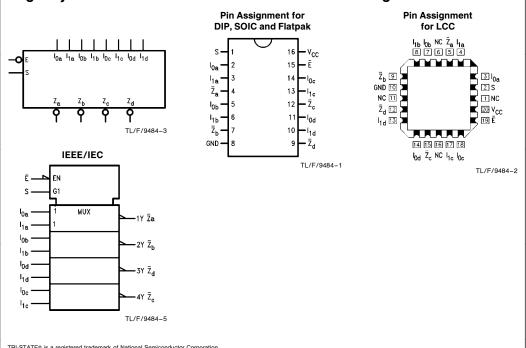
Commercial	Military	Package Number	Package Description
74F158APC		N16E	16-Lead (0.300" Wide) Molded Dual-In-Line
	54F158ADM (Note 2)	J16A	16-Lead Ceramic Dual-In-Line
74F158ASC (Note 1)		M16A	16-Lead (0.150" Wide) Molded Small Outline, JEDEC
74F158ASJ (Note 1)		M16D	16-Lead (0.300" Wide) Molded Small Outline, EIAJ
	54F158AFM (Note 2)	W16A	16-Lead Cerpack
	54F158ALM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

Logic Symbols

Connection Diagrams



Unit Loading/Fan Out

		54F/74F				
Pin Names	Pin Names Description		Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}			
I _{0a} -I _{0d}	Source 0 Data Inputs	1.0/1.0	20 μA/-0.6 mA			
I _{1a} -I _{1d}	Source 1 Data Inputs	1.0/1.0	20 μA/-0.6 mA			
Ē	Enable Input (Active LOW)	1.0/1.0	20 μA/-0.6 mA			
S	Select Input	1.0/1.0	20 μA/-0.6 mA			
$\overline{Z}_a - \overline{Z}_d$	Inverted Outputs	50/33.3	-1 mA/20 mA			

Functional Description

The 'F158A quad 2-input multiplexer selects four bits of data from two sources under the control of a common Select input (S) and presents the data in inverted form at the four outputs. The Enable input (\overline{E}) is active LOW. When \overline{E} is HIGH, all of the outputs $(\overline{\mathbf{Z}})$ are forced HIGH regardless of all other inputs. The 'F158A is the logic implementation of a 4-pole, 2-position switch where the position of the switch is determined by the logic levels supplied to the Select input.

A common use of the 'F158A is the moving of data from two groups of registers to four common output busses. The particular register from which the data comes is determined by the state of the Select input. A less obvious use is as a function generator. The 'F158A can generate four functions of two variables with one variable common. This is useful for implementing gating functions.

Truth Table

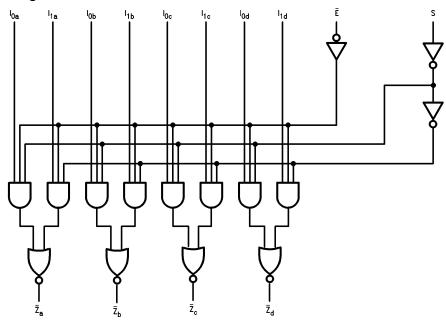
	Inp	Outputs		
Ē	S	I ₀	I ₁	Z
Н	Х	Х	Х	Н
L	L	L	Х	Н
L	L	Н	Х	L
L	Н	X	L	Н
L	Н	X	Н	L

H = HIGH Voltage Level

L = LOW Voltage Level X = Immaterial

 $\overline{Z}_n = \overline{E} \times (I_{1n} S + I_{0n} \overline{S})$

Logic Diagram



TL/F/9484-4

Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 $\begin{array}{lll} \mbox{Storage Temperature} & -65^{\circ}\mbox{C to} + 150^{\circ}\mbox{C} \\ \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to} + 125^{\circ}\mbox{C} \\ \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to} + 175^{\circ}\mbox{C} \\ \mbox{Plastic} & -55^{\circ}\mbox{C to} + 150^{\circ}\mbox{C} \\ \end{array}$

V_{CC} Pin Potential to

Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output

in HIGH State (with $V_{CC} = 0V$)

 $\begin{array}{lll} \text{Standard Output} & -0.5 \text{V to V}_{\text{CC}} \\ \text{TRI-STATE} \tiny{\,}^{\,}\text{Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$

Current Applied to Output in LOW State (Max)

twice the rated I_{OL} (mA)

4000V

ESD Last Passing Voltage (Min)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Recommended Operating Conditions

Free Air Ambient Temperature

Military $-55^{\circ}\text{C to} + 125^{\circ}\text{C}$ Commercial $0^{\circ}\text{C to} + 70^{\circ}\text{C}$

Supply Voltage

Military + 4.5V to + 5.5V Commercial + 4.5V to + 5.5V

DC Electrical Characteristics

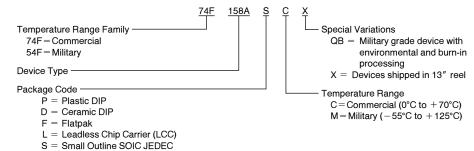
Symbol	Parameter .		54F/74F			Units	V	Conditions	
Symbol			Min	Тур	Max	Oilles	V _{CC}	Conditions	
V _{IH}	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal		
V_{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal	
V _{CD}	Input Clamp Diode Vo	oltage			-1.2	V	Min	$I_{\text{IN}} = -18 \text{mA}$	
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			٧	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	٧	Min	I _{OL} = 20 mA I _{OL} = 20 mA	
I _{IH}	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V _{IN} = 2.7V	
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	V _{IN} = 7.0V	
I _{CEX}	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$	
V _{ID}	Input Leakage Test	74F	4.75			٧	0.0	$I_{\text{ID}} = 1.9 \ \mu\text{A}$ All Other Pins Grounded	
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All Other Pins Grounded	
I _{IL}	Input LOW Current				-0.6	mA	Max	V _{IN} = 0.5V	
I _{OS}	Output Short-Circuit Current		-60		-150	mA	Max	V _{OUT} = 0V	
Іссн	Power Supply Current			10	15	mA	Max	V _O = HIGH	
I _{CCL}	Power Supply Current			15	25	mA	Max	V _O = LOW	

AC Electrical Characteristics

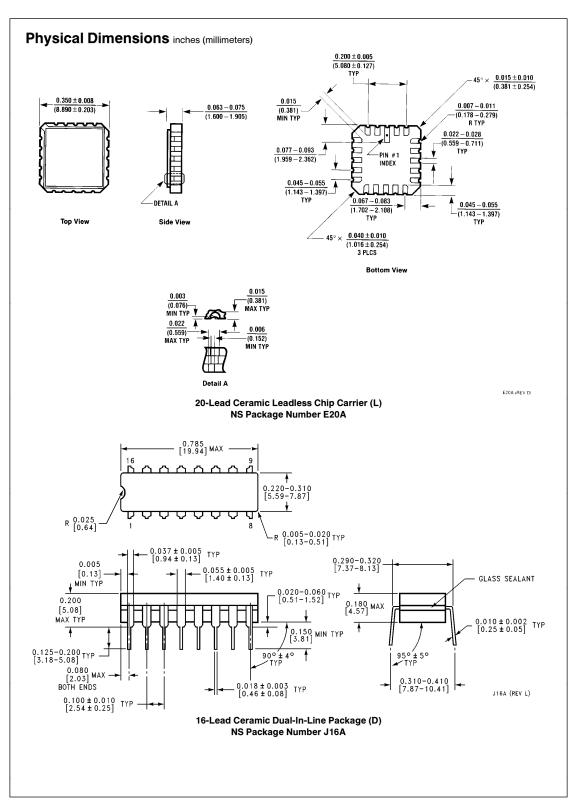
		$74F$ $T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$			54F T _A , V _{CC} = Mil C _L = 50 pF		74F T _A , V _{CC} = Com C _L = 50 pF		Units
Symbol	Parameter								
		Min	Тур	Max	Min	Max	Min	Max	<u> </u>
t _{PLH} t _{PHL}	Propagation Delay S to \overline{Z}_n	3.0 2.5	5.5 4.5	8.5 6.5	3.0 2.5	10.5 8.0	3.0 2.5	9.5 7.0	ns
t _{PLH} t _{PHL}	Propagation Delay \overline{E} to \overline{Z}_n	2.5 2.0	4.5 4.0	6.0 6.0	2.5 2.0	8.0 7.0	2.5 2.0	7.0 6.5	ns
t _{PLH}	Propagation Delay I_n to \overline{Z}_n	2.5 1.5	4.0 2.5	5.9 4.0	2.5 1.0	8.5 5.0	2.5 1.5	7.0 4.5	ns

Ordering Information

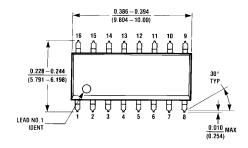
The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:

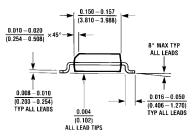


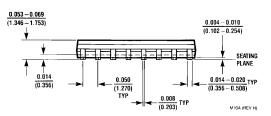
SJ = Small Outline SOIC EIAJ



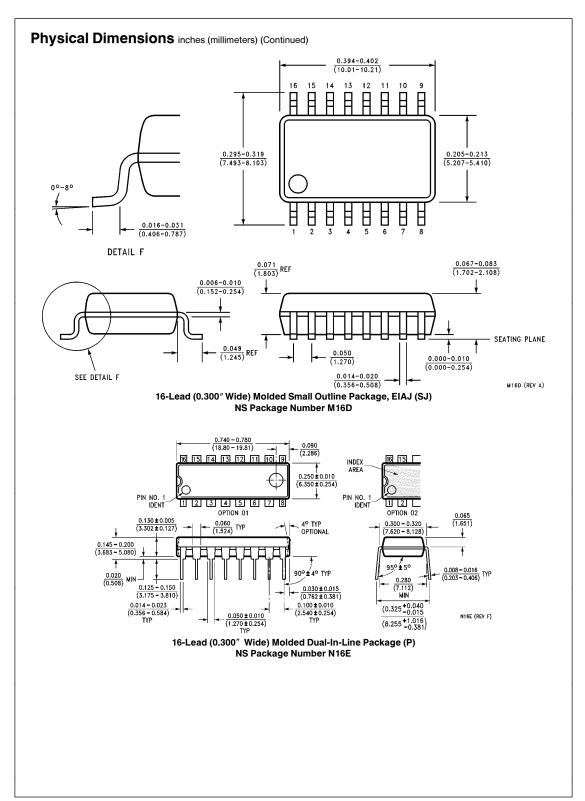
Physical Dimensions inches (millimeters) (Continued)



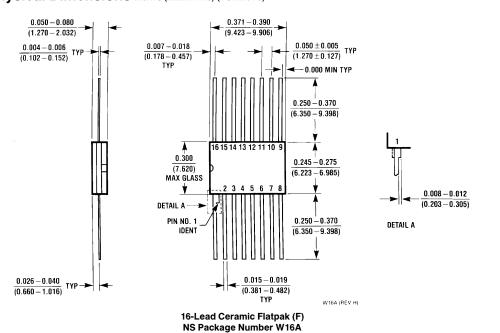




16-Lead (0.150" Wide) Molded Small Outline Package, JEDEC (S) NS Package Number M16A



Physical Dimensions inches (millimeters) (Continued)



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