

# **Schmitt Trigger Inputs**

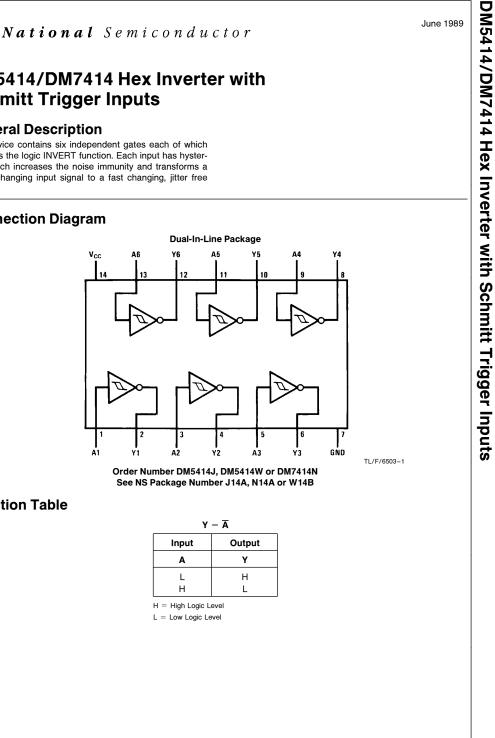
## **General Description**

This device contains six independent gates each of which performs the logic INVERT function. Each input has hysteresis which increases the noise immunity and transforms a slowly changing input signal to a fast changing, jitter free output.

## **Connection Diagram**

## **Function Table**

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## Absolute Maximum Ratings (Note)

 If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales

 Office/Distributors for availability and specifications.

 Supply Voltage
 7V

 Input Voltage
 5.5V

 Operating Free Air Temperature Range
 DM54

 DM74
 0°C to +70°C

 Storage Temperature Range
 -65°C to + 150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## **Recommended Operating Conditions**

Symbol	Parameter	DM5414			DM7414			Units
		Min	Nom	Max	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
$v_{T^+}$	Positive-Going Input Threshold Voltage (Note 1)	1.5	1.7	2	1.5	1.7	2	v
$V_{T-}$	Negative-Going Input Threshold Voltage (Note 1)	0.6	0.9	1.1	0.6	0.9	1.1	v
HYS	Input Hysteresis (Note 1)	0.4	0.8		0.4	0.8		V
I <sub>OH</sub>	High Level Output Current			-0.8			-0.8	mA
I <sub>OL</sub>	Low Level Output Current			16			16	mA
Τ <sub>Α</sub>	Free Air Operating Temperature	-55		125	0		70	°C

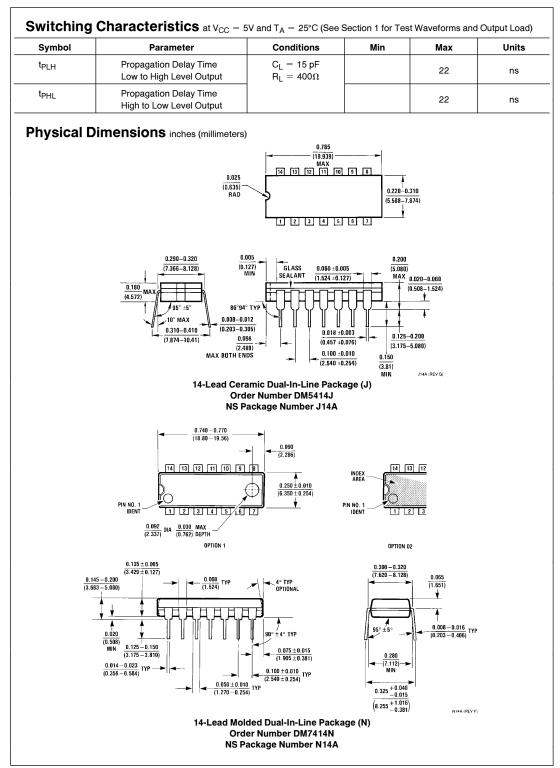
#### **Electrical Characteristics**

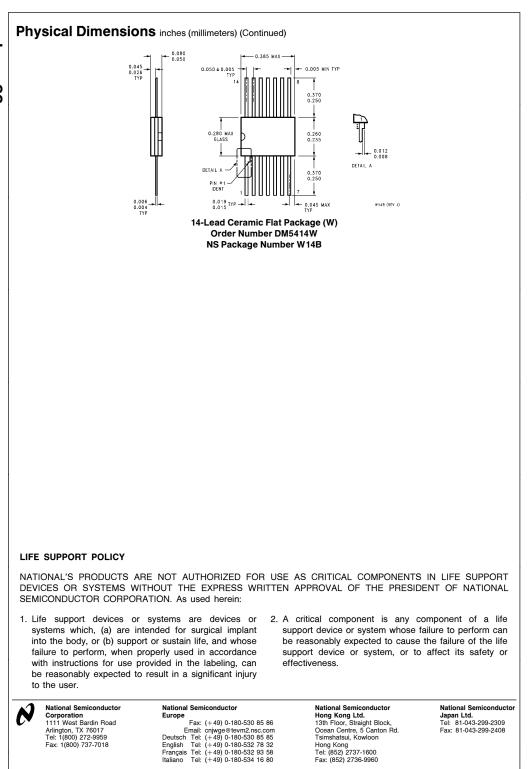
over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 2)	Мах	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -1$			-1.5	V	
V <sub>OH</sub>	High Level Output Voltage	$V_{CC} = Min, I_{OH} = N$ $V_I = V_T - Min$	2.4	3.4		v	
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = N$ $V_I = V_{T+}Max$		0.2	0.4	v	
I <sub>T+</sub>	Input Current at Positive-Going Threshold	$V_{CC} = 5V, V_I = V_T$		-0.43		mA	
I <sub>T</sub> -	Input Current at Negative-Going Threshold	$V_{CC} = 5V, V_I = V_T$		-0.56		mA	
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
IIH	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
կլ	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-1.2	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = Max	DM54	-18		-55	- mA
		(Note 3)	DM74	-18		-55	
ICCH	Supply Current with Outputs High	V <sub>CC</sub> = Max			22	36	mA
ICCL	Supply Current with Outputs Low	V <sub>CC</sub> = Max		39	60	mA	

Note 2: All typicals are at  $V_{CC}\,=\,5V,\,T_{A}\,=\,25^{\circ}C.$ 

Note 3: Not more than one output should be shorted at a time.





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