

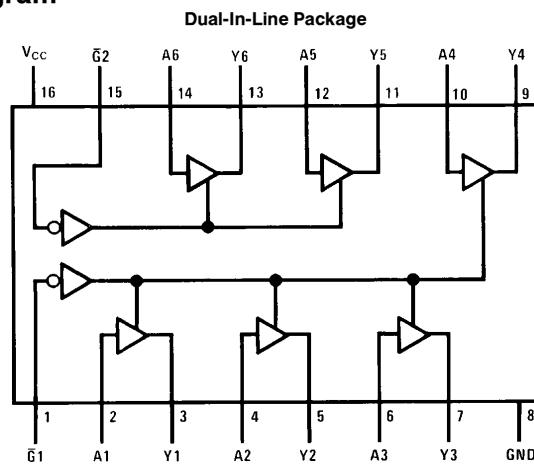
## DM54367 Hex TRI-STATE® Buffers

### General Description

This device contains six independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard TTL output with additional drive capability to permit the driving of bus lines without external resistors. When disabled, both the

output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver. To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

### Connection Diagram



TL/F/6572-1

Order Number DM54367J or DM54367W  
See NS Package Number J16A or W16A

### Function Table

$$Y = A$$

Input		Output
$\bar{G}$	A	Y
L	L	L
L	H	H
H	X	Hi-Z

H = High Logic Level  
L = Low Logic Level  
X = Either Low or High Logic Level  
Hi-Z = TRI-STATE (Outputs are disabled)

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

## Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	7V
Input Voltage	5.5V
Operating Free Air Temperature Range	-55°C to +125°C
Storage Temperature Range	-65°C to +150°C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

## Recommended Operating Conditions

Symbol	Parameter	DM54367			Units
		Min	Nom	Max	
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	High Level Input Voltage	2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8	V
I <sub>OH</sub>	High Level Output Current			-2	mA
I <sub>OL</sub>	Low Level Output Current			32	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	°C

## Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V <sub>I</sub>	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -12 mA			-1.5	V
V <sub>OH</sub>	High Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max V <sub>IL</sub> = Max, V <sub>IH</sub> = Min	2.4	3.1		V
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max V <sub>IH</sub> = Min, V <sub>IL</sub> = Max		0.2	0.4	V
I <sub>I</sub>	Input Current @ Max Input Voltage	V <sub>CC</sub> = Max, V <sub>I</sub> = 5.5V			1	mA
I <sub>IH</sub>	High Level Input Current	V <sub>CC</sub> = Max, V <sub>I</sub> = 2.4V			40	μA
I <sub>IL</sub>	Low Level Input Current	V <sub>CC</sub> = Max V <sub>I</sub> = 0.5V (Note 4)	A		-40	mA
		V <sub>CC</sub> = Max V <sub>I</sub> = 0.4V (Note 5)	A		-1.6	
		V <sub>CC</sub> = Max V <sub>I</sub> = 0.4V	$\bar{G}$		-1.6	
I <sub>OZH</sub>	Off-State Output Current with High Level Output Voltage Applied	V <sub>CC</sub> = Max, V <sub>O</sub> = 2.4V V <sub>IH</sub> = Min, V <sub>IL</sub> = Max			40	μA
I <sub>OZL</sub>	Off-State Output Current with Low Level Output Voltage Applied	V <sub>CC</sub> = Max, V <sub>O</sub> = 0.4V V <sub>IH</sub> = Min, V <sub>IL</sub> = Max			-40	μA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 2)	-40		-115	mA
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max (Note 3)		65	85	mA

Note 1: All typicals are at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

Note 2: Not more than one output should be shorted at a time.

Note 3: I<sub>CC</sub> is measured with the data inputs grounded and the output controls at 4.5V.

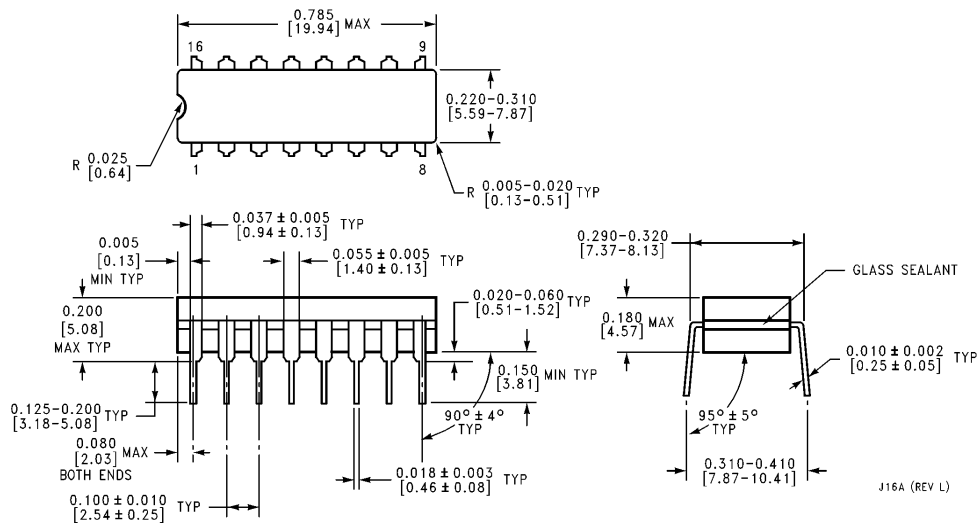
Note 4: Both  $\bar{G}$  inputs are at 2V.

Note 5: Both  $\bar{G}$  inputs are at 0.4V.

**Switching Characteristics** at  $V_{CC} = 5V$  and  $T_A = 25^\circ C$  (See Section 1 for Test Waveforms and Output Load)

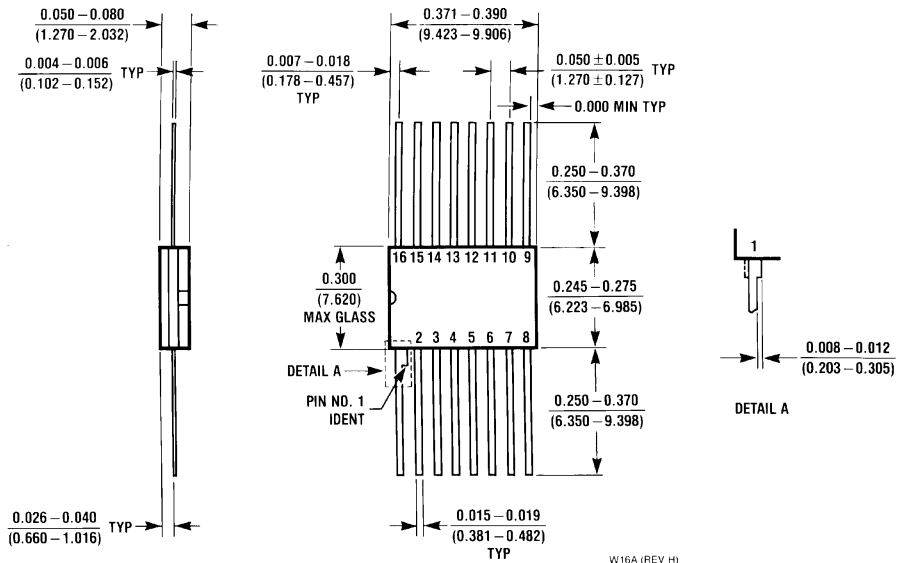
Symbol	Parameter	$R_L = 400\Omega$				Units
		$C_L = 5\text{ pF}$		$C_L = 50\text{ pF}$		
		Min	Max	Min	Max	
$t_{PLH}$	Propagation Delay Time Low to High Level Output				16	ns
$t_{PHL}$	Propagation Delay Time High to Low Level Output				22	ns
$t_{PZH}$	Output Enable Time to High Level Output				35	ns
$t_{PZL}$	Output Enable Time to Low Level Output				37	ns
$t_{PHZ}$	Output Disable Time from High Level Output		11			ns
$t_{PLZ}$	Output Disable Time from Low Level Output		27			ns

**Physical Dimensions** inches (millimeters)



**16-Lead Ceramic Dual-In-Line Package (J)**  
**Order Number DM54367J**  
**NS Package Number J16A**

**Physical Dimensions** inches (millimeters) (Continued)



**16-Lead Ceramic Flat Package (W)**  
**Order Number DM54367W**  
**NS Package Number W16A**

W16A (REV H)

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**National Semiconductor Corporation**  
 1111 West Bardin Road  
 Arlington, TX 76017  
 Tel: 1(800) 272-9959  
 Fax: 1(800) 737-7018

**National Semiconductor Europe**  
 Fax: (+49) 0-180-530 85 86  
 Email: cnjwge@tevm2.nsc.com  
 Deutsch Tel: (+49) 0-180-530 85 85  
 English Tel: (+49) 0-180-532 78 32  
 Français Tel: (+49) 0-180-532 93 58  
 Italiano Tel: (+49) 0-180-534 16 80

**National Semiconductor Hong Kong Ltd.**  
 13th Floor, Straight Block,  
 Ocean Centre, 5 Canton Rd.  
 Tsimshatsui, Kowloon  
 Hong Kong  
 Tel: (852) 2737-1600  
 Fax: (852) 2736-9960

**National Semiconductor Japan Ltd.**  
 Tel: 81-043-299-2309  
 Fax: 81-043-299-2408

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