May 1998

*N*ational *Semiconductor*

LF444
Quad Low Power JFET Input Operational Amplifier
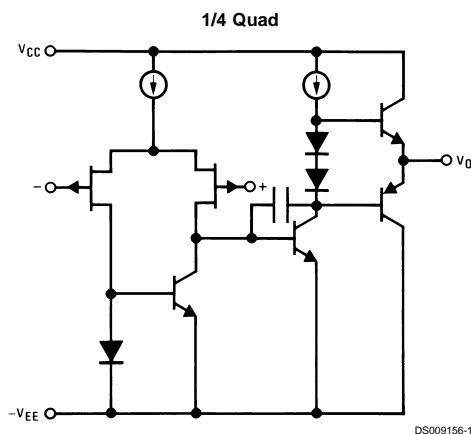
General Description

The LF444 quad low power operational amplifier provides many of the same AC characteristics as the industry standard LM148 while greatly improving the DC characteristics of the LM148. The amplifier has the same bandwidth, slew rate, and gain (10 kΩ load) as the LM148 and only draws one fourth the supply current of the LM148. In addition the well matched high voltage JFET input devices of the LF444 reduce the input bias and offset currents by a factor of 10,000 over the LM148. The LF444 also has a very low equivalent input noise voltage for a low power amplifier.

The LF444 is pin compatible with the LM148 allowing an immediate 4 times reduction in power drain in many applications. The LF444 should be used wherever low power dissipation and good electrical characteristics are the major considerations.
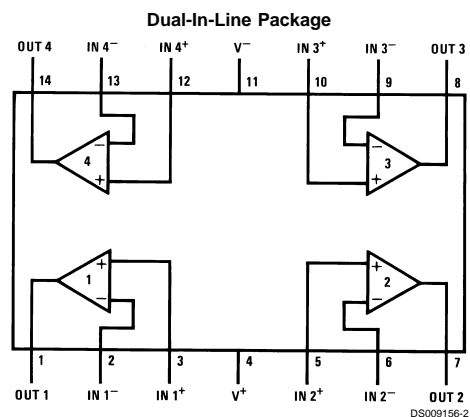
Features

■ ¼ supply current of a LM148:   200 µA/Amplifier (max)
■ Low input bias current: 50 pA (max)
■ High gain bandwidth: 1 MHz
■ High slew rate: 1 V/µs
■ Low noise voltage for low power

$$35 \text{ nV}/\sqrt{\text{Hz}}$$

■ Low input noise current

$$0.01 \text{ pA}/\sqrt{\text{Hz}}$$

■ High input impedance: $10^{12}\Omega$
■ High gain $V_O = \pm 10V$, $R_L = 10k$:   50k (min)

Simplified Schematic

1/4 Quad

DS009156-1

Connection Diagram

Dual-In-Line Package

DS009156-2

Top View
Order Number LF444AMD, LF444CM,
LF444ACN, LF444CN or LF444MD/883
See NS Package Number D14E, M14A or N14A

Ordering Information

LF444XYZ

X indicates electrical grade

Y indicates temperature range

   "M" for military, "C" for commercial

Z indicates package type "D", "M" or "N"

Absolute Maximum Ratings (Note 11)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

	LF444A	LF444
Supply Voltage	±22V	±18V
Differential Input Voltage	±38V	±30V
Input Voltage Range (Note 1)	±19V	±15V
Output Short Circuit Duration (Note 2)	Continuous	Continuous

	D Package	N, M Packages
Power Dissipation (Notes 3, 9)	900 mW	670 mW
T$_j$ max	150˚C	115˚C
θ$_{jA}$ (Typical)	100˚C/W	85˚C/W

	LF444A/LF444
Operating Temperature Range	(Note 4)
Storage Temperature Range	−65˚C ≤ T$_A$ ≤ 150˚C
ESD Tolerance (Note 10)	Rating to be determined

Soldering Information
  Dual-In-Line Packages
    (Soldering, 10 sec.) 260˚C
  Small Outline Package
    Vapor Phase (60 sec.) 215˚C
    Infrared (15 sec.) 220˚C
See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

DC Electrical Characteristics (Note 5)

Symbol	Parameter	Conditions		LF444A			LF444			Units
				Min	Typ	Max	Min	Typ	Max	
V$_{OS}$	Input Offset Voltage	R$_S$ = 10k, T$_A$ = 25˚C			2	5		3	10	mV
		0˚C ≤ T$_A$ ≤ +70˚C				6.5			12	mV
		−55˚C ≤ T$_A$ ≤ +125˚C				8				mV
ΔV$_{OS}$/ΔT	Average TC of Input Offset Voltage	R$_S$ = 10 kΩ			10			10		µV/˚C
I$_{OS}$	Input Offset Current	V$_S$ = ±15V	T$_j$ = 25˚C		5	25		5	50	pA
		(Notes 5, 6)	T$_j$ = 70˚C			1.5			1.5	nA
			T$_j$ = 125˚C			10				nA
I$_B$	Input Bias Current	V$_S$ = ±15V	T$_j$ = 25˚C		10	50		10	100	pA
		(Notes 5, 6)	T$_j$ = 70˚C			3			3	nA
			T$_j$ = 125˚C			20				nA
R$_{IN}$	Input Resistance	T$_j$ = 25˚C			10^{12}			10^{12}		Ω
A$_{VOL}$	Large Signal Voltage Gain	V$_S$ = ±15V, V$_O$ = ±10V		50	100		25	100		V/mV
		R$_L$ = 10 kΩ, T$_A$ = 25˚C								
		Over Temperature		25			15			V/mV
V$_O$	Output Voltage Swing	V$_S$ = ±15V, R$_L$ = 10 kΩ		±12	±13		±12	±13		V
V$_{CM}$	Input Common-Mode Voltage Range			±16	+18		±11	+14		V
					−17			−12		V
CMRR	Common-Mode Rejection Ratio	R$_S$ ≤ 10 kΩ		80	100		70	95		dB
PSRR	Supply Voltage Rejection Ratio	(Note 7)		80	100		70	90		dB
I$_S$	Supply Current				0.6	0.8		0.6	1.0	mA

AC Electrical Characteristics (Note 5)

Symbol	Parameter	Conditions	LF444A Min	LF444A Typ	LF444A Max	LF444 Min	LF444 Typ	LF444 Max	Units
	Amplifier-to-Amplifier Coupling			−120			−120		dB
SR	Slew Rate	$V_S = \pm15V$, $T_A = 25°C$		1			1		V/μs
GBW	Gain-Bandwidth Product	$V_S = \pm15V$, $T_A = 25°C$		1			1		MHz
e_n	Equivalent Input Noise Voltage	$T_A = 25°C$, $R_S = 100\Omega$, f = 1 kHz		35			35		nV/$\sqrt{Hz}$
i_n	Equivalent Input Noise Current	$T_A = 25°C$, f = 1 kHz		0.01			0.01		pA/$\sqrt{Hz}$

Note 1: Unless otherwise specified the absolute maximum negative input voltage is equal to the negative power supply voltage.

Note 2: Any of the amplifier outputs can be shorted to ground indefinitely, however, more than one should not be simultaneously shorted as the maximum junction temperature will be exceeded.

Note 3: For operating at elevated temperature, these devices must be derated based on a thermal resistance of θ_{jA}.

Note 4: The LF444A is available in both the commercial temperature range $0°C \leq T_A \leq 70°C$ and the military temperature range $-55°C \leq T_A \leq 125°C$. The LF444 is available in the commercial temperature range only. The temperature range is designated by the position just before the package type in the device number. A "C" indicates the commercial temperature range and an "M" indicates the military temperature range. The military temperature range is available in "D" package only.

Note 5: Unless otherwise specified the specifications apply over the full temperature range and for $V_S = \pm20V$ for the LF444A and for $V_S = \pm15V$ for the LF444. V_{OS}, I_B, and I_{OS} are measured at $V_{CM} = 0$.

Note 6: The input bias currents are junction leakage currents which approximately double for every 10°C increase in the junction temperature, T_j. Due to limited production test time, the input bias currents measured are correlated to junction temperature. In normal operation the junction temperature rises above the ambient temperature as a result of internal power dissipation, P_D. $T_j = T_A + \theta_{jA}P_D$ where θ_{jA} is the thermal resistance from junction to ambient. Use of a heat sink is recommended if input bias current is to be kept to a minimum.

Note 7: Supply voltage rejection ratio is measured for both supply magnitudes increasing or decreasing simultaneously in accordance with common practice from ±15V to ±5V for the LF444 and from ±20V to ±5V for the LF444A.

**Note 8:** Refer to RETS444X for LF444MD military specifications.

Note 9: Max. Power Dissipation is defined by the package characteristics. Operating the part near the Max. Power Dissipation may cause the part to operate outside guaranteed limits.

**Note 10:** Human body model, 1.5 kΩ in series with 100 pF.

Note 11: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.
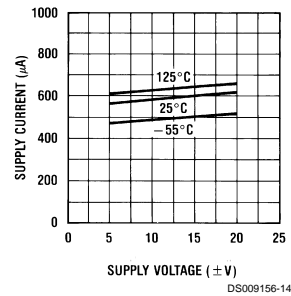
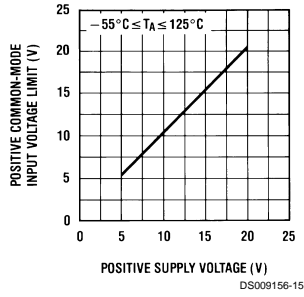## Typical Performance Characteristics

Input Bias Current
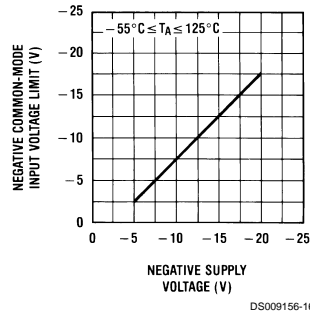

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Input Bias Current

DS009156-13

Supply Current

DS009156-14
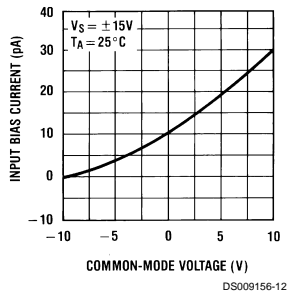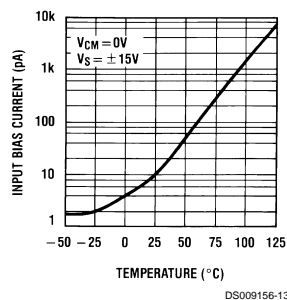
Typical Performance Characteristics (Continued)

Positive Common-Mode Input Voltage Limit

DS009156-15

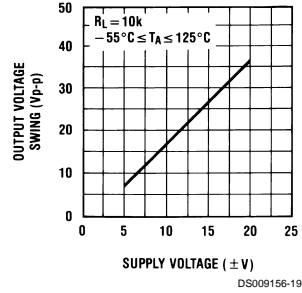### Negative Common-Mode Input Voltage Limit

DS009156-16

Positive Current Limit
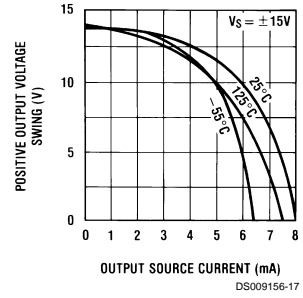
DS009156-17

Negative Current Limit
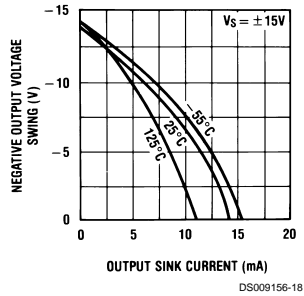
DS009156-18

Output Voltage Swing

DS009156-19

Output Voltage Swing

DS009156-20

Gain Bandwidth

DS009156-21
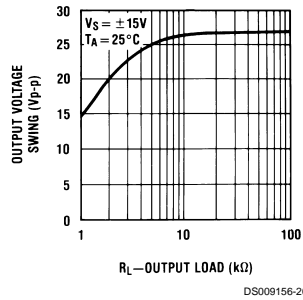
Bode Plot
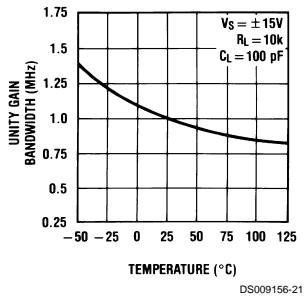
DS009156-22
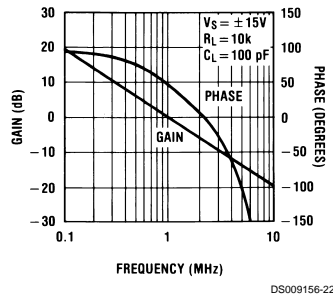
Slew Rate
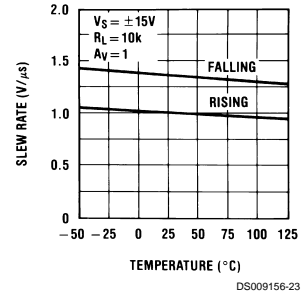
DS009156-23

Typical Performance Characteristics (Continued)

Distortion vs Frequency
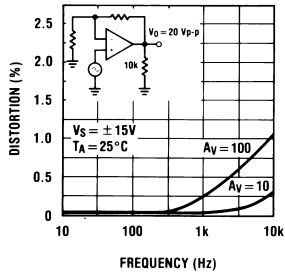
DS009156-24

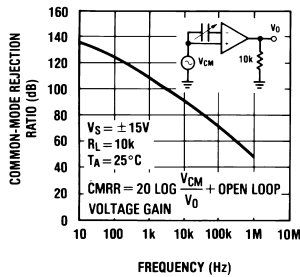### Undistorted Output Voltage Swing
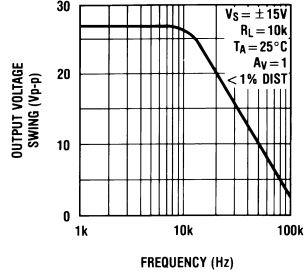
DS009156-25
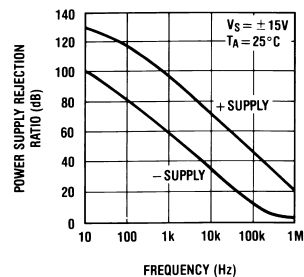
Open Loop Frequency Response
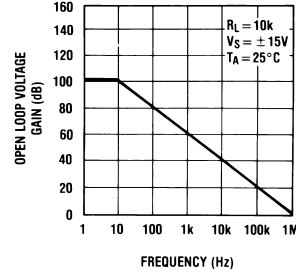
DS009156-26
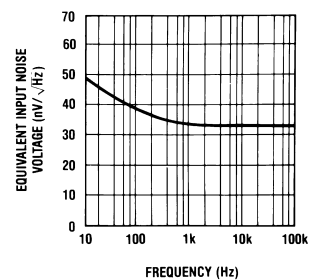
Common-Mode Rejection Ratio

DS009156-27

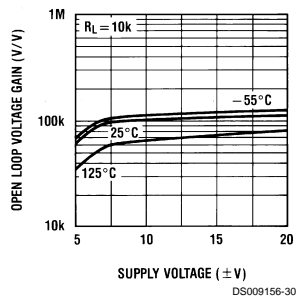### Power Supply Rejection Ratio

DS009156-28

Equivalent Input Noise Voltage

DS009156-29

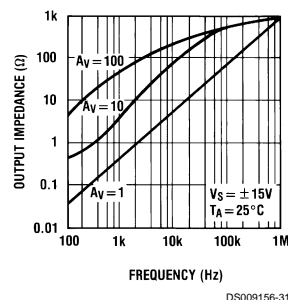### Open Loop Voltage Gain
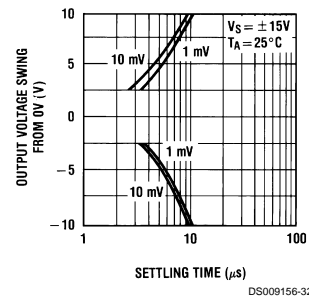

DS009156-30

Output Impedance

DS009156-31

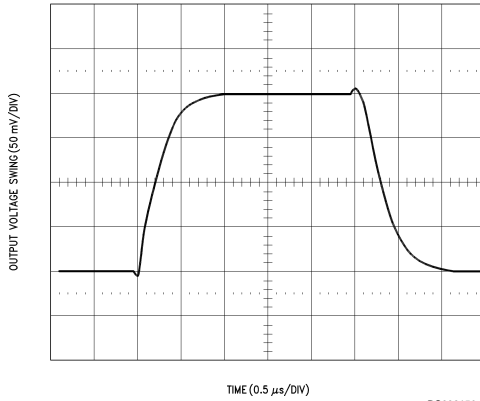### Inverter Settling Time

DS009156-32

Pulse Response R_L = 10 kΩ, C_L = 10 pF

Small Signal Inverting

OUTPUT VOLTAGE SWING (50 mV/DIV)

TIME (0.5 μs/DIV)

DS009156-6

Small Signal Non-Inverting

OUTPUT VOLTAGE SWING (50 mV/DIV)

TIME (0.5 μs/DIV)

DS009156-7

Large Signal Inverting

OUTPUT VOLTAGE SWING (5V/DIV)

TIME (10 μs/DIV)

DS009156-8

Large Signal Non-Inverting

OUTPUT VOLTAGE SWING (5V/DIV)

TIME (10 μs/DIV)

DS009156-9
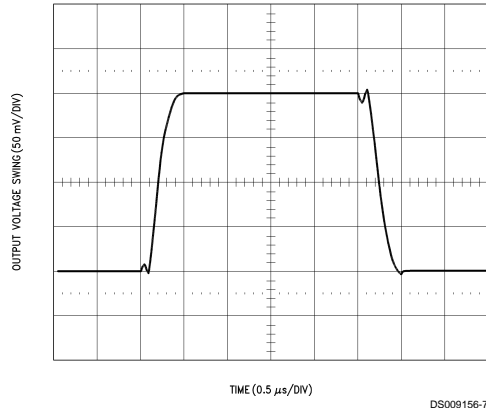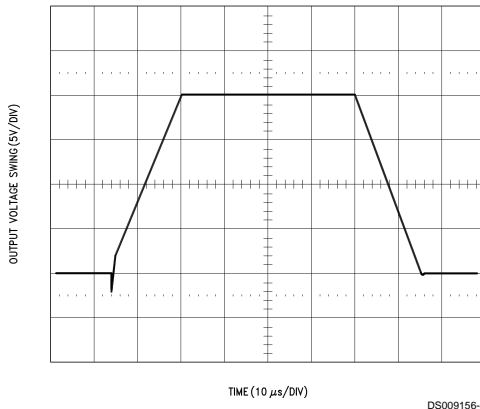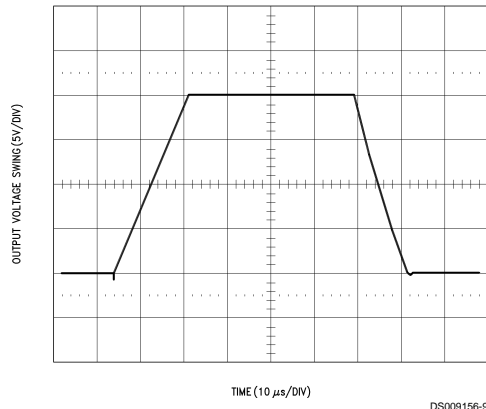
Application Hints

This device is a quad low power op amp with JFET input devices (BI-FET™). These JFETs have large reverse breakdown voltages from gate to source and drain eliminating the need for clamps across the inputs. Therefore, large differential input voltages can easily be accommodated without a large increase in input current. The maximum differential input voltage is independent of the supply voltages. However, neither of the input voltages should be allowed to exceed the negative supply as this will cause large currents to flow which can result in a destroyed unit.

Exceeding the negative common-mode limit on either input will force the output to a high state, potentially causing a reversal of phase to the output. Exceeding the negative common-mode limit on both inputs will force the amplifier output to a high state. In neither case does a latch occur since raising the input back within the common-mode range again puts the input stage and thus the amplifier in a normal operating mode.

Exceeding the positive common-mode limit on a single input will not change the phase of the output; however, if both inputs exceed the limit, the output of the amplifier will be forced to a high state.

The amplifiers will operate with a common-mode input voltage equal to the positive supply; however, the gain bandwidth and slew rate may be decreased in this condition. When the negative common-mode voltage swings to within 3V of the negative supply, an increase in input offset voltage may occur.

Each amplifier is individually biased to allow normal circuit operation with power supplies of ±3.0V. Supply voltages less than these may degrade the common-mode rejection and restrict the output voltage swing.

The amplifiers will drive a 10 kΩ load resistance to ±10V over the full temperature range. If the amplifier is forced to drive heavier load currents, however, an increase in input offset voltage may occur on the negative voltage swing and finally reach an active current limit on both positive and negative swings.

Application Hints (Continued)

Precautions should be taken to ensure that the power supply for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.

As with most amplifiers, care should be taken with lead dress, component placement and supply decoupling in order to ensure stability. For example, resistors from the output to an input should be placed with the body close to the input to minimize "pick-up" and maximize the frequency of the feedback pole by minimizing the capacitance from the input to ground.

A feedback pole is created when the feedback around any amplifier is resistive. The parallel resistance and capacitance from the input of the device (usually the inverting input) to AC ground set the frequency of the pole. In many instances the frequency of this pole is much greater than the expected 3 dB frequency of the closed loop gain and consequently there is negligible effect on stability margin. However, if the feedback pole is less than approximately 6 times the expected 3 dB frequency a lead capacitor should be placed from the output to the input of the op amp. The value of the added capacitor should be such that the RC time constant of this capacitor and the resistance it parallels is greater than or equal to the original feedback pole time constant.

Typical Application

pH Probe Amplifier/Temperature Compensator
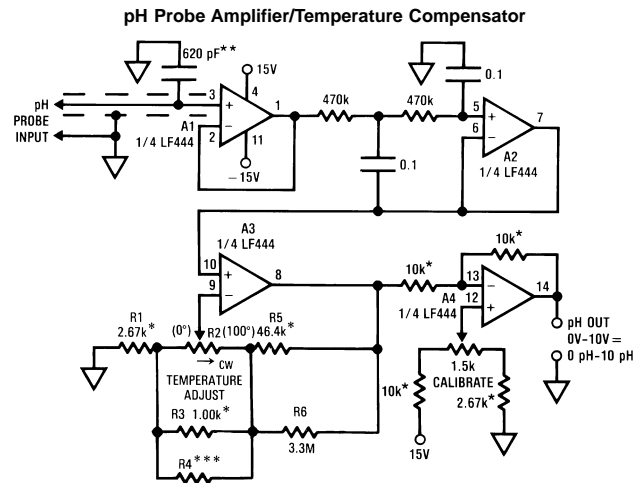
DS009156-10

***For R2 = 50k, R4 = 330k ±1%
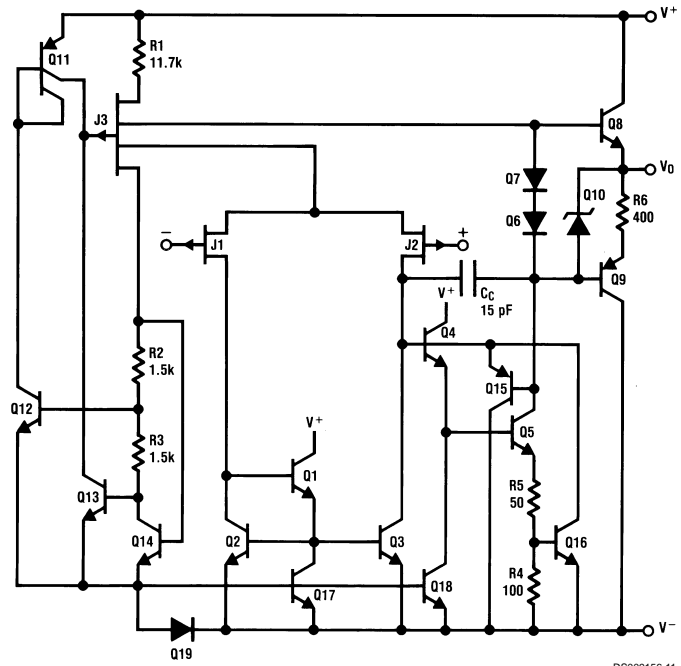For R2 = 100k, R4 = 75k ±1%
For R2 = 200k, R4 = 56k ±1%
**Polystyrene
*Film resistor type RN60C

To calibrate, insert probe in pH =7 solution. Set the "TEMPERATURE ADJUST" pot, R2, to correspond to the solution temperature: full clockwise for 0°C, and proportionately for intermediate temperatures, using a turns-counting dial. Then set "CALIBRATE" pot so output reads 7V.
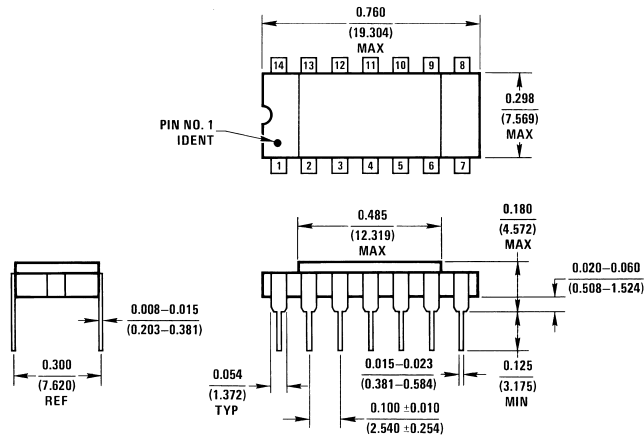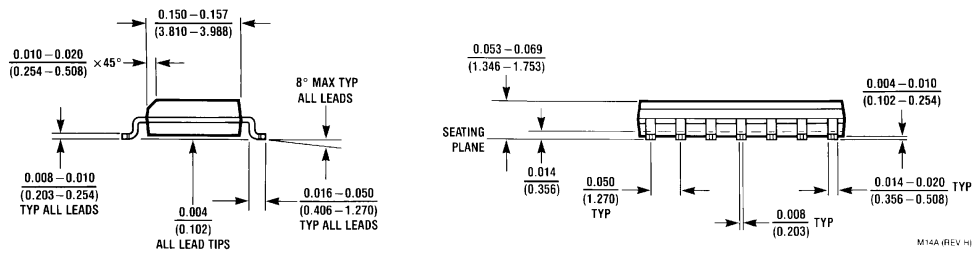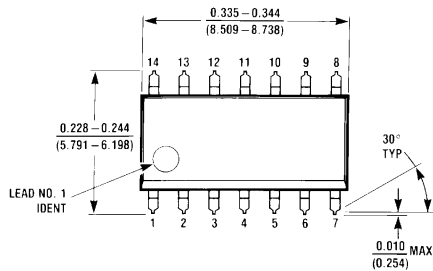Typical probe = Ingold Electrodes #465-35

www.national.com

Detailed Schematic

1/4 Quad

DS009156-11

Physical Dimensions inches (millimeters) unless otherwise noted

0.760
(19.304)
MAX

14 13 12 11 10 9 8

PIN NO. 1
IDENT

1 2 3 4 5 6 7

0.298
(7.569)
MAX

0.485
(12.319)
MAX

0.180
(4.572)
MAX

0.020−0.060
(0.508−1.524)

0.008−0.015
(0.203−0.381)

0.300
(7.620)
REF

0.054
(1.372)
TYP

0.015−0.023
(0.381−0.584)

0.100 ±0.010
(2.540 ±0.254)

0.125
(3.175)
MIN

D14E (REV E)

Order Number LF444AMD or LF444MD/883
See NS Package Number D14E

0.335−0.344
(8.509−8.738)

14 13 12 11 10 9 8

0.228−0.244
(5.791−6.198)

LEAD NO. 1
IDENT

1 2 3 4 5 6 7

30°
TYP

0.010 MAX
(0.254)

0.150−0.157
(3.810−3.988)

0.010−0.020
(0.254−0.508) ×45°

8° MAX TYP
ALL LEADS

0.008−0.010
(0.203−0.254)
TYP ALL LEADS

0.004
(0.102)
ALL LEAD TIPS

0.016−0.050
(0.406−1.270)
TYP ALL LEADS

0.053−0.069
(1.346−1.753)

0.004−0.010
(0.102−0.254)

SEATING
PLANE

0.014
(0.356)

0.050
(1.270)
TYP

0.008
(0.203) TYP

0.014−0.020
(0.356−0.508) TYP

M14A (REV H)

Order Number LF444CM
See NS Package Number M14A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)
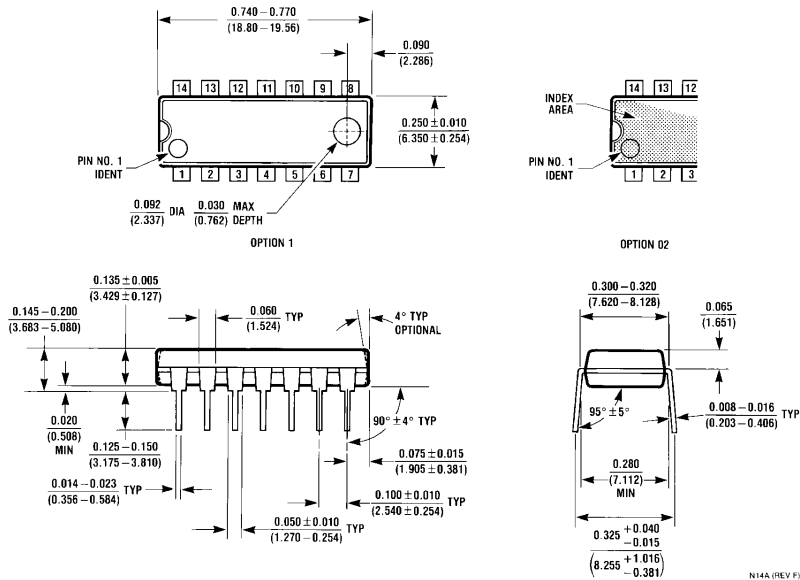
Order Number LF444ACN or LF444CN
See NS Package Number N14A

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.