OKI Semiconductor

MSM534022E

262,144-Word x 16-Bit or 524,288-Word x 8-Bit MASKROM

DESCRIPTION

The OKI MSM534022E is a high-speed CMOS Mask ROM that can electrically switch between 262,144-word x 16-bit and 524,288-word x -bit configurations. The MSM534022E operates on a single 5.0V power supply and is TTL compatible. The chip's asynchronous I/O requires no external clock assuring easy operation. A power-down mode provides low power dissipation when the chip is not selected. The CE and OE pins are provided as control signals that permit three-stated output allowing easy memory expansion on a system bus. The MSM534022E is suited for use as large capacity fixed memory for microcomputers and data terminals.

FEATURES

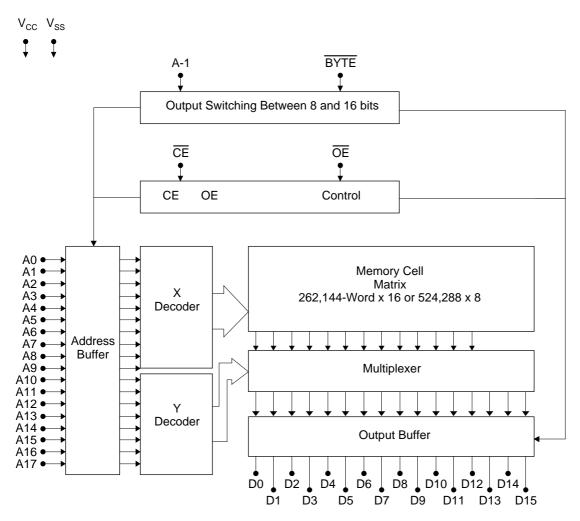
Single 5.0V power supply 262,144-words x 16-bit/524,288-words x 8-bit Access time 80ns MAX Input/Output TTL compatible Tri-State output configurations Internal powerdown function Packages: 40-PIN PLASTIC DIP (DIP40-P-600-2.54) 40-PIN PLASTIC SOP (SOP40-P-525-1.27-K) 44-PIN PLASTIC TSOP (TSOPII44-P-400-0.80-K) 4MEPROM (40-PIN) pin compatible

PIN CONFIGURATION

		1		,
A17	1	40 A8	NC 1	44 NC
A7	2	39 A9	NC 2	43 NC
A6	3	38 A10	A17 3	42 A8
A5	4	37 A11	A7 4	41 A9
			A6 5	40 A10
A4	5	36 A12	A5 6	39 A11
А3	6	35 A13	A4 7	38 A12
A2	7	34 A14	A3 8	37 A13
A1 [8	33 A15	A2 9	36 A14
A0 [9	32 A16	A1 10	35 A15
CE	10	31 BYTE	A0 11	34 A16
V _{SS} [11	30 V _{SS}	CE 12	33 BYTE
ŌE	12	29 D15/A-1	V _{SS} 13	32 V _{SS}
D0	13	28 D7	OE 14	31 D15/A-1
D8	14	27 D14	D0 15	30 D7
D1	15	26 D6	D8 16	29 D14
ſ			D1 17	28 D6
D9	16	25 D13	D9 18	27 D13
D2	17	24 D5	D2 19	26 D5
D10	18	23 D12	D10 20	25 D12
D3 [19	22 D4	D3 21	24 D4
D11 [20	21 V _{CC}	D11 22	23 V _{CC}
		J]
	40PINDIP/SOP		44PINTSOP	

Pin Name	Function
D15/A-1	Data output / address input
A0 to A17	Address input
D0 to D15	Data output
CE	Chip enable
ŌĒ	Output enable
BYTE	Mode switch
V _{CC} , V _{SS}	Power supply

BLOCK DIAGRAM



FUNCTION TABLE

CE	ŌĒ	BYTE	A-1/D15	D0 to D7	D8 to D15	D _{OUT} Mode	LSB	MSB
Н	Χ	Х	X	Hi-Z	Hi-Z	Hi-Z		_
L	Н	Х	X	Hi-Z	Hi-Z	111-2	_	
L	L	Н	Input Inhibited (D15)	D0 to D7	D8 to D15	16 bit	A0	A17
L	L	L	L	D0 to D7	Hi-Z	8 bit	Λ 1	A17
L	L	L	Н	D8 to D15	Hi-Z	O DIL	A-1	A17

ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings

Parameter	Symbol	Conditions	Rated Value	Unit
Power Supply Voltage	V _{cc}		-0.3 to 7	V
Input Voltage	V _I	to V _{SS}	-0.3 to V _{CC} + 0.5	V
Output Voltage	Vo		-0.3 to $V_{CC} + 0.5$	V
Power Dissipation	P _D	Per Package T _{opr} = 25°C	1.0	W
Operating Temperature	T _{opr}	_	0 to 70	°C
Storage Temperature	T _{stg}	_	-55 to 150	°C

Recommended Operating Conditions

Dovernator	Symbol Conditions	O a maditi a ma	F	I Imit		
Parameter		Min.	Тур.	Max.	Unit	
D 0	V _{cc}	_	4.75	5.0	5.25	٧
Power Supply Voltage	V _{SS}	_	0.0	0.0	0.0	٧
"H" Input Voltage	V _{IH}	_	2.2	5.0	V _{CC} + 0.5	٧
"L" Input Voltage	V _{IL}	_	-0.3	0.0	0.8	V
Operating Temperature	T _{opr}	_	0	_	70	°C

DC Characteristics

 $(V_{CC} = 5V\pm5\%, Ta = 0 \text{ to } 70^{\circ}C)$

Doromotor	Symbol	Conditions	R	Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
"H" Output Voltage	V _{OH}	$I_{OH} = -400 \mu A$	2.4	_	_	V
"L" Output Voltage	V _{OL}	I _{OH} = 2.1mA	_		0.4	V
Input Leakage Current	I _{LI}	$V_I = 0$ to V_{CC}	-10		10	μA
Output Leakage Current I _L		$\frac{V_O}{CE} = 0 \text{ to } V_{CC}$	-10	_	10	μA
Power Supply Current (Operating)	I _{cc}	$\overline{CE} = V_{IL,} \overline{OE} = V_{IH,} t_C = 80 \text{ns}$	_	_	45	mA
Power Supply Current	I _{CCS} 1	$\overline{CE} = V_{CC} - 0.2V$	_	_	50	μA
(Standby)	I _{ccs}	CE = V _{IH MIN}	_	_	500	μA

AC CHARACTERISTICS

Timing conditions

Parameter	Conditions
Input Signal Level	V _{IH} =3.0V, V _{IL} =0.0V
Transtion Time	t _r =t _f =5ns
Timing Reference Level	Input Voltage=1.5V Output Voltage=0.8V&2.0V
Load Condition	CL=50pF+1TTL

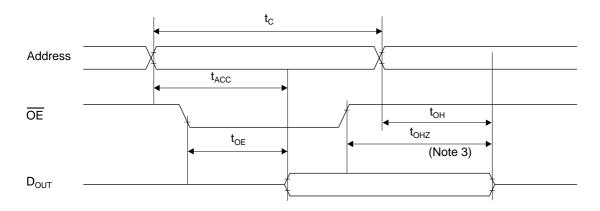
Read Cycle

 $(Ta = 0 \text{ to } 70^{\circ}C)$

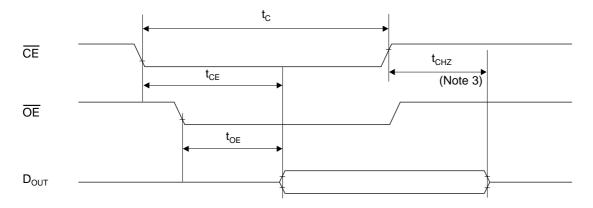
Down-return	Symbol	Conditions	Rated Value			11-24
Parameter			Min.	Тур.	Max.	Unit
Cycle time	t _C	_	80	_	_	ns
Address Access time	t _{ACC}		_	_	80	ns
CE Access time	t _{CE}	_	_	_	80	ns
OE Access time	t _{OE}	_	_	_	40	ns
CE Output Disable time	t _{CHZ}	_	0	_	35	ns
OE Output Disable time	t _{OHZ}	_	0	_	30	ns
Output Hold time	t _{OH}	_	0	_	_	ns

MSM534022E

Read Cycle (Note 1)



Read Cycle (Note 2)



Note)

- \overline{CE} is low level.
 Address is fixed before or at the same time when \overline{CE} level falls.
 t_{CHZ} & t_{OHZ} indicate the time until floating. They are not determined by the output level.

I/O CAPACITANCE

Damanatan	Symbol	Conditions	R	1		
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	Cı	V _I =0V	_	_	8	pF
Output Capacitance	Co	V _O =0V	_		10	pF



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