JFETs — General Purpose 2N5457 **N–Channel — Depletion** 1 DRAIN *Motorola Preferred Device ٦ GATE 2 SOURCE **MAXIMUM RATINGS** Rating Symbol Value Unit Drain-Source Voltage VDS 25 Vdc Drain-Gate Voltage VDG 25 Vdc CASE 29-04, STYLE 5 Reverse Gate-Source Voltage -25 Vdc VGSR TO-92 (TO-226AA) Gate Current 10 mAdc IG Total Device Dissipation @ T_A = 25°C P_{D} 310 mW Derate above 25°C 2.82 mW/°C Junction Temperature Range 125 °C ТJ T_{stg} Storage Channel Temperature Range -65 to +150 °C ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) Characteristic Min Unit Symbol Тур Max **OFF CHARACTERISTICS** Gate-Source Breakdown Voltage V(BR)GSS -25 Vdc $(I_{G} = -10 \ \mu Adc, V_{DS} = 0)$ Gate Reverse Current nAdc IGSS $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0)$ -1.0 $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0, T_A = 100^{\circ}\text{C})$ -200 Gate-Source Cutoff Voltage VGS(off) -0.5-6.0Vdc $(V_{DS} = 15 \text{ Vdc}, I_{D} = 10 \text{ nAdc})$ Gate-Source Voltage VGS -2.5Vdc $(V_{DS} = 15 \text{ Vdc}, I_{D} = 100 \mu \text{Adc})$ **ON CHARACTERISTICS** Zero-Gate-Voltage Drain Current (1) 1.0 3.0 5.0 mAdc IDSS $(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0)$ SMALL-SIGNAL CHARACTERISTICS Forward Transfer Admittance Common Source (1) y_{fs} | 1000 5000 umhos $(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ kHz})$ Output Admittance Common Source (1) |yos| 10 50 μmhos (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 kHz) Input Capacitance 4.5 7.0 Ciss pF (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 MHz) **Reverse Transfer Capacitance** Crss 1.5 3.0 pF (V_{DS} = 15 Vdc, V_{GS} = 0, f = 1.0 MHz)

1. Pulse Test; Pulse Width \leq 630 ms, Duty Cycle \leq 10%.

Preferred devices are Motorola recommended choices for future use and best overall value.



TYPICAL CHARACTERISTICS



V_{DS}, DRAIN–SOURCE VOLTAGE (VOLTS) Figure 3. Typical Drain Characteristics



VGS, GATE-SOURCE VOLTAGE (VOLTS)

TYPICAL CHARACTERISTICS



Note: Graphical data is presented for dc conditions. Tabular data is given for pulsed conditions (Pulse Width = 630 ms, Duty Cycle = 10%). Under dc conditions, self heating in higher IDSS units reduces IDSS.



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