8-Bit Magnitude Comparators

The SN74LS682, 684, 688 are 8-bit magnitude comparators. These device types are designed to perform comparisons between two eight-bit binary or BCD words. All device types provide $\overline{P = Q}$ outputs and the LS682 and LS684 have $\overline{P > Q}$ outputs also.

The LS682, LS684 and LS688 are totem pole devices. The LS682 has a 20 k Ω pullup resistor on the Q inputs for analog or switch data.

ТҮРЕ	P = Q	P > Q	OUTPUT ENABLE	OUTPUT CONFIGURATION	PULLUP
LS682	yes	yes	no	totem-pole	yes
LS684	yes	yes	no	totem-pole	no
LS688	yes	no	yes	totem-pole	no

GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Тур	Max	Unit
V _{CC}	Supply Voltage	4.75	5.0	5.25	V
T _A	Operating Ambient Temperature Range	0	25	70	°C
I _{OH}	Output Current – High			- 0.4	mA
I _{OL}	Output Current – Low			24	mA



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> LOW POWER SCHOTTKY

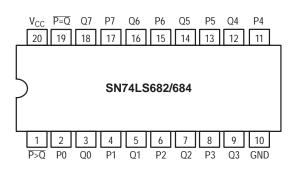


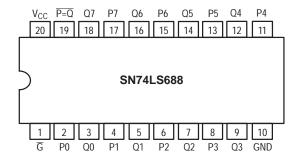


ORDERING INFORMATION

Device	Package	Shipping
SN74LS682N	16 Pin DIP	1440 Units/Box
SN74LS682DW	16 Pin	2500/Tape & Reel
SN74LS684N	16 Pin DIP	1440 Units/Box
SN74LS684DW	16 Pin	2500/Tape & Reel
SN74LS688N	16 Pin DIP	1440 Units/Box
SN74LS688DW	16 Pin	2500/Tape & Reel

CONNECTION DIAGRAMS (TOP VIEW)





FUNCTION TABLE

	INPUTS	OUTI	PUTS	
DATA	ENABL	ES		
P, Q	G, GT	G2	P = Q	<u>P > Q</u>
P = Q	L	L	L	Н
P > Q	L	L	н	L
P < Q	L	L	н	н
Х	н	н	н	Н

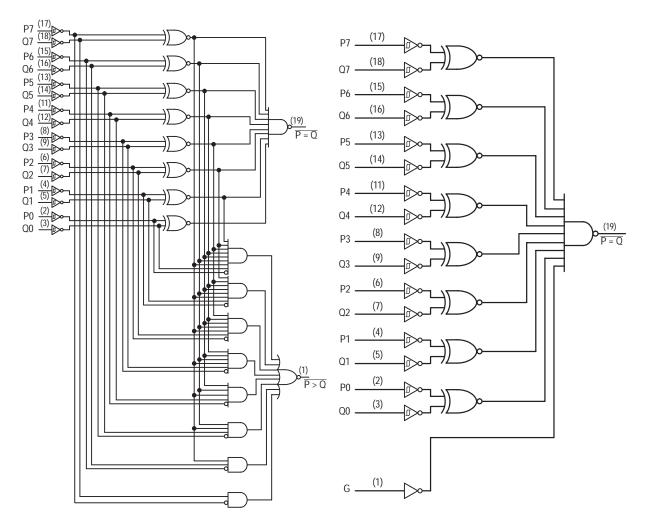
H = HIGH Level, L = LOW Level, X = Irrelevant

				Limits				
Symbol	Parameter		Min	Тур	Max	Unit	Tes	t Conditions
V _{IH}	Input HIGH Voltage		2.0			V	Guaranteed Inpu All Inputs	ut HIGH Voltage for
V _{IL}	Input LOW Voltage				0.8	V	Guaranteed Inpu All Inputs	ut LOW Voltage for
V _{IK}	Input Clamp Diode Vo	oltage		-0.65	-1.5	V	V _{CC} = MIN, I _{IN} =	- 18 mA
V _{OH}	Output HIGH Voltage		2.7	3.5		V	V _{CC} = MIN, I _{OH} or V _{IL} per Truth	= MAX, V _{IN} = V _{IH} Table
V _{OL}	Output LOW Voltage			0.25	0.4	V	I _{OL} = 12 mA	$V_{CC} = V_{CC} MIN,$
				0.35	0.5	V	I _{OL} = 24 mA	V _{IN} = V _{IL} or V _{IH} per Truth Table
					20	μΑ	V _{CC} = MAX, V _{IN}	= 2.7 V
IIH	Input HIGH Current	LS682-Q Inputs			0.1	mA	$V_{CC} = MAX, V_{IN}$	= 5.5 V
		Others			0.1	mA	$V_{CC} = MAX, V_{IN}$	= 7.0 V
		LS682-Q Inputs			-0.4	mA		0.4.1/
IIL	Input LOW Current	Others			-0.2	mA	V _{CC} = MAX, V _{IN}	= 0.4 V
I _{OS}	Short Circuit Current (Note 1)		-30		-130	mA	V _{CC} = MAX	
	LS682			70	mA			
I _{CC}	Power Supply Current	LS684			65	mA	V _{CC} = MAX	
	LS688			65	mA]		

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

LOGIC DIAGRAMS



SN74LS682 and LS684

SN74LS688

AC CHARACTERISTICS ($T_A = 25^{\circ}C$)

SN74LS682

			Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t _{PLH} t _{PHL}	Propagation Delay, P to $\overline{P} = Q$		13 15	25 25	ns	
t _{PLH} t _{PHL}	Propagation Delay, Q to $\overline{P} = \overline{Q}$		14 15	25 25	ns	V _{CC} = 5.0 V C _L = 45 pF
t _{PLH} t _{PHL}	Propagation Delay, P to $\overline{P} > Q$		20 15	30 30	ns	$R_L = 667 \Omega$
t _{PLH} t _{PHL}	Propagation Delay, Q to $\overline{P > Q}$		21 19	30 30	ns	

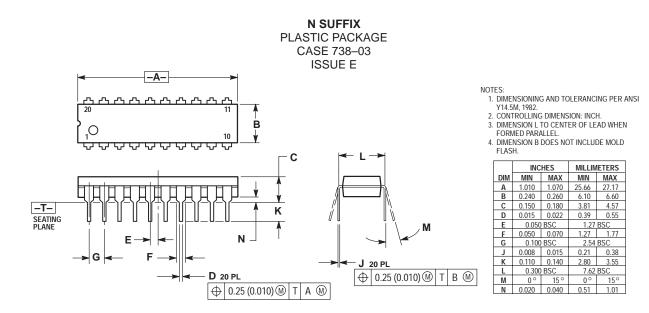
SN74LS684

			Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t _{PLH} t _{PHL}	Propagation Delay, P to $\overline{P} = Q$		15 17	25 25	ns	
t _{PLH} t _{PHL}	Propagation Delay, Q to $\overline{P} = Q$		16 15	25 25	ns	V _{CC} = 5.0 V C _L = 45 pF
t _{PLH} t _{PHL}	Propagation Delay, P to $\overline{P > Q}$		22 17	30 30	ns	$R_L = 667 \Omega$
t _{PLH} t _{PHL}	Propagation Delay, Q to $\overline{P > Q}$		24 20	30 30	ns	

SN74LS688

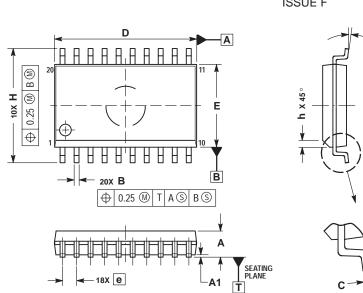
			Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t _{PLH} t _{PHL}	Propagation Delay, P to $\overline{P} = Q$		12 17	18 23	ns	
t _{PLH} t _{PHL}	Propagation Delay, Q to $\overline{P} = Q$		12 17	18 23	ns	$V_{CC} = 5.0 V$ $C_{L} = 45 \text{ pF}$ $R_{L} = 667 \Omega$
t _{PLH} t _{PHL}	Propagation Delay, \overline{G} , $\overline{G1}$ to $\overline{P} = Q$		12 13	18 20	ns	L

PACKAGE DIMENSIONS



PACKAGE DIMENSIONS

D SUFFIX



PLASTIC SOIC PACKAGE CASE 751D-05 ISSUE F

ß

- NOTES:
 1. DIMENSIONS ARE IN MILLIMETERS.
 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
 3. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
 5. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF B DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIMETERS						
DIM	MIN	MAX					
Α	2.35	2.65					
A1	0.10	0.25					
В	0.35	0.49					
С	0.23	0.32					
D	12.65	12.95					
Ε	7.40	7.60					
е	1.27	BSC					
Н	10.05	10.55					
h	0.25	0.75					
L	0.50	0.90					
θ	0 °	7 °					

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SN74LS682/D