4-Bit D Flip-Flop

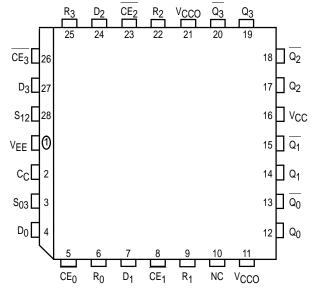
The MC10E/100E131 is a quad master-slave D-type flip-flop with differential outputs. Each flip-flop may be clocked separately by holding Common Clock (C_C) LOW and using the Clock Enable (C_E) inputs for clocking. Common clocking is achieved by holding the C_E inputs LOW and using C_C to clock all four flip-flops. In this case, the C_E inputs perform the function of controlling the common clock, to each flip-flop.

Individual asynchronous resets are provided (R). Asynchronous set controls (S) are ganged together in pairs, with the pairing chosen to reflect physical chip symmetry.

Data enters the master when both C_C and CE are LOW, and transfers to the slave when either C_C or CE (or both) go HIGH.

- 1100MHz Min. Toggle Frequency
- Differential Outputs
- · Individual and Common Clocks
- Individual Resets (asynchronous)
- Paired Sets (asynchronous)
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

Pinout: 28-Lead PLCC (Top View)



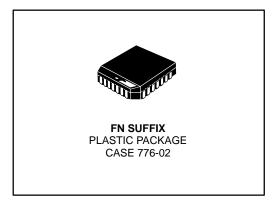
 * All VCC and VCCO pins are tied together on the die.

PIN NAMES

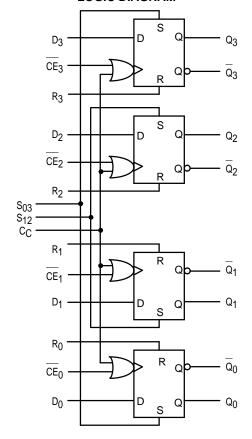
Pin	Function						
<u>D</u> ₀ – D ₃	Data Inputs						
CE ₀ – CE ₃	Clock Enables (Individual)						
$R_0 - R_3$	Resets						
CC	Common Clock						
S ₀₃ , S ₁₂	Sets (paired)						
$Q_0 - Q_3$	True Outputs						
$Q_0 - Q_3$	Inverting Outputs						

MC10E131 MC100E131

4-BIT D FLIP-FLOP



LOGIC DIAGRAM





7/96

© Motorola, Inc. 1996 REV 3

MC10E131 MC100E131

DC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = VCCO = GND)

		-40°C		0°C			25°C			85°C					
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Cond
lн	Input HIGH													μΑ	
	Current C _C			350			350			350			350	,	
	<u>_S</u>			450			450			450			450		
	R, CE			300			300			300			300		
	D			150			150			150			150		
IEE	Power Supply													mA	
	Current 10E		58	70		58	70		58	70		58	70		
	100E		58	70		58	70		58	70		67	81		

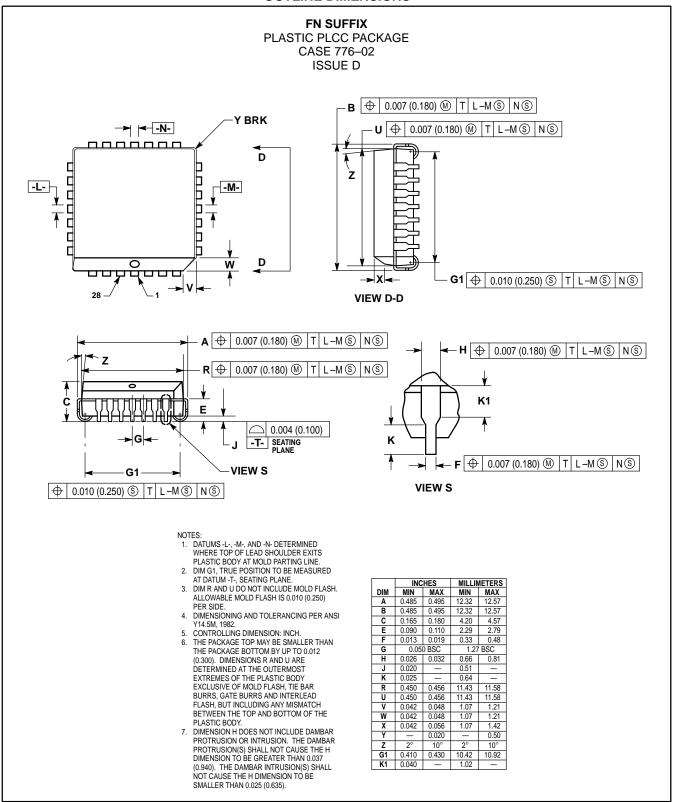
AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

			−40°C			0	°C to 85°0			
Symbol	Characteristic		Min	Тур	Max	Min	Тур	Max	Unit	Condition
fMAX	Maximum Toggle Frequency		1000	1400		1100	1400		MHz	
^t PLH ^t PHL	Propagation Delay to Output	CE C _C R S	310 275 300 300	600 600 625 550	750 725 775 775	360 325 350 350	500 500 550 550	700 675 725 725	ps	
ts	Setup Time	D	200	20		150	20		ps	1
^t H	Hold Time	D	225	-20		175	-20		ps	1
tRR	Reset Recovery Time		450	150		400	150		ps	
t _{PW}	Minimum Pulse Width	CLK R, S	400 400			400 400			ps	
tSKEW	Within-Device Skew			60			60		ps	2
t _r /t _f	Rise/Fall Time		275	460	725	300	480	675	ps	20–80%

MOTOROLA 2-2

Setup/hold times guaranteed for both C_C and CE.
Within-device skew is defined as identical transitions on similar paths through a device.

OUTLINE DIMENSIONS



MC10E131 MC100E131

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and was negligent regarding the design or manufacture of the part. Motorola and are registered trademarks of Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



