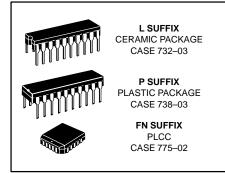
Dual Bus Driver/Receiver with 4-to-1 Output Multiplexers

The MC10H332 is a Dual Bus Driver/Receiver with four-to-one output multiplexers. These multiplexers have common selects and output enable. When disabled, (OE = high) the bus outputs go to –2.0 V. The parameters specified are with 25 Ω loading on the bus drivers and 50 Ω loads on the receivers.

- Propagation Delay, 1.5 ns Typical Data-to-Output
- Improved Noise Margin 150 mV (Over Operating Voltage and Temperature Range)
- Voltage Compensated
- MECL 10K-Compatible

MC10H332



MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit	
Power Supply (V _{CC} = 0)	VEE	-8.0 to 0	Vdc	
Input Voltage (V _{CC} = 0)	V _I	0 to VEE	Vdc	
Output Current — Continuous — Surge	lout	50 100	mA	
Operating Temperature Range	TA	0 to +75	°C	
Storage Temperature Range — Plastic — Ceramic	T _{Stg}	-55 to +150 -55 to +165	°C	

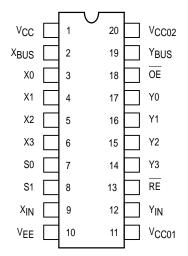
ELECTRICAL CHARACTERISTICS (VEE = -5.2 V ±5%) (See Note)

		0	0	2	5°	7	′5°	
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit
Power Supply Current	ΙE	_	115	_	110	_	115	mA
Input Current High Pins 3,4,5,6,14,	l _{inH}							μΑ
15,16,17		l —	667	l —	417	_	417	
Pins 7,8		—	437	l —	273	l —	273	
Pins 13, 18		_	456	_	285	_	285	
Input Current Low	l _{inL}	0.5	_	0.5	_	0.3	-	μΑ
High Output Voltage	Vон	-1.02	-0.84	-0.98	-0.81	-0.92	-0.735	Vdc
Low Output Voltage	VOL	-1.95	-1.63	-1.95	-1.63	-1.95	-1.60	Vdc
High Input Voltage	VIH	-1.17	-0.84	-1.13	-0.81	-1.07	-0.735	Vdc
Low Input Voltage	V_{IL}	-1.95	-1.48	-1.95	-1.48	-1.95	-1.45	Vdc

AC PARAMETERS

Propagation Delay	t _{pd}							ns
Data-to-Bus Output	·	0.8	3.0	0.8	3.0	0.8	3.2	
Select-to-Bus								
<u>O</u> utput		0.8	3.4	0.8	3.4	0.8	3.8	
OE-to-Bus Output		0.8	2.4	0.8	2.4	0.8	2.6	
Bus-to-Receiver		0.8	2.1	0.8	2.1	0.8	2.4	
Select-to-Receiver		1.8	4.5	1.8	4.5	1.8	5.0	
RE-to-Receiver		0.8	2.2	0.8	2.2	0.8	2.5	
Data-to-Receiver		1.3	4.0	1.3	4.0	1.3	4.5	
Rise Time	t _r	0.5	2.0	0.5	2.0	0.5	2.1	ns
Fall Time	t _f	0.5	2.0	0.5	2.0	0.5	2.1	ns

DIP & PLCC PIN ASSIGNMENT



Pin assignment is for Dual–in–Line Package. For PLCC pin assignment, see the Pin Conversion Tables on page 6–11 of the Motorola MECL Data Book (DL122/D).

NOTE:

Each MECL 10H series circuit has been designed to meet the dc specifications shown in the test table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 Ifpm is maintained. Receiver outputs are terminated through a 50–ohm resistor to –2.0 volts dc. Bus outputs are terminated through a 25–ohm resistor to –2.0 volts dc.

<u>3/9</u>3



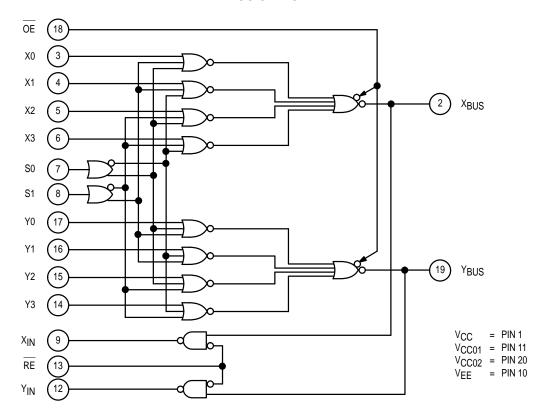
MULTIPLEXER TRUTH TABLE

OE	S 1	S0	X _{Bus}	Y _{Bus}
Н	Х	Х	-2.0V	-2.0V
L	L	L	X0	Y0
L	L	Н	X1	Y1
L	Н	L	X2	Y2
L	Н	Н	Х3	Y3

RECEIVER TRUTH TABLE

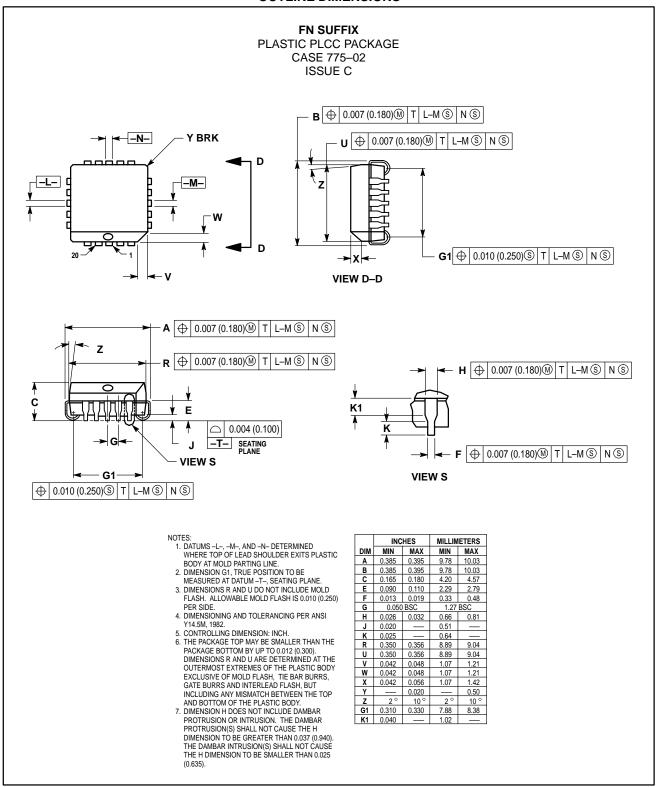
RE	Xin	Yin
Н	L	L
Ĺ	X _{Bus}	Y _{Bus}

LOGIC DIAGRAM

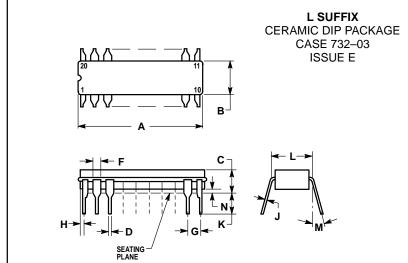


MOTOROLA 2–14

OUTLINE DIMENSIONS



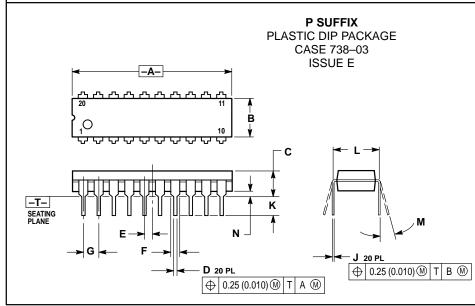
OUTLINE DIMENSIONS



- 1. LEADS WITHIN 0.010 DIAMETER, TRUE POSITION AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
- 2. DIMENSION L TO CENTER OF LEADS WHEN
- FORMED PARALLEL.

 3. DIMENSIONS A AND B INCLUDE MENISCUS.

	INCHES					
DIM	MIN MAX					
Α	0.940	0.990				
В	0.260	0.295				
C	0.150	0.200				
ם	0.015	0.022				
F	0.055	0.065				
G	0.100	0.100 BSC				
Η	0.020	0.050				
J	0.008	0.012				
K	0.125	0.160				
L	0.300 BSC					
М	0°	15°				
N	0.010	0.040				



NOTES:

- OTES.

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.
- DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
 DIMENSION B DOES NOT INCLUDE MOLD

T EAGH.						
	INC	HES	MILLIMETERS			
DIM	MIN	MIN MAX		MAX		
Α	1.010	1.070	25.66	27.17		
В	0.240	0.260	6.10	6.60		
С	0.150	0.180	3.81	4.57		
D	0.015	0.022	0.39	0.55		
Е	0.050 BSC		1.27 BSC			
F	0.050	0.070	1.27	1.77		
G	0.100	BSC	2.54 BSC			
J	0.008	0.015	0.21	0.38		
K	0.110	0.140	2.80	3.55		
L	0.300	BSC	7.62	BSC		
М	0°	15°	0°	15°		
N	0.020	0.040	0.51	1.01		

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