# **Dual Differential LVPECL to TTL Translator**

The MC100LVELT23 is a dual differential LVPECL to TTL translator. Because LVPECL (Positive ECL) levels are used only +3.3V and ground are required. The small outline 8-lead SOIC package and the dual gate design of the LVELT23 makes it ideal for applications which require the translation of a clock and a data signal.

The LVELT23 is available in only the ECL 100K standard. Since there are no LVPECL outputs or an external VBB reference, the LVELT23 does not require both ECL standard versions. The LVPECL inputs are differential; there is no specified difference between the differential input 10H and 100K standards. Therefore, the MC100LVELT23 can accept any standard differential LVPECL input referenced from a VCC of 3.3V.

- 2.0ns Typical Propagation Delay
- Differential LVPECL Inputs
- Small Outline SOIC Package
- 24mA TTL Outputs
- Flow Through Pinouts
- ESD Performance: Human Body Model 1200V; Machine Model 150V

#### Note:

 Pulling the output higher than V<sub>CC</sub> is not recommended. Doing so causes excessive leakage and possible latchup leading to reliability risk.

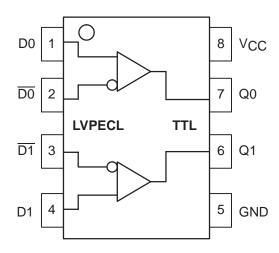


Figure 1. 8-Lead Pinout and Logic Diagram

## MC100LVELT23



D SUFFIX
8-LEAD PLASTIC SOIC PACKAGE
CASE 751-06

#### **PIN DESCRIPTION**

PIN	FUNCTION
Qn	TTL Outputs
Dn	Diff LVPECL Inputs
VCC	+3.3V Supply
GND	Ground



#### **MAXIMUM RATINGS\***

Symbol	Parameter	Value	Unit
Vcc	DC Supply Voltage (Referenced to GND)	-0.5 to +3.8	V
TA	Operating Temperature Range (In Free-Air)	-40 to 85	°C
TSTG	Storage Temperature Range	−55 to +150	°C
Θ	Thermal Resistnace Through Package (No Air Flow)	130	°C/W

<sup>\*</sup> Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

## TTL OUTPUT DC CHARACTERISTICS ( $V_{CC}$ = 3.0V to 3.6V; $T_A$ = -40°C to 85°C)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
V <sub>OH</sub>	Output HIGH Voltage	2.0			V	I <sub>OH</sub> = -3.0mA
VOL	Output LOW Voltage			0.5	V	I <sub>OL</sub> = 24mA
Іссн	Power Supply Current		20	26	mA	
ICCL	Power Supply Current		28	37	mA	
los	Output Short Circuit Current	-80		-130	mA	

## **PECL INPUT DC CHARACTERISTICS** ( $V_{CC} = 3.0 V$ to 3.6 V; $T_A = -40 ^{\circ} C$ to $85 ^{\circ} C$ )

		-40°C		0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
lіН	Input HIGH Current		150		150			150		150	μΑ	
IIL	Input LOW Current $\frac{D0}{\overline{D0}}, \frac{D1}{\overline{D1}}$	-100 -100		-100 -100		-100 -100			-100 -100		μА	
VCMR	Common Mode Range	1.2	Vcc	1.2	Vcc	1.2		Vcc	1.2	Vcc	V	
VPP	Minimum Peak-to-Peak Input	100		100		100			100		mV	Note 1.

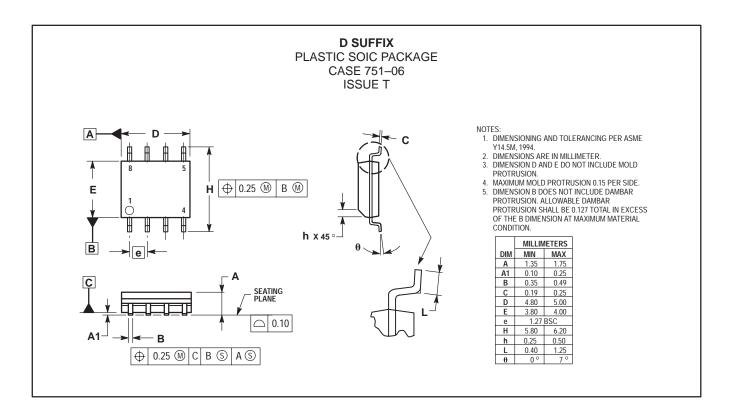
<sup>1. 200</sup>mV input guarantees full logic swing at the output.

### AC CHARACTERISTICS ( $V_{CC} = 3.0V \text{ to } 3.6V; T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$ )

		-40°C			0°C			25°C			85°C			
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
<sup>t</sup> PLH	Propagation Delay (Note 2.)	1.0	1.7	3.0	1.0	1.7	3.0	1.0	1.7	3.0	1.0	1.7	3.0	ns
tPHL	Propagation Delay (Note 2.)	1.0	1.4	3.0	1.0	1.4	3.0	1.0	1.4	3.0	1.0	1.4	3.0	ns
f <sub>max</sub>	Max Input Frequency (Note 2.)	275			275			275			275			MHz
<sup>t</sup> skpp	Part-to-Part Skew (Note 2.)			0.5			0.5			0.5			0.5	ns
t <sub>sk++</sub>	Output-to-Output Skew			60			60			60			110	ps
t <sub>sk</sub>	Output-to-Output Skew			25			25			25			25	ps
t <sub>r</sub> , t <sub>f</sub>	Output Rise/Fall	330		700	330		700	330		700	330		700	ps

<sup>2.</sup> C<sub>L</sub> = 20pF.

MOTOROLA 3–2



3-3

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