3-Bit Registered Bus Transceiver

The MC10E/MC100E336 contains three bus transceivers with both transmit and receive registers. The bus outputs (BUS0-BUS2) are specified for driving a 25Ω bus; the receive outputs (Q0 - Q2) are specified for 50Ω . The bus outputs feature a normal HIGH level (V_{OH}) and a cutoff LOW level — when LOW, the outputs go to -2.0V and the output emitter-follower is "off", presenting a high impedance to the bus. The bus outputs also feature edge slow-down capacitors.

- 25Ω Cutoff Bus Outputs
- 50Ω Receiver Outputs
- Transmit and Receive Registers
- 1500ps Max. Clock to Bus
- 1000ps Max. Clock to Q
- Bus Outputs Feature Internal Edge Slow-Down Capacitors
- Additional Package Ground Pins
- Extended 100E V_{EE} Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

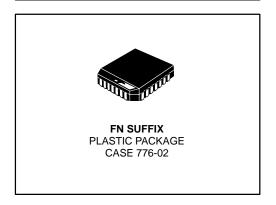
The Transmit Enable pins (TEN) control whether current data is held in the transmit register, or new data is loaded from the A/B inputs. A LOW on both of the Bus Enable inputs (BUSEN), when clocked through the register, disables the bus outputs to -2.0V.

The receiver section clocks bus data into the receive registers, after gating with the Receive Enable (RXEN) input.

All registers are clocked by a positive transition of CLK1 or CLK2 (or both).

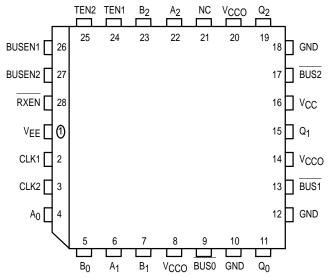
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3-BIT REGISTERED BUS TRANSCEIVER

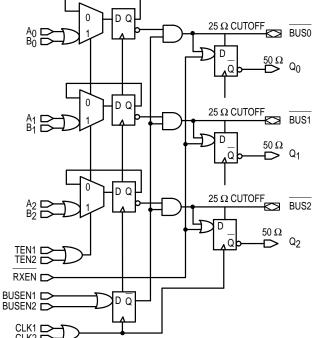


Additional leadframe grounding is provided through the Ground pins (GND) which should be connected to 0V. The GND pins are not electrically connected to the chip.

Pinout: 28-Lead PLCC (Top View)



* All VCC and VCCO pins are tied together on the die.



LOGIC DIAGRAM

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DC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		0°C			25°C			85°C				
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
VCUT	Cut-off Output Voltage1	- 2.10		- 2.03	- 2.10		- 2.03	- 2.10		- 2.03	V	
liH	Input HIGH Current RXEN All Other Inputs			225 150			225 150			225 150	μА	
lEE	Power Supply Current 10E 100E		125 125	150 150		125 125	150 150		125 144	150 173	mA	

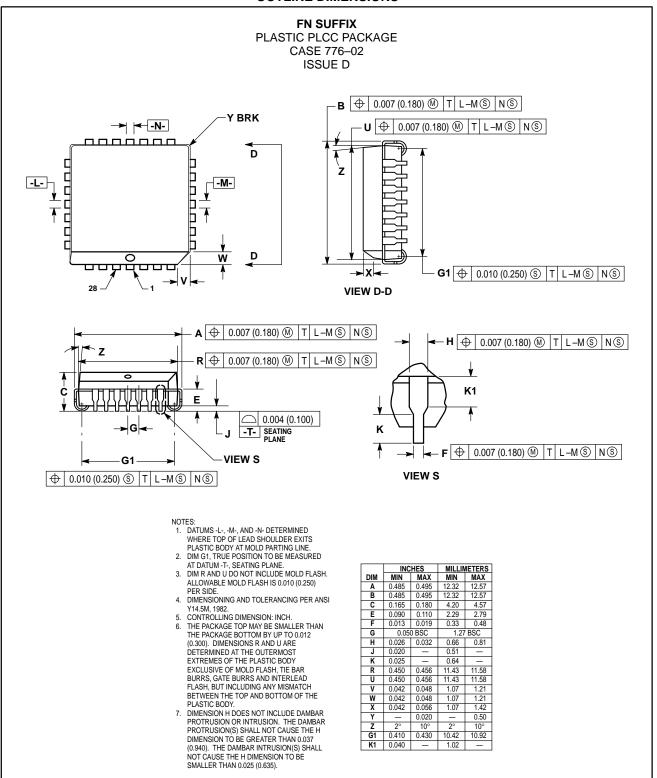
^{1.} Measured with $V_{TT} = -2.10V$

$\textbf{AC CHARACTERISTICS} \; (\forall_{EE} = \forall_{EE} (min) \; to \; \forall_{EE} (max); \; \forall_{CC} = \forall_{CCO} = GND)$

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH	Propagation Delay to Output										ps	
t _{PHL}	Clk to Q	500	700	100	500	700	1000	500	700	1000		
	Clk to BUS	825	1250	1800	825	1250	1800	825	1250	1800		
t _S	Setup Time										ps	
	BUS, RXEN	150	-150		150	-150		150	-150			
	BUSEN	100	- 200		100	- 200		100	- 200			
	A, B Data	300	- 50		300	- 50		300	- 50			
	TEN	450	150		450	150		450	150			
t _h	Hold Time										ps	
	BUS, RXEN	450	150		450	150		450	150			
	BUSEN	500	200		500	200		500	200			
	A, B Data	350	50		350	50		350	50			
	TEN	200	-150		200	-150		200	-150			
tpW	Minimum Pulse Width										ps	
	Clk	400			400			400				
t _r	Rise/Fall Times										ps	
t _f	20 - 80% (<u>Q</u> n)	300	450	700	300	450	700	300	450	700		
	20 - 80% (BUSn Rise)	500	800	1000	500	800	1000	500	800	1000		
	20 - 80% (BUSn Fall)	300	500	800	300	500	800	300	500	800		

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OUTLINE DIMENSIONS



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