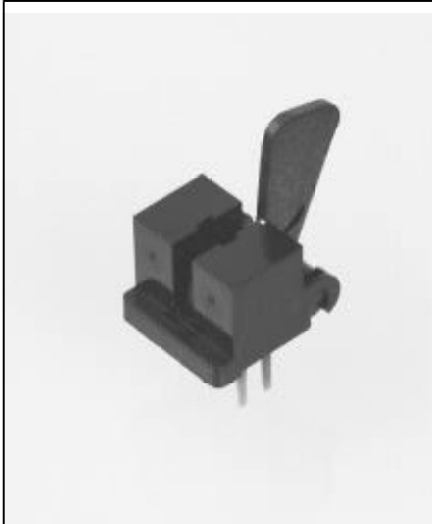


Photologic[®] Optical Flag Switch

Types OPB685, OPB686, OPB687, OPB688



Features

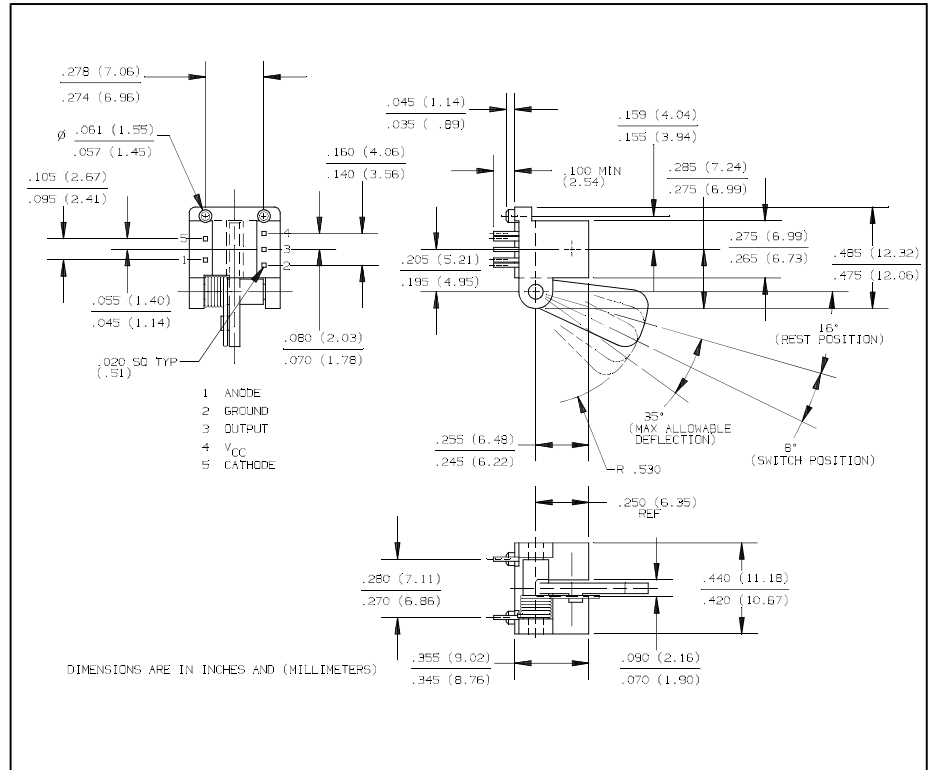
- Photologic[®] output
- Four output options
- Mechanical switch replacement
- Printed circuit board mounting

Description

The OPB685 series flag switches consist of an infrared emitting diode and a monolithic integrated circuit, which incorporates a photodiode, a linear amplifier and a Schmitt trigger. A lever arm actuated flag interrupts the light beam switching the output between states that can readily drive logic gates.

Customized lever arms and spring torques can be designed for specific applications.

The device features TTL/LSTTL compatible logic level output which can drive up to 10 TTL loads over a voltage range from 4.5 V to 16 V.



Absolute Maximum Ratings (T_A = 25^o C unless otherwise noted)

Storage Temperature Range	-40 ^o C to +100 ^o C
Operating Temperature Range	-40 ^o C to +100 ^o C
Lead Soldering Temperature [1/16 inch (1.6 mm) from case for 5 sec. with soldering iron]	240 ^o C

Input Diode

Forward DC Current	50 mA
Peak Forward Current (1μs pulse width, 300 pps)	3.0 A
Reverse DC Voltage	3.0 V
Power Dissipation	100 mW ⁽²⁾

Output Photologic[®]

Supply Voltage, V _{CC}	18 V
Duration of Output Short To V _{CC}	1.00 sec
Voltage at Output	30 V
Low Level Output Current (sinking)	16 mA
Power Dissipation	240 mW ⁽³⁾

Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 sec. max. when flow soldering.
- (2) Derate linearly 1.33 mW/^o C above 25^o C.
- (3) Derate linearly 2.50 mW/^o C above 30^o C.

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Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Input Diode						
V_F	Forward Voltage			1.6	V	$I_F = 10\text{ mA}$
I_R	Reverse Current			100	μA	$V_R = 3.0\text{ V}$
Output Photologic[®] Sensor						
V_{CC}	Operating D.C. Supply Voltage	4.5		16.0	V	
$I_F(+)$	LED Positive-Going Threshold Current	0.1	1.8	10.0	mA	$V_{CC} = 5.0\text{ V}$
$I_F(+)/I_F(-)$	Hysteresis Ratio	1.05	1.20	1.60		$V_{CC} = 5.0\text{ V}$
I_{CCH}	High Level Supply Current:					$V_{CC} = 16\text{ V}$, No Load On Output, $I_F = 10\text{ mA}$
	Buffer, 10K Pull-up Buffer, Open-Collector	OPB685 OPB686	5.0	12.0	mA	
	Inverter, 10K Pull-up Inverter, Open-Collector	OPB687 OPB688	4.0	12.0	mA	$V_{CC} = 16\text{ V}$, No Load On Output, $I_F = 0\text{ mA}$
	I_{CCL}	Low Level Supply Current:				
Buffer, 10K Pull-up		OPB685	5.5	12.0	mA	
Buffer, Open-Collector		OPB686	4.0	12.0		
Inverter, 10K Pull-up Inverter, Open-Collector		OPB687 OPB688	6.5 5.0	12.0 12.0	mA	
V_{OH}	High Level Output Voltage:					$I_{OH} = 100\ \mu\text{A}$, $I_F = 10\text{ mA}$
	Buffer, 10K Pull-up Inverter, 10K Pull-up	OPB685 OPB687	$(V_{CC}-1.5)^{(5)}$ $(V_{CC}-1.5)^{(5)}$		V	
I_{OH}	High Level Output Current:					$V_{CC} = 16\text{ V}$, $V_{OH} = 30\text{ V}$, $I_F = 10\text{ mA}$
	Buffer, Open-Collector Inverter, Open-Collector	OPB686 OPB688		100 100	μA μA	
V_{OL}	Low Level Output Voltage:					$V_{CC} = 4.5\text{ V}$, $I_{OL} = 16\text{ mA}$, $I_F = 0\text{ mA}^{(4)}$
	Buffer, 10K Pull-up Buffer, Open-Collector	OPB685 OPB686		0.4	V	
	Inverter, 10K Pull-up Inverter, Open-Collector	OPB687 OPB688		0.4	V	
t_r, t_f	Output Rise Time, Output Fall Time		30		ns	$V_{CC} = 5\text{ V}$, $I_F = 0$ or 10 mA , $f = 10\text{ kHz}$, D.C. = 50%, $R_L = 300\ \Omega$
t_{PLH}	Propagation Delay, Low-High					
	Buffer, 10K Pull-up Buffer, Open-Collector	OPB685 OPB686	1.0		μs	
	Inverter, 10K Pull-up Inverter, Open-Collector	OPB687 OPB688	2.0		μs	
t_{PHL}	Propagation Delay, High-Low					
	Buffer, 10K Pull-up Buffer, Open-Collector	OPB685 OPB686	2.0		μs	
	Inverter, 10K Pull-up Inverter, Open-Collector	OPB687 OPB688	1.0		μs	

(4) Normal application would be with light source blocked, simulated by $I_F = 0\text{ mA}$.

(5) $V_{OH} = V_{CC} - 1.5\text{ V}$ for $V_{CC} = 4.5\text{ V}$ to 16 V .

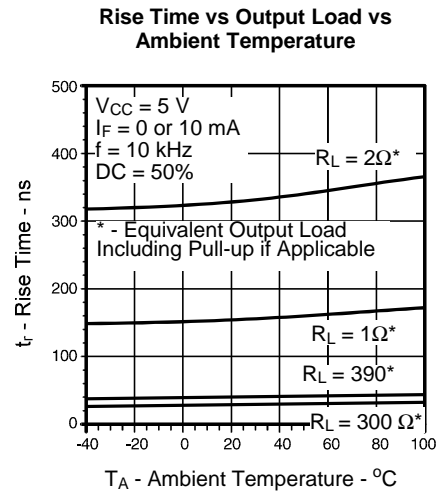
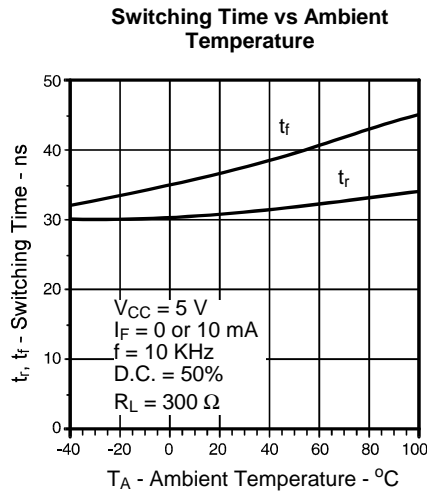
SLOTTED
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SEMICONDUCTORS

Optek reserves the right to make changes at any time in order to improve design and to supply the best product possible.

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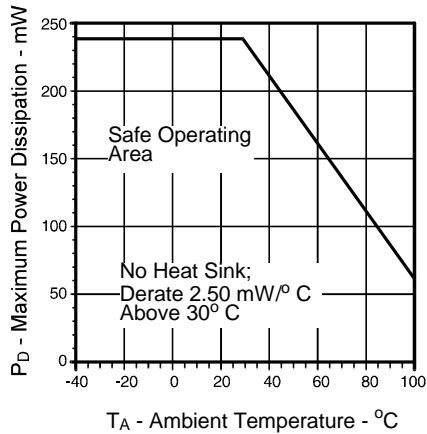
Types OPB685, OPB686, OPB687, OPB688

Typical Performance Curves

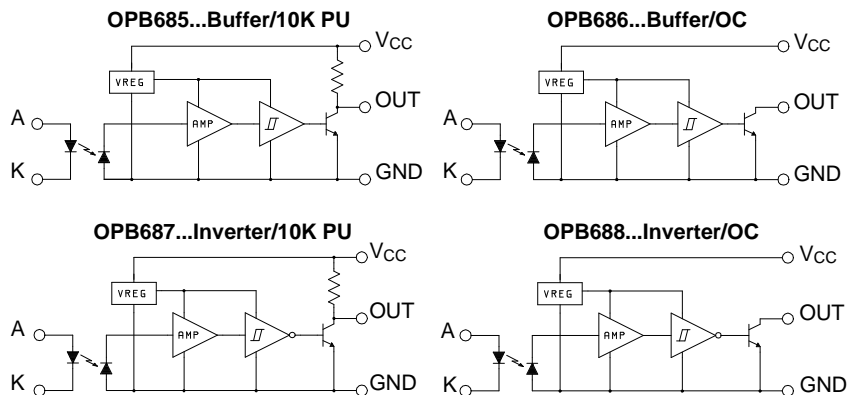


SLOTTED OPTICAL SEMICONDUCTORS

Typical Thermal Derating Curve



Schematics

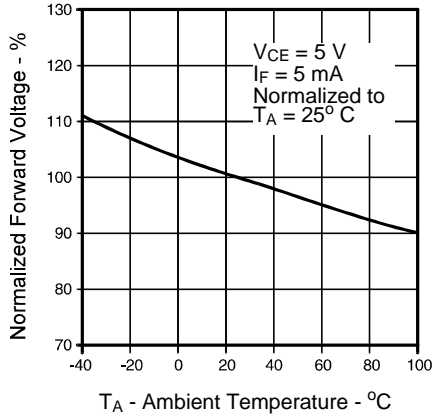


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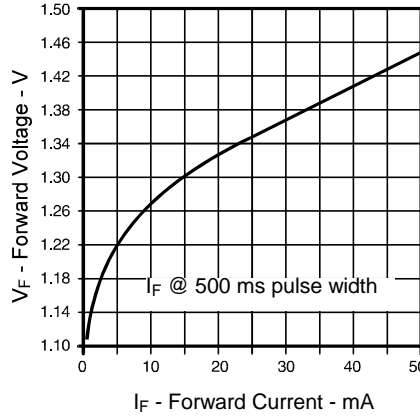
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Typical Performance Curves

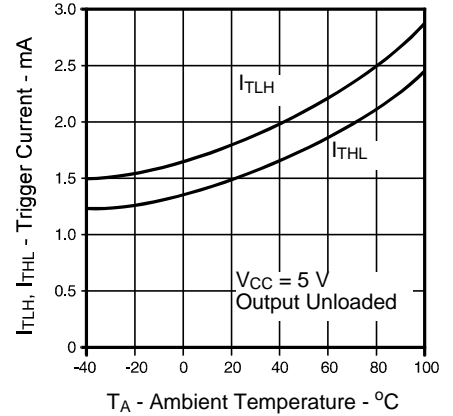
Normalized Forward Voltage vs Ambient Temperature



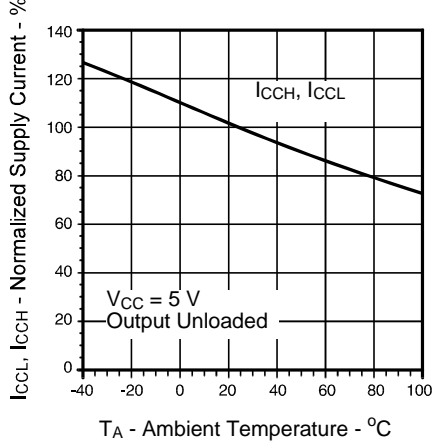
Forward Current vs Forward Voltage Input Diode



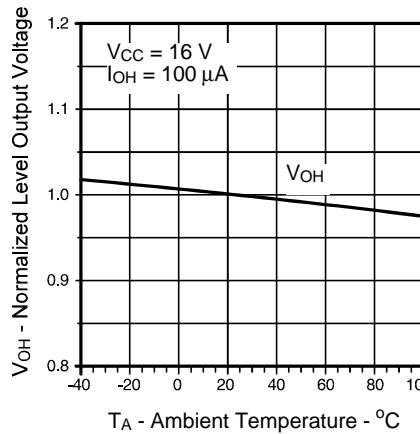
Trigger Current vs Ambient Temperature



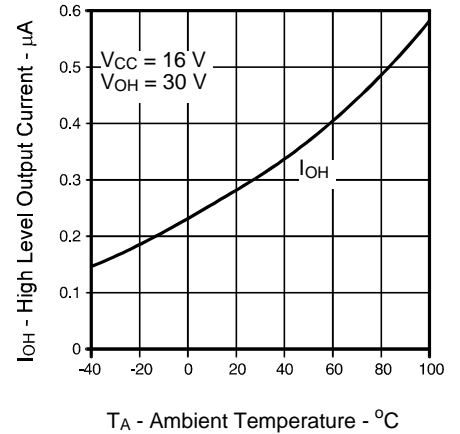
Normalized Supply Current vs Ambient Temperature



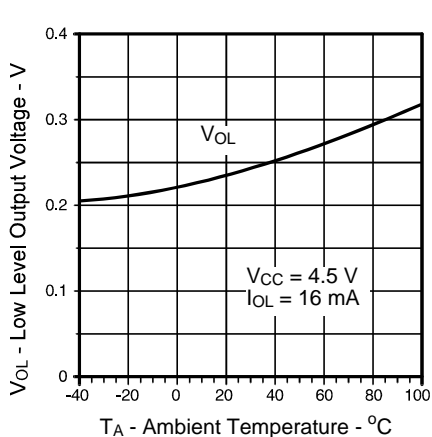
OPB685, OPB687 Normalized High Level Output Voltage vs Ambient Temperature



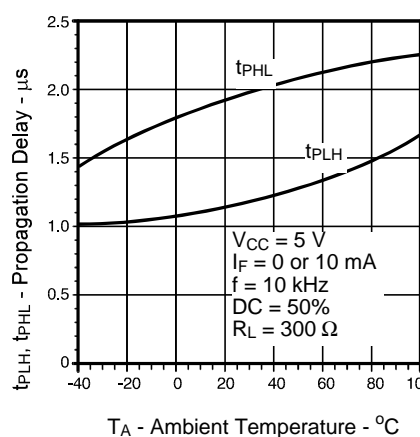
OPB686, OPB688 High Level Output Current vs Ambient Temperature



Low Level Output Voltage vs Ambient Temperature



OPB685, OPB686 Propagation Delay vs Ambient Temperature



OPB687, OPB688 Propagation Delay vs Ambient Temperature

