

MN101C273

Type	MN101C273		
ROM (×8-Bit)	4 K		
RAM (×8-Bit)	512		
Minimum Instruction Execution Time	0.25 μs (at 2.7 V to 5.5 V, 8 MHz) 1.00 μs (at 2.0 V to 5.5 V, 2 MHz)* * The lower limit for operation guarantee for EPROM built-in version is 2.7 V.		
Interrupts	• RESET • Watchdog • External 0 • External 1 • Timer 2 • Timer 3 • Serial 0 • A/D Conversion finish		
Timer Counter	Timer Counter 2 : 8-Bit × 1 (Square-Wave/8-Bit PWM Output, Event Count, Synchronous Output Event) Clock Source 1/1, 1/4 of System Clock, External Clock Input Interrupt Source Coincidence with Compare Register 2 Timer Counter 3 : 8-Bit × 1 (Square-Wave Output, Event Count, Generation of Remote Control Carrier, Serial 0 Baud Rate Timer) Clock Source 1/4, 1/16 of System Clock, 1/1 of OSC Oscillation Clock, External Clock Input Interrupt Source Coincidence with Compare Register 3 Timer Counter 2, 3 can be cascade-connected. Watchdog Timer Interrupt Source 1/1048576 of System Clock		
Serial Interface	Serial 0 : 8-Bit × 1 (Synchronous Type/Simple UART{Half-Duplex}) Clock Source 1/2, 1/4, 1/16 of System Clock 1/2 of Timer Counter 3		
I/O Pins	I/O	16	• Common use • Specified pull-up Resistor available • Input/Output selectable (bit unit)
	Input	6	• Common use • Specified pull-up Resistor available
A/D Inputs	10-Bit × 4ch (with S/H)		
Special Ports	Remote Control Carrier Signal Output, High-Current Drive Port		
Package	SDIP028-P-0400		

Electrical Characteristics

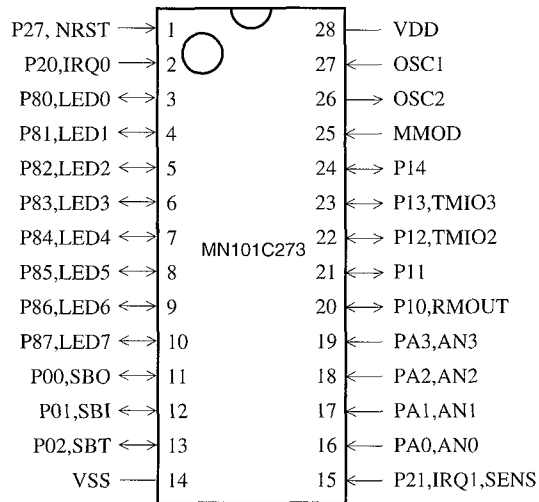
Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc = 8.39 MHz, VDD = 5 V			25	mA
Supply Current at HALT	IDD2	fosc = 8.39 MHz, VDD = 5 V			3	mA
Supply Current at STOP	IDD3	VDD = 5 V, Ta = 25 °C			2	μA
		VDD = 5 V, Ta = -40 °C to +85 °C			20	μA

Support Tool

In-Circuit Emulator	PX-ICE101C / D + PX-PRB101C27-28SDIP-C / D	
EPROM built-in Type	Type	MN101CP273
	ROM (× 8-Bit)	4 K
	RAM (× 8-Bit)	512
	Minimum Instruction Execution Time	0.25 μs (at 2.7 V to 5.5 V, 8 MHz)
	Package	SDIP028-P-0400 (under development)

Pin Assignment



SDIP028-P-0400