

**Product Features**

- Functionally compatible with FCT3, LVT, and 74 series 16827 families of products
- Tri-State outputs
- 5V Tolerant inputs and outputs
- 2.0V-3.6V Vcc supply operation
- Balanced sink and source output drives (24 mA)
- Low ground bounce outputs
- Supports live insertion
- ESD Protection exceeds 2000V, Human Body Model 200V, Machine Model
- Packages available:
  - 56-pin 240-mil wide plastic TSSOP (A)
  - 56-pin 300-mil wide plastic SSOP (V)

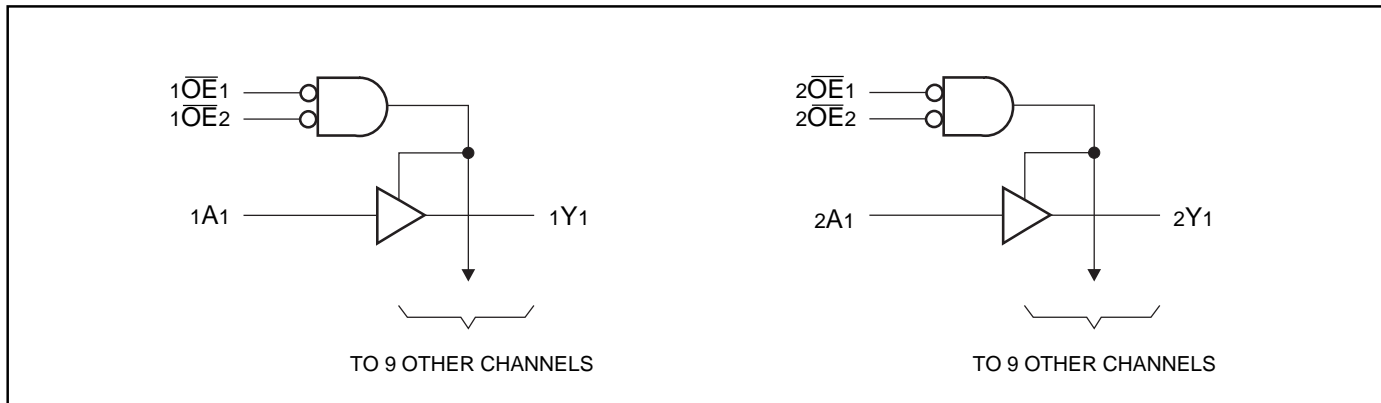
**Product Description**

Pericom Semiconductor’s PI74LCX series of logic circuits are produced in the Company’s advanced 0.6 micron CMOS technology, achieving industry leading speed grades.

The PI74LCX16827 is a 20-bit wide bus driver designed to provide buffering and high-performance bus interfacing for wide data/address paths or busses with parity. Two pairs of nanded output enable controls allow the device to be operated as two 10-bit buffers or as one 20-bit buffer. Signal pins are arranged in a flow-through organization for ease of layout and hysteresis is designed into all inputs to improve noise margin.

The PI74LCX16827 can be driven from either 3.3V or 5.0V devices allowing this device to be used as a translator in a mixed 3.3V/5.0V system.

**Logic Block Diagram**



### Product Pin Description

Pin Name	Description
$\overline{xOE_x}$	Output Enable Inputs (Active LOW)
$xAx$	Data Inputs
$xYx$	3-State Outputs

### Truth Table<sup>(1)</sup>

Inputs			Outputs
$\overline{xOE_1}$	$\overline{xOE_2}$	$xAx$	$xYx$
L	L	L	L
L	L	H	H
H	X	X	Z
X	H	X	Z

**Note:**

- H = High Voltage Level, X = Don't Care,  
L = Low Voltage Level, Z = High Impedance

### Product Pin Configuration

$\overline{1OE_1}$	1	56	$\overline{1OE_2}$
$1Y_1$	2	55	$1A_1$
$1Y_2$	3	54	$1A_2$
GND	4	53	GND
$1Y_3$	5	52	$1A_3$
$1Y_4$	6	51	$1A_4$
VCC	7	50	VCC
$1Y_5$	8	49	$1A_5$
$1Y_6$	9	48	$1A_6$
$1Y_7$	10	47	$1A_7$
GND	11	46	GND
$1Y_8$	12	45	$1A_8$
$1Y_9$	13	44	$1A_9$
$1Y_{10}$	14	43	$1A_{10}$
$2Y_1$	15	42	$2A_1$
$2Y_2$	16	41	$2A_2$
$2Y_3$	17	40	$2A_3$
GND	18	39	GND
$2Y_4$	19	38	$2A_4$
$2Y_5$	20	37	$2A_5$
$2Y_6$	21	36	$2A_6$
VCC	22	35	VCC
$2Y_7$	23	34	$2A_7$
$2Y_8$	24	33	$2A_8$
GND	25	32	GND
$2Y_9$	26	31	$2A_9$
$2Y_{10}$	27	30	$2A_{10}$
$\overline{2OE_1}$	28	29	$\overline{2OE_2}$

### Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....	-65°C to +150°C
Ambient Temperature with Power Applied .....	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only) .....	-0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only) ..	-0.5V to +7.0V
DC Input Voltage .....	-0.5V to +7.0V
DC Output Current .....	120 mA
Power Dissipation .....	1.0W

**Note:**

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### Recommended Operating Conditions

Symbol	Parameter	Min.	Max.	Units		
Vcc	Supply Voltage	Operating	2.0	3.6	V	
		Data Retention	1.5	3.6		
VI	Input Voltage	0	5.5			
Vo	Output Voltage	HIGH or LOW State	0	Vcc		
		TRI-State	0	5.5		
IOH/IOL	Output Current	Vcc = 3.0V-3.6V	—	±24		mA
		Vcc = 2.7V	—	±12		
TA	Free-Air Operating Temperature	-40	+85	°C		
Δt/ΔV	Input Edge Rate	V = 0.8V-2.0V, Vcc = 3.0V		0	10	ns/V

**DC Electrical Characteristics** (Over the Operating Range,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ ,  $V_{CC} = 2.7\text{V}$  to  $3.6\text{V}$ )

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ <sup>(2)</sup>	Max.	Units
$V_{IH}$	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	—	—	V
$V_{IL}$	Input LOW Voltage	Guaranteed Logic LOW Level		—	—	0.8	
$V_{OH}$	Output HIGH Voltage	$V_{CC} = 2.7\text{-}3.6$	$I_{OH} = -0.1\text{mA}$	$V_{CC}-0.2$	—	—	
		$V_{CC} = 2.7$	$I_{OH} = -12\text{mA}$	2.2	—	—	
		$V_{CC} = 3.0$	$I_{OH} = -18\text{mA}$	2.4	—	—	
			$I_{OH} = -24\text{mA}$	2.2	—	—	
$V_{OL}$	Output LOW Voltage	$V_{CC} = 2.7\text{-}3.6$	$I_{OL} = 0.1\text{mA}$	—	—	0.2	
		$V_{CC} = 2.7$	$I_{OL} = 12\text{mA}$	—	—	0.4	
		$V_{CC} = 3.0$	$I_{OL} = 16\text{mA}$	—	—	0.4	
			$I_{OL} = 24\text{mA}$	—	—	0.55	
$V_{IK}$	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$		—	-0.7	-1.2	
$I_I$	Input Leakage Current	$0 \leq V_I \leq 5.5\text{V}$	$V_{CC} = 2.7\text{-}3.6$	—	—	$\pm 5$	$\mu\text{A}$
$I_{OZ}$	Tri-State Output Leakage	$0 \leq V_O \leq 5.5\text{V}$ $V_I = V_{IH}$ or $V_{IL}$	$V_{CC} = 2.7\text{-}3.6$	—	—	$\pm 5$	
$I_{OFF}$	Power Down Disable	$V_{CC} = 0\text{V}, V_{IN}$ or $V_{OUT} \leq 5.5\text{V}$		—	—	10	
$I_{CC}$	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND}$ or $V_{CC}$	—	0.1	10	
$\Delta I_{CC}$	Quiescent Power Supply Current TTL Inputs HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = V_{CC} - 0.6\text{V}^{(3)}$	—	—	500	

**Notes:**

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at  $V_{CC} = 3.3\text{V}$ ,  $+25^\circ\text{C}$  ambient.
3. Per TTL driven input; all other inputs at  $V_{CC}$  or GND.

**Capacitance**

Parameters	Description	Test Conditions	Typ.	Units
$C_{IN}$	Input Capacitance	$V_{CC} = \text{Open}, V_I = 0\text{V}$ or $V_{CC}$	7	pF
$C_{OUT}$	Output Capacitance	$V_{CC} = 3.3\text{V}, V_I = 0\text{V}$ or $V_{CC}$	8	
$C_{PD}$	Power Dissipation Capacitance	$V_{CC} = 3.3\text{V}, V_I = 0\text{V}$ or $V_{CC}, F = 10\text{MHz}$	20	

**Switching Characteristics over Operating Range**

Parameters	Description	Conditions	V <sub>CC</sub> = 3.3V ± 0.3V		V <sub>CC</sub> = 2.7V		Units
			Min.	Max.	Min.	Max.	
t <sub>PHL</sub> t <sub>PHL</sub>	Propagation Delay D <sub>xx</sub> to O <sub>xx</sub>	C <sub>L</sub> = 50pF R <sub>L</sub> = 500Ω	1.5	5.0	1.5	5.6	ns
t <sub>PZH</sub> t <sub>PZH</sub>	Output Enable Time		1.5	8.0	1.5	8.6	
t <sub>PHZ</sub> t <sub>PLZ</sub>	Output Disable Time		1.5	6.0	1.5	6.3	
t <sub>SK(O)</sub>	Output Skew <sup>(1)</sup>		—	1.0	—	—	

**Note:**

1. Skew between any two outputs, of the same package, switching in the same direction.

**Dynamic Switching Characteristics (T<sub>A</sub> = +25°C)**

Parameters	Description	Test Conditions <sup>(1)</sup>	Typical	Units
V <sub>OLP</sub>	Dynamic LOW Peak Voltage	V <sub>CC</sub> = 3.3V, C <sub>L</sub> = 50pF V <sub>IH</sub> = 3.3V, V <sub>IL</sub> = 0V	0.8	V
V <sub>OLV</sub>	Dynamic LOW Valley Voltage	V <sub>CC</sub> = 3.3V, C <sub>L</sub> = 50pF V <sub>IH</sub> = 3.3V, V <sub>IL</sub> = 0V	0.8	

**Note:**

1. Measured with n-1 outputs switching from HIGH-to-LOW or LOW-to-HIGH. The remaining output is measured in the LOW state.