

Voltage comparator

LM111/211/311/ LM311B

DESCRIPTION

The LM111 series are voltage comparators that have input currents approximately a hundred times lower than devices like the $\mu\text{A}710$. They are designed to operate over a wider range of supply voltages; from standard $\pm 15\text{V}$ op amp supplies down to a single 3V supply. Their output is compatible with RTL, DTL, and TTL as well as MOS circuits. Further, they can drive lamps or relays, switching voltages up to 50V at currents as high as 50mA.

Both the inputs and the outputs of the LM111 series can be isolated from system ground, and the output can drive loads referred to ground, the positive supply, or the negative supply. Offset balancing and strobe capability are provided and outputs can be wire-ORed.

Although slower than the $\mu\text{A}710$ (200ns response time vs 40ns), the devices are also much less prone to spurious oscillations. The LM111 series has the same pin configuration as the $\mu\text{A}710$ series.

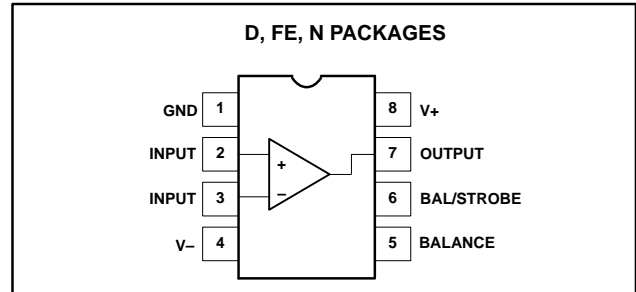
FEATURES

- Operates from single 3V supply (LM311B)
- Maximum input bias current: 150nA (LM311—250nA)
- Maximum offset current: 20nA (LM311—50nA)
- Differential input voltage range: $\pm 30\text{V}$
- Power consumption: 135mW at $\pm 15\text{V}$
- High sensitivity—200V/mV
- Zero crossing detector

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Dual In-Line Package (DIP)	-55°C to +125°C	LM111N	0404B
8-Pin Plastic Dual In-Line Package (DIP)	-25°C to +85°C	LM211N	0404B
8-Pin Plastic Small Outline Package (SO)	0 to +70°C	LM311D	0174C
8-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	LM311N	0404B
8-Pin Plastic Small Outline Package (SO)	-25°C to +85°C	LM211D	0174C
8-Pin Ceramic Dual In-Line Package (CERDIP)	-55°C to +125°C	LM111FE	0580A
8-Pin Plastic Small Outline Package (SO)	0 to +70°C	LM311BD	0174C
8-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	LM311BN	0404B

PIN CONFIGURATION



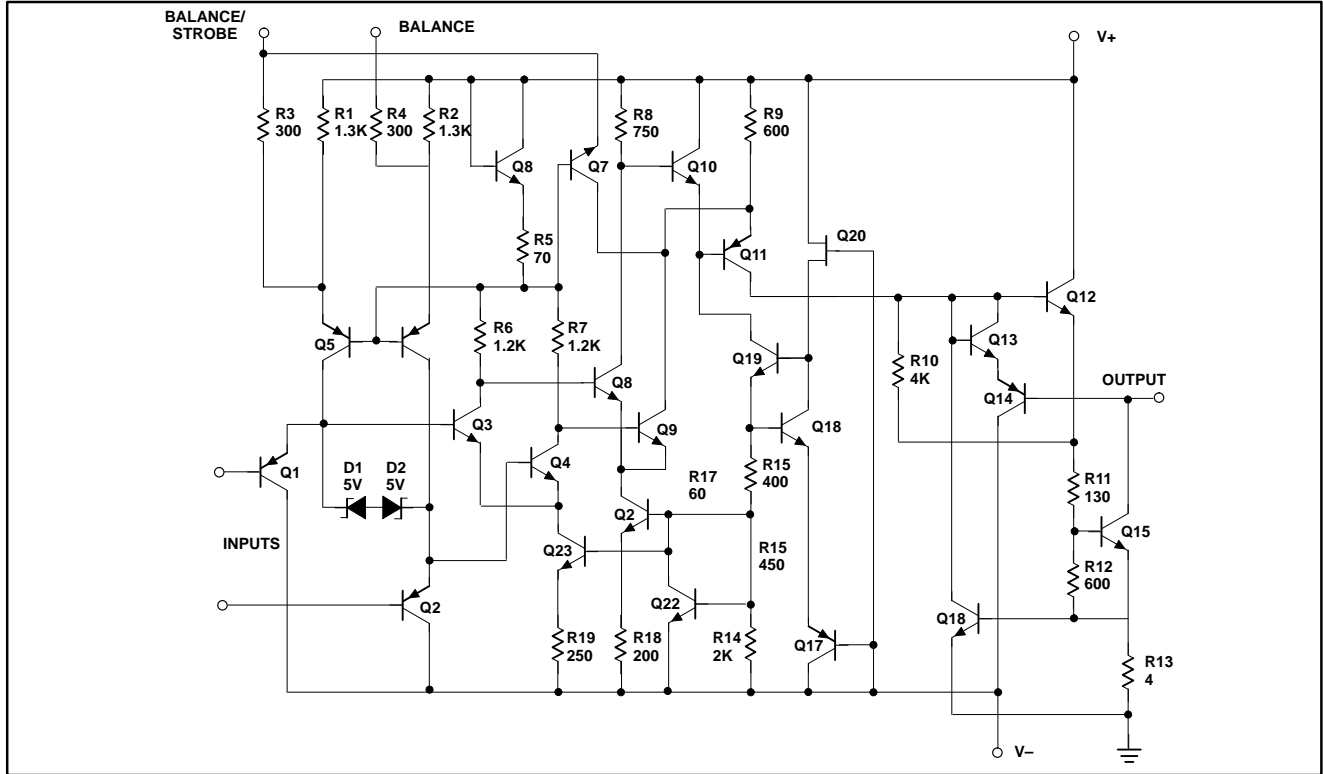
APPLICATIONS

- Precision squarer
- Positive/negative peak detector
- Low voltage adjustable reference supply
- Switching power amplifier

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EQUIVALENT SCHEMATIC



ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V_S	Total supply voltage	36	V
	Output to negative supply voltage: LM111/LM211	50	V
	LM311/LM311B	40	V
	Ground to negative supply voltage	30	V
	Differential input voltage	± 30	V
V_{IN}	Input voltage ¹	± 15	V
$P_{D MAX}$	Maximum power dissipation, $T_A=25^\circ\text{C}$ (still-air) ²		
	F package	810	mW
	N package	1190	mW
	D package	780	mW
I	Output short-circuit duration	10	sec
T_A	Operating ambient temperature range		
	LM111	-55 to +125	$^\circ\text{C}$
	LM211	-25 to +85	$^\circ\text{C}$
	LM311/LM311B	0 to +70	$^\circ\text{C}$
T_{STG}	Storage temperature range	-65 to +150	$^\circ\text{C}$
T_{SOLD}	Lead soldering temperature (10sec max)	300	$^\circ\text{C}$

NOTES:

1. This rating applies for $\pm 15\text{V}$ supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.
2. Derate above 25°C , at the following rates: F package at $6.4\text{mW}/^\circ\text{C}$; N package at $9.5\text{mW}/^\circ\text{C}$; D package at $6.2\text{mW}/^\circ\text{C}$

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LM111/211/311/
LM311B**DC ELECTRICAL CHARACTERISTICS^{1, 2, 3, 6}**

Over temperature range unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	LM111/LM211			LM311			LM311B			UNIT
			Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
V _{OS}	Input offset voltage ³	T _A =25°C, R _S ≤50kΩ		0.7	3.0		2.0	7.5		2.0	7.5	mV
I _{OS}	Input offset current ³	T _A =25°C		4.0	10		6.0	50		6	25	nA
I _{BIAS}	Input bias current	T _A =25°C		60	100		100	250		100	200	nA
A _V	Voltage gain	T _A =25°C		200			200			200		V/mV
	Response time ⁴	T _A =25°C		200			200			500		ns
V _{SAT}	Saturation voltage	LM111/211 V _{IN} ≤-5mV, I _{OUT} =50mA LM311/B V _{IN} ≤-10mV, I _{OUT} =50mA T _A =25°C		0.75	1.5		0.75	1.5		0.75	1.5	V
I _{BAL/STR}	Strobe on current	T _A =25°C		3.0			3.0			3.0		mA
I _{LEAKAGE}	Output leakage current ⁶	LM111/211 V _{IN} ≥5mV, V _{OUT} =35V LM311/B V _{IN} ≥10mV, V _{OUT} =35V T _A =25°C, I _{STROBE} =3mA (V ₋ = V _{GND} = -5V)		0.2	10		0.2	50		0.2	50	nA
V _{OS}	Input offset voltage ³	R _S ≤50kΩ			4.0			10			10	mV
I _{OS}	Input offset current ³				20			70			50	nA
I _{BIAS}	Input bias current				150			300			250	nA
V _{IN}	Input voltage range	V=±15V (Pin 7 may go to 5V)	-14.5	13.8 to -14.7	13.0	-14.5	13.8 to -14.7	13.0	V- +0.5		V+ -1.5	V
V _{OL}	Saturation voltage ⁶	V ₊ ≥ 4.5V, V ₋ = 0 LM111/211 V _{IN} ≤-6mV, I _{SINK} ≤8mA LM311/B V _{IN} ≤-10mV, I _{SINK} ≤8mA		0.23	0.4		0.23	0.4		0.23	0.4	V
I _{OH}	Output leakage current	V _{IN} ≥5mV, V _{OUT} =35V		0.1	0.5							μA
I _{CC}	Positive supply current	T _A =25°C		5.1	6.0		5.1	7.5		1.6	3.5	mA
I _{EE}	Negative supply current	T _A =25°C		4.1	5.0		4.1	5.0				mA

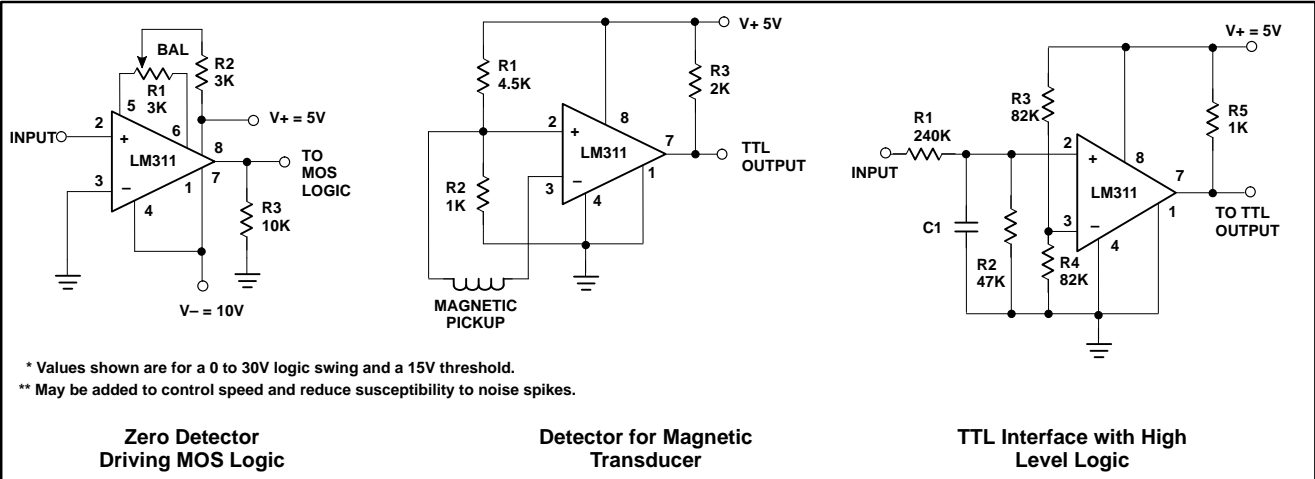
NOTES:

1. This rating applies for ±15V supplies. The positive input voltage limit is 30V above the negative supply. The negative input voltage limit is equal to the negative supply voltage or 30V below the positive supply, whichever is less.
2. These specifications apply for V_S=±15V and 0°C < T_A < 70°C unless otherwise specified. With the LM211, however, all temperature specifications are limited to -25°C ≤ T_A ≤ 85°C and for the LM111 is limited to -55°C T_A < 125°C. The offset voltage, offset current, and bias current specifications apply for any supply voltage from a single 5V supply up to ±15V supplies.
3. The offset voltages and offset currents given are the maximum values required to drive the output within a volt of either supply with 1mA load. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.
4. The response time specified is for a 100mV input step with 5mV over-drive.
5. Do not short the strobe pin to ground; it should be current driven at 3mA to 5mA.
6. LM311B, all parameters are at V₊ = 3V ±10%, V₋ = GND = 0V.

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TYPICAL APPLICATIONS



Zero Detector
Driving MOS Logic

Detector for Magnetic
Transducer

TTL Interface with High
Level Logic