Three quadrant triacs guaranteed commutation

BTA208 series D, E and F

GENERAL DESCRIPTION

QUICK REFERENCE DATA

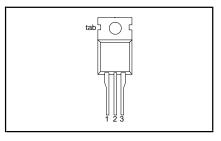
Passivated guaranteed commutation triacs in a plastic envelope intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

SYMBOL	PARAMETER	MAX.	MAX.	UNIT
	BTA208- BTA208- BTA208-	600D 600E 600F	- 800E 800F	
V_{DRM}	Repetitive peak	600	800	V
I _{T(RMS)} I _{TSM}	off-state voltages RMS on-state current Non-repetitive peak on-state current	8 65	8 65	A A

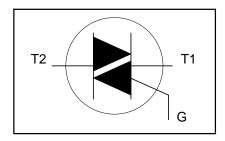
PINNING - TO220AB

PIN	DESCRIPTION		
1	main terminal 1		
2	main terminal 2		
3	gate		
tab	main terminal 2		

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
V_{DRM}	Repetitive peak off-state voltages		-	-600 600 ¹	-800 800	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 102 °C	-	8		Α
I _{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25$ °C prior to surge t = 20 ms	-	65		A
l ² t	l ² t for fusing	t = 16.7 ms t = 10 ms	-	72 2°		A^2 s
dl _T /dt	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 12 \text{ A}; I_G = 0.2 \text{ A}; \\ dI_G/dt = 0.2 \text{ A}/\mu\text{s}$		10	U	A/μs
I_{GM}	Peak gate current Peak gate voltage		-	2		A V
P _{GM} P _{G(AV)}	Peak gate power Average gate power	over any 20 ms	-	5 5 0.	5	ŴW
T_{stg}	Storage temperature Operating junction temperature	period	-40 -	15 12		Ç

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¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 $A/\mu s$.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th j-mb}$ $R_{th j-a}$	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle in free air	- - -	- - 60	2.0 2.4 -	K/W K/W K/W

STATIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.		MAX.		UNIT
		BTA208-		D	D	E	F	
I _{GT}	Gate trigger current ²	V _D = 12 V; I _T = 0.1 A T2+ G+ T2+ G- T2- G-	- - -	0.8 2.0 3.2	5 5 5	10 10 10	25 25 25	mA mA mA
I _L	Latching current	V _D = 12 V; I _{GT} = 0.1 A T2+ G+ T2+ G- T2- G-	- - -	7.2 8.2 11.0	15 25 25	25 30 30	30 40 40	mA mA mA
I _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	5.3	15	25	30	mA
					D, E, F	=		
V _T V _{GT}	On-state voltage Gate trigger voltage	$I_T = 10 \text{ A}$ $V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ $V_D = 400 \text{ V}; I_T = 0.1 \text{ A};$ $I_i = 125 \text{ °C}$	- - 0.25	1.3 0.7 0.4		1.65 1.5 -		V V V
I _D	Off-state leakage current	$V_{D} = V_{DRM(max)};$ $T_{j} = 125 \text{ °C}$	-	0.1		0.5		mA

DYNAMIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
		BTA208-	D	Е	F	D		
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)};$ $T_j = 110 ^{\circ}C;$ exponential waveform; gate open circuit	20	60	70	60	-	V/μs
dl _{com} /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 110 ^{\circ}\text{C};$ $I_{T(RMS)} = 8 \text{ A};$ $dV_{com}/dt = 20 \text{V}/\mu\text{s}; \text{ gate}$ open circuit	1.8	3.5	5	8.5	-	A/ms
dl _{com} /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 110 ^{\circ}\text{C};$ $I_{T(RMS)} = 8 \text{ A};$ $dV_{com}/dt = 0.1 \text{V}/\mu\text{s}; \text{ gate}$ open circuit	5	16	19	100	-	A/ms
			D, E, F					
t _{gt}	Gate controlled turn-on time	$I_{TM} = 12 \text{ A; } V_D = V_{DRM(max)}; $ $I_G = 0.1 \text{ A; } dI_G/dt = 5 \text{ A/}\mu s$	-	-	-	2	-	μs

² Device does not trigger in the T2-, G+ quadrant.

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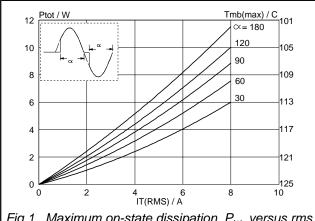


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where $\alpha =$ conduction angle.

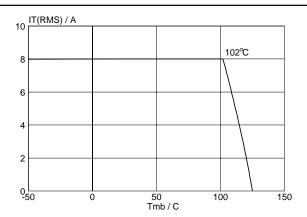


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

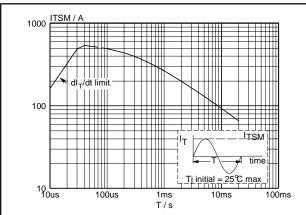


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 20$ ms.

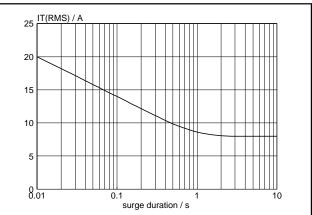


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 102 \,^{\circ}\text{C}$.

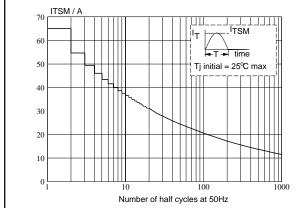


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

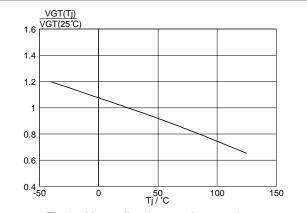
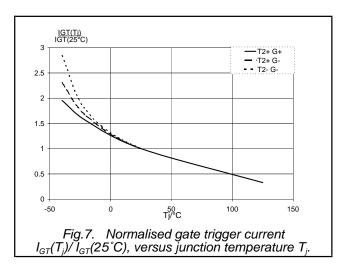


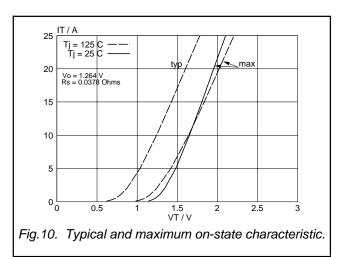
Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^{\circ}C)$, versus junction temperature T_i .

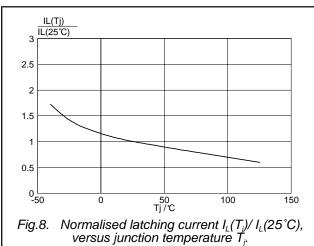
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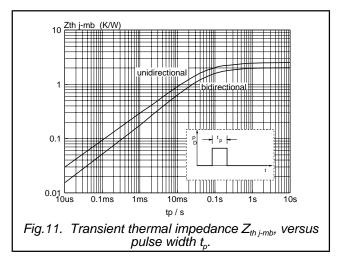
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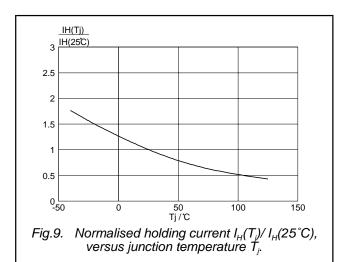
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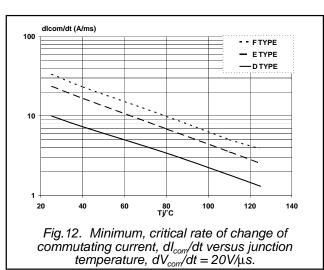








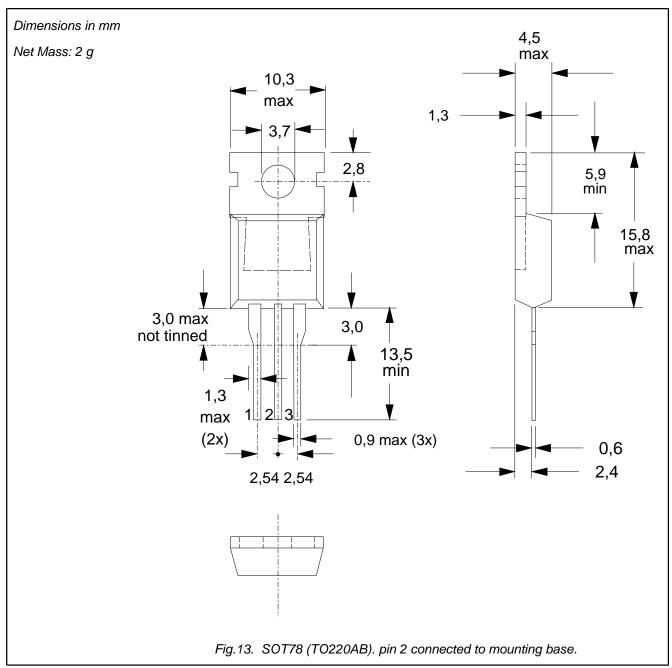




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MECHANICAL DATA



- Refer to mounting instructions for SOT78 (TO220) envelopes.
 Epoxy meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification This data sheet contains final product specifications.				
1.1				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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