# Three quadrant triacs guaranteed commutation

## BTA208B series D, E and F

## **GENERAL DESCRIPTION**

Glass passivated high commutation triacs in a plastic envelope suitable for surface mounting, intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series and "logic level" D series are intended for interfacing with low power drivers, including micro controllers.

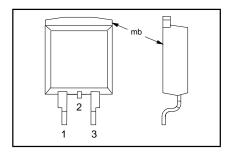
## **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
	BTA208B- BTA208B- BTA208B-	500D 500E 500F	600D 600E 600F	- 800E 800F	
$V_{DRM}$	Repetitive peak off-state	500	600	800	V
I <sub>T(RMS)</sub> I <sub>TSM</sub>	voltages RMS on-state current Non-repetitive peak on-state current	8 65	8 65	8 65	A A

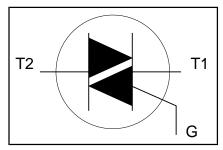
## **PINNING - SOT404**

PIN	DESCRIPTION				
1	main terminal 1				
2	main terminal 2				
3	gate				
mb	main terminal 2				

## **PIN CONFIGURATION**



## **SYMBOL**



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
$V_{DRM}$	Repetitive peak off-state voltages			<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	V
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave;	-		8		Α
I <sub>TSM</sub>	Non-repetitive peak on-state current	$T_{mb} \le 102$ °C full sine wave; $T_{j} = 25$ °C prior to surge t = 20 ms t = 16.7 ms	-		65 71		A A
l²t dl <sub>⊤</sub> /dt	I <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after	t = 10.7  ms t = 10  ms $I_{TM} = 12 \text{ A}; I_{G} = 0.2 \text{ A};$ $dI_{G}/dt = 0.2 \text{ A}/\mu\text{s}$	-		21 100		A <sup>2</sup> s A/μs
I <sub>GM</sub> V <sub>GM</sub> P <sub>GM</sub> P <sub>G(AV)</sub>	triggering Peak gate current Peak gate voltage Peak gate power Average gate power	over any 20 ms period	- - -		2 5 5 0.5		A V W
$T_{stg} \\ T_{j}$	Storage temperature Operating junction temperature		-40 -		150 125		္ခိုင

September 1997 1 Rev 1.000

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6  $A/\mu s$ .

# Three quadrant triacs guaranteed commutation

BTA208B series D, E and F

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{\text{th } j\text{-mb}}$ $R_{\text{th } j\text{-a}}$	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle minimum footprint, FR4 board		- - 55	2.0 2.4 -	K/W K/W K/W

## STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.		UNIT	
		BTA208B-			D	Е	F	
I <sub>GT</sub>	Gate trigger current <sup>2</sup>	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ $T2 + G + G - T2 + G - G - G - G - G - G - G - G - G - G$	2 2 2	- -	5 5 5	10 10	25 25	mA mA
I <sub>L</sub>	Latching current	$T2- G-V_D = 12 V;$ $I_{GT} = 0.1 A$ $T2+ G+$ $T2+ G-$	- -	- - -	6 9	10 12 18	25 30 45	mA mA mA
I <sub>H</sub> V <sub>T</sub>	Holding current On-state voltage	$T_{D} = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$ $I_{T} = 10 \text{ A}$	- - -	- - 1.3	6 6 1.65	12 12 1.65	30 30 1.65	mA mA V
V <sub>GT</sub>	Gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ $V_D = 400 \text{ V}; I_T = 0.1 \text{ A};$ $T_1 = 125 \text{ °C}$	- 0.25	0.7 0.4		1.5 -		V
I <sub>D</sub>	Off-state leakage current	$V_D = V_{DRM(max)};$ $T_j = 125 °C$	-	0.1		0.5		mA

## **DYNAMIC CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise stated

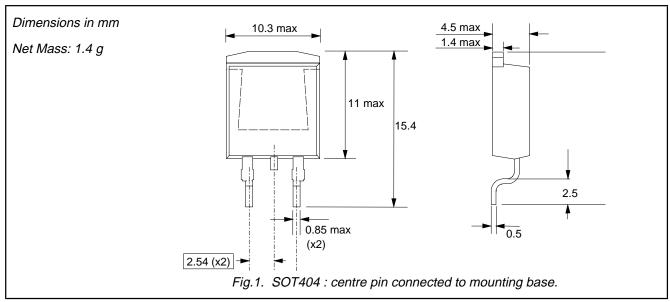
SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
		BTA208B-	D	Е	F			
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)};$ $T_j = 125 °C;$ exponential waveform; gate open circuit	10	20	50	-	-	V/μs
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C};$ $I_{T(RMS)} = 8 \text{ A};$ $dV_{com}/dt = 20v/\mu s; \text{ gate}$ open circuit	1.8	2.5	3.5	-	-	A/ms
t <sub>gt</sub>	Gate controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = V_{DRM(max)};$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu\text{s}$	-	-	-	2	-	μs

<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

# Three quadrant triacs guaranteed commutation

## BTA208B series D, E and F

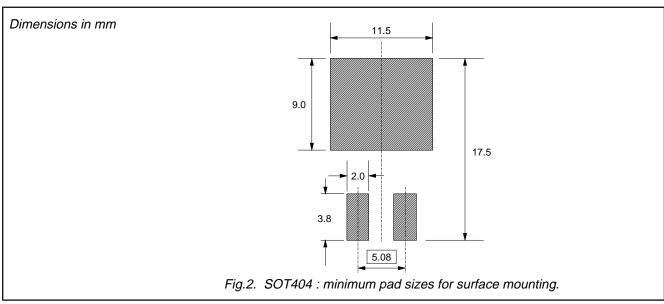
## **MECHANICAL DATA**



#### **Notes**

1. Epoxy meets UL94 V0 at 1/8".

## **MOUNTING INSTRUCTIONS**



## **Notes**

1. Plastic meets UL94 V0 at 1/8".

Philips Semiconductors Objective specification

# Three quadrant triacs guaranteed commutation

## BTA208B series D, E and F

#### **DEFINITIONS**

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting a contract	

## Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

## **Application information**

Where application information is given, it is advisory and does not form part of the specification.

## © Philips Electronics N.V. 1997

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, it is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent or other industrial or intellectual property rights.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.