BTA225B series C

GENERAL DESCRIPTION

Glass passivated high commutation triacs in a plastic envelope suitable for surface mounting, intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. These devices will commutate the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

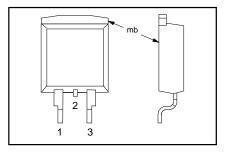
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V _{DRM} I _{T(RMS)} I _{TSM}	BTA225B- Repetitive peak off-state voltages RMS on-state current Non-repetitive peak on-state current	500C 500 25 180	600C 600 25 180	800C 800 25 180	V A A

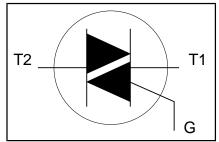
PINNING - SOT404

PIN	DESCRIPTION	
1	main terminal 1	
2	main terminal 2	
3	gate	
mb	main terminal 2	

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V_{DRM}	Repetitive peak off-state voltages		-	-500 500 ¹	-600 600 ¹	-800 800	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 91 ^{\circ}C$	-		25		А
I _{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25$ °C prior to surge					
		t = 20 ms t = 16.7 ms	-		190 209		A A
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after triggering	t = 10 ms $I_{TM} = 30 \text{ A}; I_{G} = 0.2 \text{ A};$ $dI_{G}/dt = 0.2 \text{ A}/\mu\text{s}$	-		180 100		A ² s A/μs
I _{GM} V _{GM} P _{GM}	Peak gate current Peak gate voltage Peak gate power		- - -		2 5 5		A V W
$ \begin{array}{c} P_{G(AV)}^{\text{G}} \\ T_{stg} \\ T_{j} \end{array} $	Average gate power Storage temperature Operating junction temperature	over any 20 ms period	-40 -		0.5 150 125		O, Ĉ

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¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ μ s.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb}	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle minimum footprint, FR4 board		- - 55	1.0 1.4 -	K/W K/W K/W

STATIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current ²	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$					
		T2+	+ G+	2	18	35	mA
		T2+	+ G-	2	21	35	mΑ
			- G-	2	34	35	mΑ
I _L	Latching current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$					
-		T2-	+ G+	-	-	20	mΑ
			+ G-	-	-	30	mΑ
			- G-	-	-	20	mΑ
I _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$		-	-	15	mΑ
V _T	On-state voltage	$I_{T} = 30 \text{ A}$		-	1.2	1.55	V
V_{GT}	Gate trigger voltage	$\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$		-	0.7	1.5	V
	1	$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_L = 125 ^{\circ}\text{C}$		0.25	0.4	-	V
I _D	Off-state leakage current	$V_D^{\prime} = V_{DRM(max)}$; $T_j = 125$ °C		-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

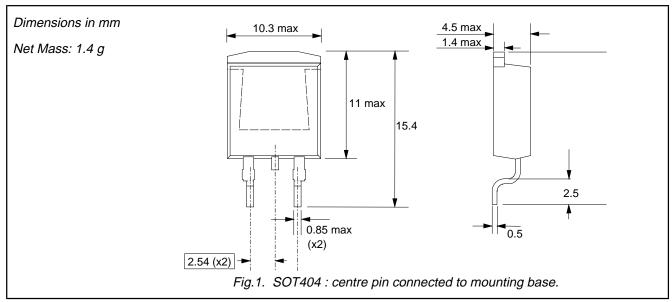
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	V _{DM} = 67% V _{DRM(max)} ; T _j = 125 °C; exponential waveform; gate open circuit	1000	-	V/µs
dl _{com} /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; T_j = 125 \text{ °C}; I_{T(RMS)} = 25 \text{ A}; without snubber; gate open circuit}$	3	14	A/ms
t _{gt}	Gate controlled turn-on time	$I_{TM} = 30 \text{ A}$; $V_D = V_{DRM(max)}$; $I_G = 0.1 \text{ A}$; $dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	μs

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² Device does not trigger in the T2-, G+ quadrant.

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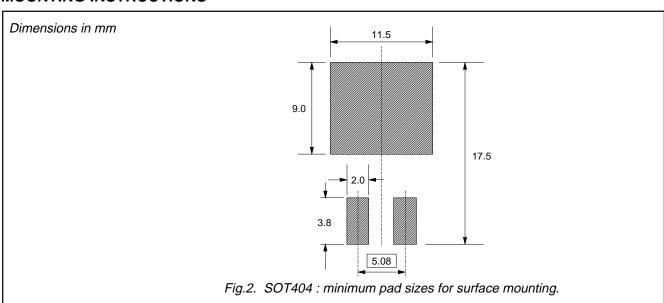
MECHANICAL DATA



Notes

1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS



Notes

1. Plastic meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status					
Objective specification	This data sheet contains target or goal specifications for product development.				
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.				
Product specification	This data sheet contains final product specifications.				
Limiting a control					

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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