Triacs BT137B series

GENERAL DESCRIPTION

Glass passivated triacs in a plastic envelope suitable for surface mounting, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

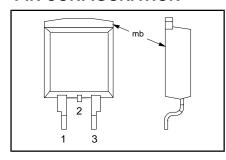
QUICK REFERENCE DATA

| SYMBOL | PARAMETER | MAX. | MAX. | MAX. | UNIT |
|---|---|---------------------|---------------------|---------------------|--------|
| | BT137B- BT137B- BT137B- | 500 500F 500G | 600 600F 600G | 800 800F 800G | |
| V_{DRM} | Repetitive peak off-state | 500 | 600 | 800 | V |
| I _{T(RMS)} I _{TSM} | voltages RMS on-state current Non-repetitive peak on-state current | 8 65 | 8 65 | 8 65 | A A |

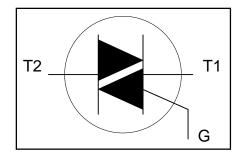
PINNING - SOT404

| PIN | DESCRIPTION |
|-----|-----------------|
| 1 | main terminal 1 |
| 2 | main terminal 2 |
| 3 | gate |
| mb | main terminal 2 |

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | | MAX. | | UNIT |
|---|---|--|---------------|---------------------------------|---------------------------------|--------------------|-----------------------|
| V_{DRM} | Repetitive peak off-state voltages | | - | -500 500 ¹ | -600 600 ¹ | -800 800 | V |
| I _{T(RMS)} | RMS on-state current Non-repetitive peak on-state current | full sine wave; $T_{mb} \le 102 ^{\circ}\text{C}$ full sine wave; $T_{j} = 25 ^{\circ}\text{C}$ prior to surge | - | | 8 | | A |
| | | t = 20 ms | - | | 65 | | A |
| l ² t | I ² t for fusing | t = 16.7 ms t = 10 ms | _ | | 71 21 | | A A ² s |
| dl _⊤ /dt | Repetitive rate of rise of on-state current after | $I_{TM} = 12 \text{ A}; I_G = 0.2 \text{ A}; \\ dI_G/dt = 0.2 \text{ A}/\mu\text{s}$ | | | | | 7.0 |
| | triggering | T2+ G+ | - | | 50 | | A/μs |
| | | T2+ G- T2- G- | _ | | 50 50 | | A/μs A/μs |
| | | T2- G+ | _ | | 10 | | A/μs |
| I _{GM} | Peak gate current | | - | | 2 | | À |
| V _{GM} | Peak gate voltage Peak gate power | | - | | 5 5 | | W W |
| P _{GM} P _{G(AV)} T _{stg} T _j | Average gate power Storage temperature Operating junction temperature | over any 20 ms period | - -40 - | | 0.5 150 125 | | O, O, |

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/µs.

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THERMAL RESISTANCES

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------------------|--|------------------------------|------|------|------------|------------|
| R _{th j-mb} | Thermal resistance junction to mounting base | full cycle half cycle | | | 2.0 2.4 | K/W K/W |
| R _{th j-a} | Thermal resistance junction to ambient | minimum footprint, FR4 board | - | 55 | - | K/W |

STATIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | | | UNIT |
|-----------------|--|--|------|------------|----------|-------------|----------|----------|
| | Coto tria con a compant | BT137B- | | | | F | G | |
| I _{GT} | Gate trigger current | $V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ T2+ G+ | - | 5 | 35 | 25 | 50 | mA |
| | | T2+ G- T2- G- | - | 8 11 | 35 35 | 25 25 | 50 50 | mA mA |
| | Latabina aurrant | T2- G+ | - | 30 | 70 | 70 | 100 | mA |
| l I∟ | Latching current | $V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$ T2+ G+ | - | 7 | 30 | 30 | 45 | mA |
| | | T2+ G- T2- G- | - | 16 5 | 45 30 | 45 30 | 60 45 | mA mA |
| | Holding current | T_2 - G+ $V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$ | - | 7 5 | 45 20 | 45 20 | 60 40 | mA mA |
| I'H | | 1 - | _ | | 20 | | 40 | ł 1 |
| V_{T} | On-state voltage Gate trigger voltage | $I_T = 10 \text{ A}$ $V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ | - | 1.3 0.7 | | 1.65 1.5 | | V |
| | | $V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; $ $T_i = 125 \text{ °C}$ | 0.25 | 0.4 | | - | | V |
| I _D | Off-state leakage current | $ \begin{vmatrix} V_D = V_{DRM(max)}; \\ T_j = 125 °C \end{vmatrix} $ | - | 0.1 | | 0.5 | | mA |

DYNAMIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

| SYMBOL | PARAMETER | CONDITIONS | | MIN. | | TYP. | MAX. | UNIT |
|-----------------------|--|---|---------|----------------|-----------------|------|--------|-------|
| STWIDGE | FAINAIVILILIX | COMPLITIONS | | IVIIIV. | | III. | IVIAA. | OIVII |
| dV _D /dt | Critical rate of rise of off-state voltage | BT137B- $V_{DM} = 67\% V_{DRM(max)};$ $T_i = 125 °C;$ exponential | 100 | F 50 | G 200 | 250 | - | V/μs |
| dV _{com} /dt | Critical rate of change of commutating voltage | waveform; gate open circuit $V_{DM} = 400 \text{ V}; T_j = 95 ^{\circ}\text{C};$ $I_{T(RMS)} = 8 \text{ A};$ $dI_{com}/dt = 3.6 \text{ A/ms};$ gate | - | - | 10 | 20 | - | V/µs |
| \mathbf{t}_{gt} | Gate controlled turn-on time | open circuit $I_{TM} = 12 \text{ A}; V_D = V_{DRM(max)};$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu \text{s}$ | - | - | - | 2 | - | μs |

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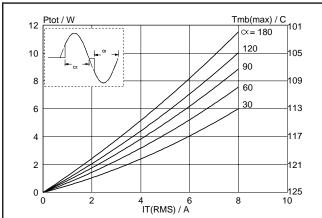


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where $\alpha =$ conduction angle.

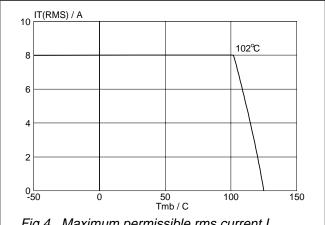


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

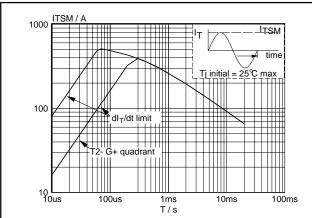


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 20$ ms.

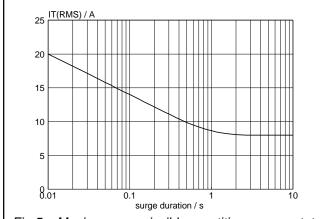


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 102$ °C.

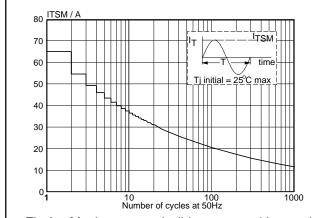


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

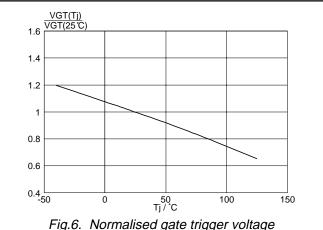
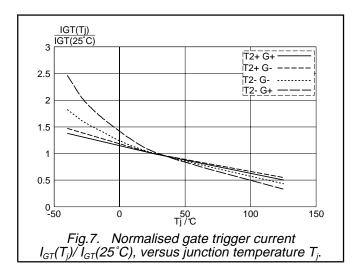
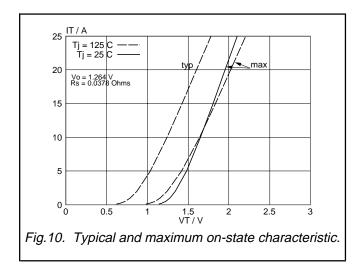
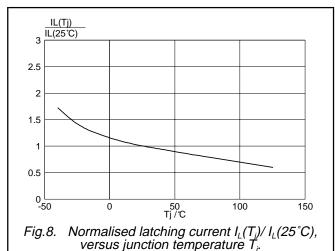


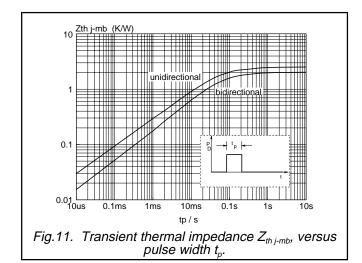
Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^{\circ}C)$, versus junction temperature T_j .

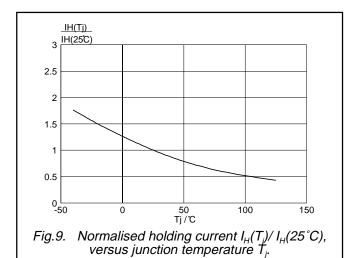
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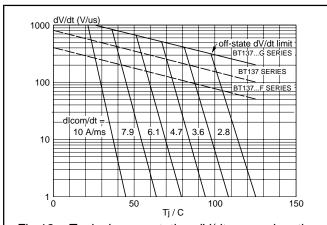
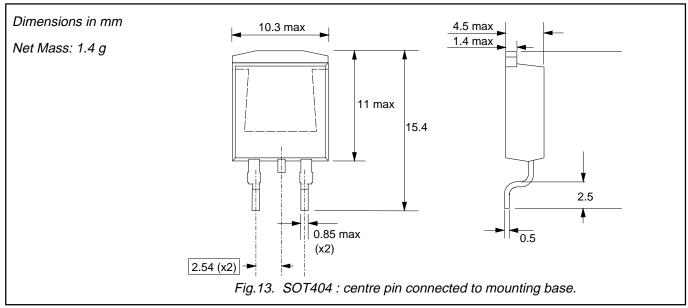


Fig.12. Typical commutation dV/dt versus junction temperature, parameter commutation dl_↑/dt. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dl_↑/dt.

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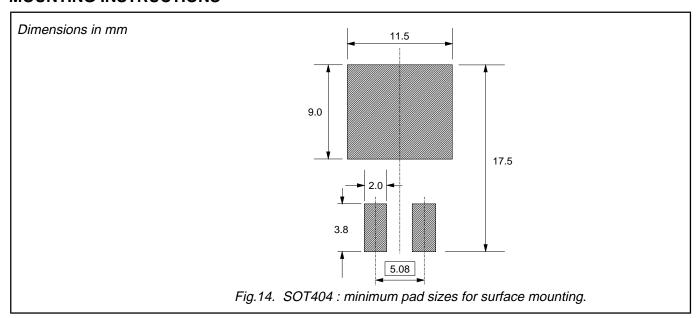
MECHANICAL DATA



Notes

1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS



Notes

1. Plastic meets UL94 V0 at 1/8".

Triacs BT137B series

DEFINITIONS

| Data sheet status | | | | | | |
|---|--|--|--|--|--|--|
| This data sheet contains target or goal specifications for product development. | | | | | | |
| This data sheet contains preliminary data; supplementary data may be published later. | | | | | | |
| This data sheet contains final product specifications. | | | | | | |
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Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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