## GENERAL DESCRIPTION

High-voltage, high-speed planar-passivated npn power switching transistor in TO220AB envelope intended for use in high frequency electronic lighting ballast applications, converters, inverters, switching regulators, motor control systems, etc.

## QUICK REFERENCE DATA

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
| :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\text {CESM }}$ | Collector-emitter voltage peak value | $\mathrm{V}_{\mathrm{BE}}=0 \mathrm{~V}$ | - | 1000 | V |
| $\mathrm{~V}_{\text {CBO }}$ | Collector-Base voltage (open emitter) |  | - | 1000 | V |
| $\mathrm{~V}_{\text {CEO }}$ | Collector-emitter voltage (open base) |  | - | 500 | V |
| $\mathrm{I}_{\mathrm{C}}$ | Collector current (DC) |  | - | 6 | A |
| $\mathrm{I}_{\mathrm{IM}}$ | Collector current peak value |  | - | 10 | A |
| $\mathrm{P}_{\text {tot }}$ | Total power dissipation | $\mathrm{T}_{\text {mb }} \leq 25{ }^{\circ} \mathrm{C}$ | 100 | W |  |
| $\mathrm{~V}_{\text {CEsat }}$ | Collector-emitter saturation voltage | $\mathrm{I}_{\mathrm{C}}=4.0 \mathrm{~A} ; \mathrm{I}_{\mathrm{B}}=0.8 \mathrm{~A}$ | 0.3 | 1.0 | V |
| $\mathrm{~h}_{\text {FESAT }}$ |  | $\mathrm{I}_{\mathrm{C}}=4.0 \mathrm{~A} ; \mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}$ | 11 | 15 |  |
| $\mathrm{t}_{\mathrm{f}}$ | Fall time (Inductive) | $\mathrm{I}_{\mathrm{C}}=5.0 \mathrm{~A} ; \mathrm{I}_{\mathrm{B} 1}=1.0 \mathrm{~A}$ | 25 | 50 | ns |

PINNING - TO220AB

| PIN | DESCRIPTION |
| :---: | :--- |
| 1 | base |
| 2 | collector |
| 3 | emitter |
| tab | collector |

PIN CONFIGURATION


SYMBOL


## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
| :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\text {CESM }}$ | Collector to emitter voltage | $\mathrm{V}_{\mathrm{BE}}=0 \mathrm{~V}$ | - | 1000 | V |
| $\mathrm{~V}_{\text {CEO }}$ | Collector to emitter voltage (open base) |  | - | 500 | V |
| $\mathrm{~V}_{\text {CBO }}$ | Collector to base voltage (open emitter) |  | - | 1000 | V |
| $\mathrm{I}_{\mathrm{C}}$ | Collector current (DC) |  | - | 6 | A |
| $\mathrm{I}_{\mathrm{CM}}$ | Collector current peak value |  | - | 10 | A |
| $\mathrm{I}_{\mathrm{B}}$ | Base current (DC) |  | - | 3 | A |
| $\mathrm{I}_{\mathrm{BM}}$ | Base current peak value |  | - | 6 | A |
| $\mathrm{P}_{\text {tot }}$ | Total power dissipation | $\mathrm{T}_{\text {mb }} \leq 25{ }^{\circ} \mathrm{C}$ | - | 100 | W |
| $\mathrm{~T}_{\text {stg }}$ | Storage temperature |  | -65 | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{j}}$ | Junction temperature |  | - | 150 | ${ }^{\circ} \mathrm{C}$ |

## THERMAL RESISTANCES

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
| :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{R}_{\mathrm{th} j-\mathrm{mb}}$ | Junction to mounting base |  | - | 1.25 | $\mathrm{~K} / \mathrm{W}$ |
| $\mathrm{R}_{\mathrm{th} \mathrm{j}-\mathrm{a}}$ | Junction to ambient | in free air | 60 | - | $\mathrm{K} / \mathrm{W}$ |

## STATIC CHARACTERISTICS

$\mathrm{T}_{\mathrm{mb}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {CES }}, \mathrm{I}_{\text {CBo }}$ | Collector cut-off current ${ }^{1}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{BE}}=0 \mathrm{~V} ; \mathrm{V}_{\mathrm{CE}}=\mathrm{V}_{\mathrm{CESMmax}} \\ & \mathrm{~V}_{\mathrm{BE}}=0 \mathrm{~V} ; \mathrm{V}_{\mathrm{CE}}=\mathrm{V}_{\mathrm{CESMmax}} ; \end{aligned}$ | - | - | 0.2 0.5 | $\underset{\mathrm{mA}}{\mathrm{~mA}}$ |
| $\mathrm{I}_{\text {ceo }}$ | Collector cut-off current Emitter | $V_{\text {CEO }}=V_{\text {CEOMAX }}(500 \mathrm{~V})$ | - | - | 0.1 | mA |
| Ebo | cut-off current | $V_{E B}=9 \mathrm{~V} ; \mathrm{I}_{\mathrm{C}}=0 \mathrm{~A}$. | 500 | - | 1 | mA |
| $\mathrm{V}_{\text {CEOsust }}$ | Collector-emitter sustaining voltage | $\begin{aligned} & I_{B}=0 A ; I_{C}=10 \mathrm{~mA} ; \\ & \mathrm{L}=25 \mathrm{mH} \end{aligned}$ | 500 | - | - | V |
| $\mathrm{V}_{\text {CEsat }}$ | Collector-emitter saturation voltage | $\mathrm{I}_{\mathrm{C}}=4 \mathrm{~A} ; \mathrm{I}_{\mathrm{B}}=0.8 \mathrm{~A}$ | - | 0.3 | 1.0 | V |
| $V_{\text {BEsat }}$ | Base-emitter saturation voltage | $\mathrm{I}_{\mathrm{C}}=4 \mathrm{~A} ; \mathrm{I}_{\mathrm{B}}=0.8 \mathrm{~A}$ | - | 1.0 | 1.3 | V |
| $\mathrm{h}_{\text {FE }}$ | DC current gain | $\mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA} ; \mathrm{V}_{C E}=5 \mathrm{~V}$ | 10 | 17 | 34 |  |
| $\mathrm{h}_{\text {FE }}$ |  | $\mathrm{I}_{\mathrm{C}}=500 \mathrm{~mA} ; \mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}$ | 14 | 22 | 35 |  |
| $\mathrm{h}_{\text {FESAT }}$ |  | $\mathrm{I}_{\mathrm{C}}=4 \mathrm{~A} ; \mathrm{V}_{\text {CE }}=5 \mathrm{~V}$ | 8 | 11 | 15 |  |

## DYNAMIC CHARACTERISTICS

$\mathrm{T}_{\mathrm{mb}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Switching times (resistive load) | $\begin{aligned} & \mathrm{I}_{\text {Con }}=5.0 \mathrm{~A} ; \mathrm{I}_{\text {Bon }}=-\mathrm{I}_{\text {Boff }}=1.0 \mathrm{~A} ; \\ & \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohms} ; \mathrm{V}_{\mathrm{BB} 2}=4 \mathrm{~V} ; \end{aligned}$ |  |  |  |
| $\mathrm{t}_{\text {on }}$ | Turn-on time |  | 0.8 | 1.0 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\text {s }}$ | Turn-off storage time |  | 2.1 | 3.0 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\mathrm{f}}$ | Turn-off fall time |  | 375 | 527 | ns |
|  | Switching times (inductive load) | $\begin{aligned} & \mathrm{I}_{\mathrm{Con}}=5.0 \mathrm{~A} ; \mathrm{I}_{\mathrm{Bon}}=1.0 \mathrm{~A} ; \mathrm{L}_{\mathrm{B}}=1 \mu \mathrm{H} ; \\ & -\mathrm{V}_{\mathrm{BR}}=5 \mathrm{~V} \end{aligned}$ |  |  |  |
| $\mathrm{t}_{\text {s }}$ | Turn-off storage time |  | 1.45 | 1.7 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\mathrm{f}}$ | Turn-off fall time |  | 25 | 50 | ns |
|  | Switching times (inductive load) | $\begin{aligned} & I_{C o n}=5.0 \mathrm{~A} ; \mathrm{I}_{\text {Bon }}=1.0 \mathrm{~A} ; \mathrm{L}_{\mathrm{B}}=1 \mu \mathrm{H} ; \\ & -\mathrm{V}^{\circ}=5 \mathrm{~V} \cdot \mathrm{~T}=100^{\circ} \mathrm{C} \end{aligned}$ |  |  |  |
| $\mathrm{t}_{\mathrm{s}}$ | Turn-off storage time |  | 1.64 | 2.2 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\mathrm{f}}$ | Turn-off fall time |  | 40 | 100 | ns |



Fig.1. Test circuit for $V_{C E O s u s t}$.


Fig.2. Oscilloscope display for $V_{\text {CEOsust }}$.


Fig.3. Test circuit resistive load. $V_{I M}=-6$ to +8 V $V_{C C}=250 \mathrm{~V} ; t_{p}=20 \mu \mathrm{~s} ; \delta=t_{p} / T=0.01$.
$R_{B}$ and $R_{L}$ calculated from $I_{\text {Con }}$ and $I_{\text {Bon }}$ requirements.


Fig.4. Switching times waveforms with resistive load.


Fig.6. Switching times waveforms with inductive load.


Fig.7. Normalised power dissipation. $P D \%=100 \cdot P D / P D_{25^{\circ} \mathrm{C}}=f\left(T_{h s}\right)$


Fig.8. Typical DC current gain. $h_{\mathrm{FE}}=f\left(I_{\mathrm{C}}\right)$ parameter $V_{\text {CE }}$


Fig.9. Collector-Emitter saturation voltage.
Solid lines = typ values, $V_{\text {CEsat }}=f(I B) ; T_{j}=25^{\circ} \mathrm{C}$.


Fig.10. Base-Emitter saturation voltage. Solid lines = typ values, $V_{B E s a t}=f(I C)$; at $I C / I B=4$.


Fig.11. Collector-Emitter saturation voltage. Solid lines = typ values, $V_{C E s a t}=f(I C)$; at $I C / I B=4$.


## Silicon Diffused Power Transistor



Fig.13. Reverse bias safe operating area ( $T_{j}<T_{\text {jmax }}$ ) for $-V_{b e}=5 \mathrm{~V}, 3 \mathrm{~V} \& 1 \mathrm{~V}$


Fig.14. Test circuit for reverse bias safe operating area.
$V_{\text {clamp }}<1100 \mathrm{~V} ; V_{c c}=150 \mathrm{~V} ;-V_{\text {be }}=5 \mathrm{~V}, 3 \mathrm{~V} \& 1 \mathrm{~V}$; $L_{B}=1 \mu H ; L_{C}=200 \mu H$

## Silicon Diffused Power Transistor

## MECHANICAL DATA



Fig.15. TO220AB; pin 2 connected to mounting base.

## Notes

1. Refer to mounting instructions for TO220 envelopes.
2. Epoxy meets UL94 V0 at 1/8".

## Silicon Diffused Power Transistor

## DEFINITIONS

| Data sheet status |  |
| :--- | :--- |
| Objective specification | This data sheet contains target or goal specifications for product development. |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification | This data sheet contains final product specifications. |
| Limiting values |  |
| Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one <br> or more of the limititing values may cause permanent damage to the device. These are stress ratings only and <br> operation of the device at these or at any other conditions above those given in the Characteristics sections of <br> othis specification is not implied. Exposure to limiting values for extended periods may affect device reliability. |  |
| Application information |  |
| Where application information is given, it is advisory and does not form part of the specification. |  |
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