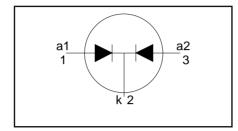
# PBYL3025CT, PBYL3025CTB series

### **FEATURES**

- Low forward volt drop
- Fast switching
- · Reverse surge capability
- High thermal cycling performance
- Low thermal resistance

### **SYMBOL**



### QUICK REFERENCE DATA

$$V_R = 20 \text{ V}/ 25 \text{ V}$$
 $I_{O(AV)} = 30 \text{ A}$ 
 $V_F \le 0.43 \text{ V}$ 

### **GENERAL DESCRIPTION**

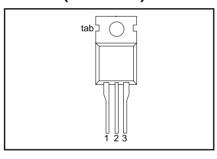
Dual schottky rectifier diodes intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYL3025CT series is supplied in the SOT78 (TO220AB) conventional leaded package. The PBYL3025CTB series is supplied in the SOT404 surface mounting package.

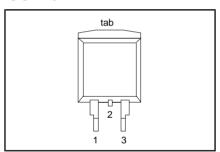
### **PINNING**

DESCRIPTION		
gate		
drain <sup>1</sup>		
source		
drain		

# **SOT78 (TO220AB)**



## **SOT404**



### **LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
		PBYL30 PBYL30		20CT 20CTB	25CT 25CTB	
$V_{RRM}$	Peak repetitive reverse voltage		-	20	25	V
$V_{RWM}$	Working peak reverse voltage		-	20	25	V
$V_R$	Continuous reverse voltage	T <sub>mb</sub> ≤ 120 °C	-	20	25	V
I <sub>O(AV)</sub>	Average rectified output current (both diodes conducting)	square wave; $\delta = 0.5$ ; $T_{mb} \le 123$ °C	-	30		А
I <sub>FRM</sub>	Repetitive peak forward current per diode	square wave; $\delta = 0.5$ ; $T_{mb} \le 123$ °C	-	30		Α
I <sub>FSM</sub>	Non-repetitive peak forward current per diode	t = 10  ms t = 8.3  ms sinusoidal; $T_j = 125 ^{\circ}\text{C}$ prior to surge; with reapplied $V_{\text{RRM(max)}}$	-		35 50	A A
I <sub>RRM</sub>	Peak repetitive reverse surge current per diode	pulse width and repetition rate limited by T <sub>i max</sub>	-	1		Α
T <sub>j</sub>	Operating junction temperature	Jillax	-	150		°C
T <sub>stg</sub>	Storage temperature		- 65	175		°C

<sup>1.</sup> It is not possible to make connection to pin 2 of the SOT404 package.

# PBYL3025CT, PBYL3025CTB series

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
		per diode	-	-	2	K/W
	to mounting base	both diodes	-	-	1.5	K/W
$R_{th j-a}$	Thermal resistance junction	SOT78 package, in free air	-	60	-	K/W
	to ambient	SOT404 package, pcb mounted, minimum footprint, FR4 board	-	50	-	K/W

## **ELECTRICAL CHARACTERISTICS**

All characteristics are per diode at T<sub>i</sub> = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{F}$	Forward voltage	I <sub>F</sub> = 15 A; T <sub>i</sub> = 150°C I <sub>F</sub> = 15 A; T <sub>i</sub> = 125°C	-	0.35	0.43	V
		I <sub>F</sub> = 15 A; T <sub>i</sub> = 125°C	-	0.38	0.46	V
		$I_{\rm F} = 30 \text{ A}; T_{\rm i} = 125^{\circ}\text{C}$	-	0.52	0.6	V
		$I_{\rm F} = 30  {\rm A}$	-	0.6	0.67	V
I <sub>R</sub>	Reverse current	$\dot{V}_R = V_{RWM}$	-	1	5	mΑ
		$IV_{P} = V_{PMM}$ : $T_{i} = 100^{\circ}C$	-	22	40	mΑ
$C_d$	Junction capacitance	$V_{R} = 5 \text{ V}; \text{ f} = 1 \text{ MHz}, T_{j} = 25 ^{\circ}\text{C} \text{ to } 125 ^{\circ}\text{C}$	-	700	-	pF

# PBYL3025CT, PBYL3025CTB series

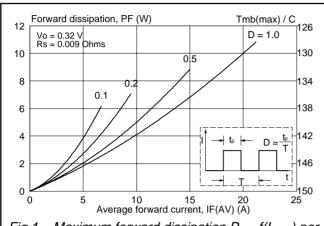


Fig.1. Maximum forward dissipation  $P_F = f(I_{F(AV)})$  per diode; square current waveform where  $I_{F(AV)} = I_{F(RMS)} x \sqrt{D}$ .

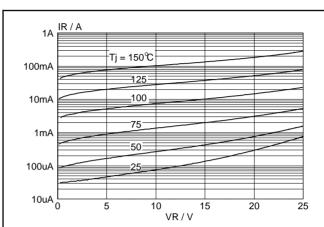


Fig.4. Typical reverse leakage current per diode;  $I_R = f(V_R)$ ; parameter  $T_j$ 

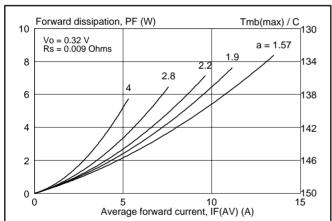


Fig.2. Maximum forward dissipation  $P_F = f(I_{F(AV)})$  per diode; sinusoidal current waveform where a = form factor  $= I_{F(RMS)} / I_{F(AV)}$ .

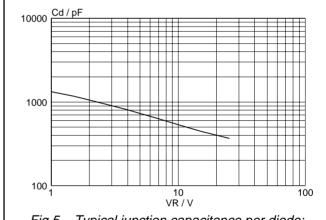


Fig.5. Typical junction capacitance per diode;  $C_d = f(V_R)$ ; f = 1 MHz;  $T_j = 25$ °C to 125°C.

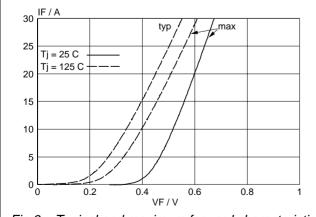


Fig.3. Typical and maximum forward characteristic  $I_F = f(V_F)$ ; parameter  $T_i$ 

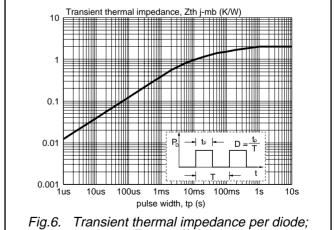
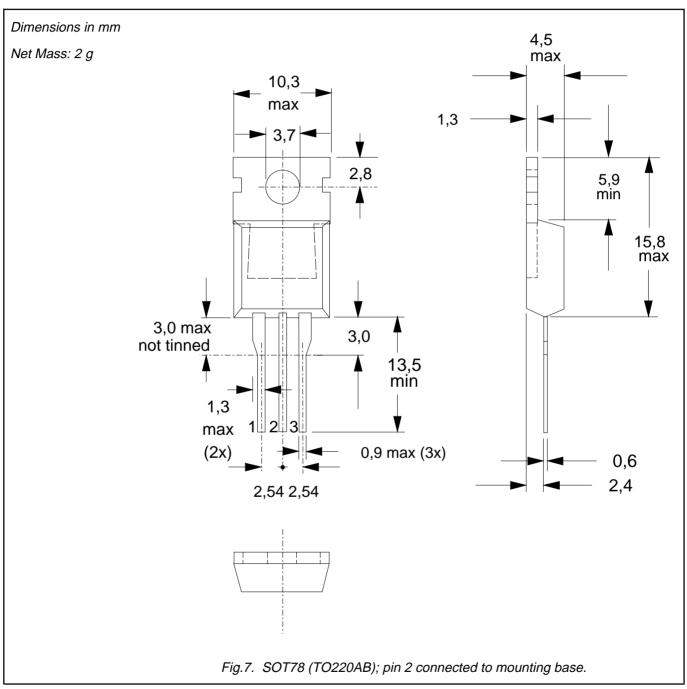


Fig.6. Transient thermal impedance per diode;  $Z_{th j-mb} = f(t_p)$ .

# PBYL3025CT, PBYL3025CTB series

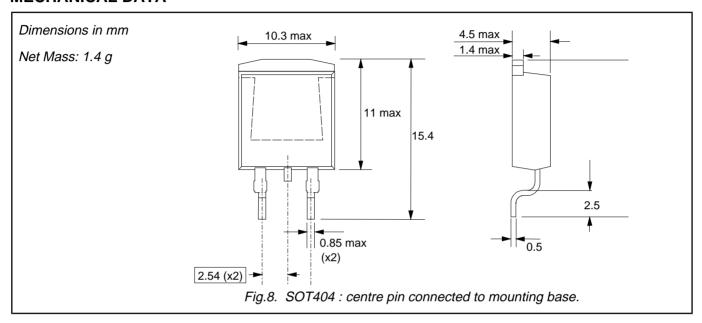
### **MECHANICAL DATA**



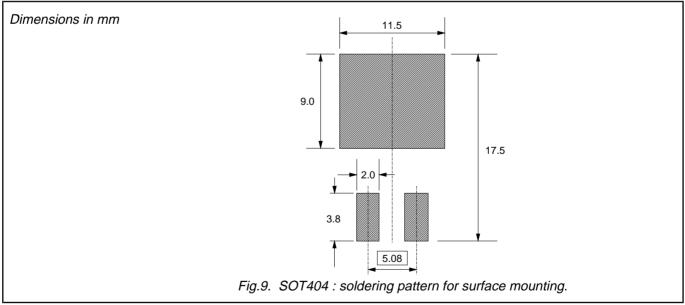
- Notes
  1. Refer to mounting instructions for SOT78 (TO220) envelopes.
  2. Epoxy meets UL94 V0 at 1/8".

# PBYL3025CT, PBYL3025CTB series

### **MECHANICAL DATA**



### **MOUNTING INSTRUCTIONS**



### **Notes**

1. Epoxy meets UL94 V0 at 1/8".

Philips Semiconductors Product specification

# Rectifier diodes Schottky barrier

# PBYL3025CT, PBYL3025CTB series

#### **DEFINITIONS**

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification This data sheet contains preliminary data; supplementary data may be published la				
Product specification This data sheet contains final product specifications.				
Limiting values				

### **Limiting values**

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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