

# DATA SHEET

**BFR93AW**

**NPN 5 GHz wideband transistor**

Product specification  
Supersedes data of November 1992  
File under Discrete Semiconductors, SC14

1995 Sep 18

# NPN 5 GHz wideband transistor

# BFR93AW

### FEATURES

- High power gain
- Gold metallization ensures excellent reliability
- SOT323 (S-mini) package.

### APPLICATIONS

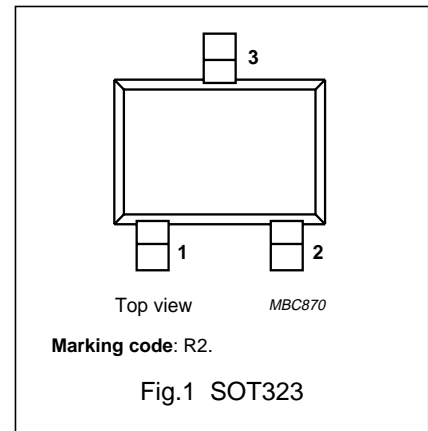
It is designed for use in RF amplifiers, mixers and oscillators with signal frequencies up to 1 GHz.

### DESCRIPTION

Silicon NPN transistor encapsulated in a plastic SOT323 (S-mini) package. The BFR93AW uses the same crystal as the SOT23 version, BFR93A.

### PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



### QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	–	–	15	V
$V_{CEO}$	collector-emitter voltage	open base	–	–	12	V
$I_C$	collector current (DC)		–	–	35	mA
$P_{tot}$	total power dissipation	up to $T_s = 93\text{ °C}$ ; note 1	–	–	300	mW
$h_{FE}$	DC current gain	$I_C = 30\text{ mA}$ ; $V_{CE} = 5\text{ V}$	40	90	–	
$C_{re}$	feedback capacitance	$I_C = 0$ ; $V_{CE} = 5\text{ V}$ ; $f = 1\text{ MHz}$ ; $T_{amb} = 25\text{ °C}$	–	0.6	–	pF
$f_T$	transition frequency	$I_C = 30\text{ mA}$ ; $V_{CE} = 5\text{ V}$ ; $f = 500\text{ MHz}$	4	5	–	GHz
$G_{UM}$	maximum unilateral power gain	$I_C = 30\text{ mA}$ ; $V_{CE} = 8\text{ V}$ ; $f = 1\text{ GHz}$ ; $T_{amb} = 25\text{ °C}$	–	13	–	dB
		$I_C = 30\text{ mA}$ ; $V_{CE} = 8\text{ V}$ ; $f = 2\text{ GHz}$ ; $T_{amb} = 25\text{ °C}$	–	8	–	dB
F	noise figure	$I_C = 5\text{ mA}$ ; $V_{CE} = 8\text{ V}$ ; $f = 1\text{ GHz}$ ; $\Gamma_s = \Gamma_{opt}$	–	1.5	–	dB
$T_j$	junction temperature		–	–	150	°C

### Note

1.  $T_s$  is the temperature at the soldering point of the collector pin.

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**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

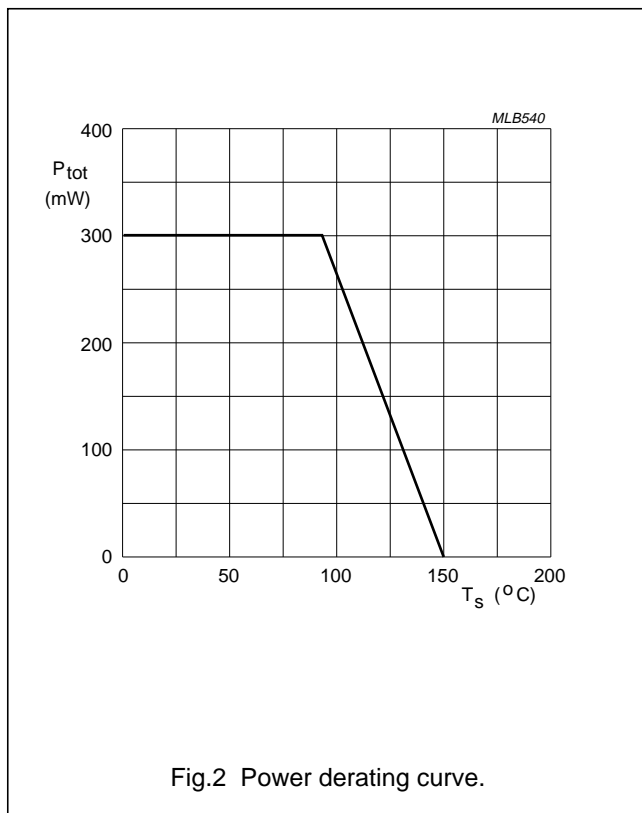
SYMBOL	PARAMETER	CONDITION	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	–	15	V
V <sub>CEO</sub>	collector-emitter voltage	open base	–	12	V
V <sub>EBO</sub>	emitter-base voltage	open collector	–	2	V
I <sub>C</sub>	collector current (DC)		–	35	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>s</sub> = 93 °C; see Fig.2; note 1	–	300	mW
T <sub>stg</sub>	storage temperature		–65	+150	°C
T <sub>j</sub>	junction temperature		–	150	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITION	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	up to T <sub>s</sub> = 93 °C; note 1	190	K/W

**Note to the Limiting values and Thermal characteristics**

1. T<sub>s</sub> is the temperature at the soldering point of the collector pin.



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**CHARACTERISTICS**

$T_j = 25\text{ °C}$  (unless otherwise specified).

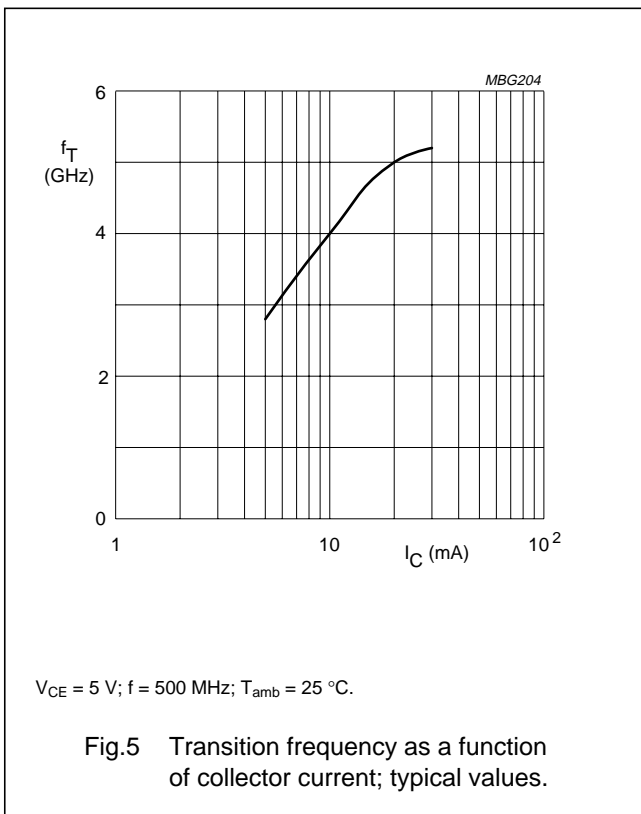
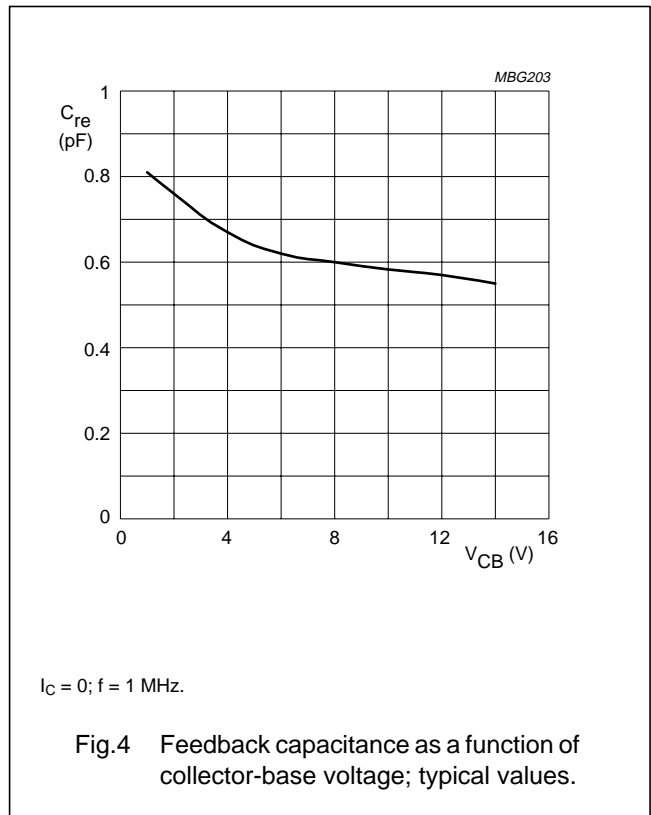
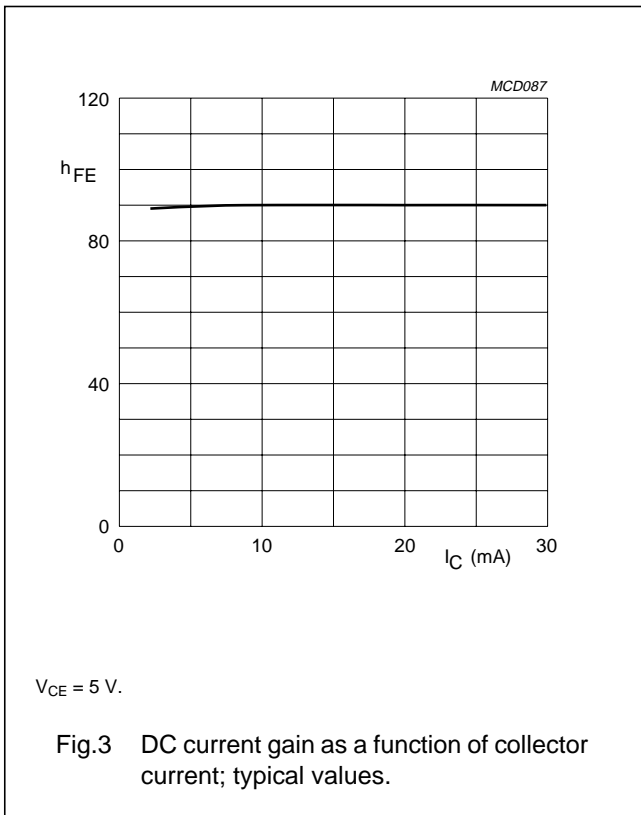
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$I_{CBO}$	collector leakage current	$I_E = 0; V_{CB} = 5\text{ V}$	–	–	50	nA
$h_{FE}$	DC current gain	$I_C = 30\text{ mA}; V_{CE} = 5\text{ V}$	40	90	–	
$C_c$	collector capacitance	$I_E = i_e = 0; V_{CB} = 5\text{ V}; f = 1\text{ MHz}$	–	0.7	–	pF
$C_e$	emitter capacitance	$I_C = i_c = 0; V_{EB} = 0.5\text{ V}; f = 1\text{ MHz}$	–	2.3	–	pF
$C_{re}$	feedback capacitance	$I_C = 0; V_{CE} = 5\text{ V}; f = 1\text{ MHz}$	–	0.6	–	pF
$f_T$	transition frequency	$I_C = 30\text{ mA}; V_{CE} = 5\text{ V}; f = 500\text{ MHz}$	4	5	–	GHz
$G_{UM}$	maximum unilateral power gain; note 1	$I_C = 30\text{ mA}; V_{CE} = 8\text{ V}; f = 1\text{ GHz}; T_{amb} = 25\text{ °C}$	–	13	–	dB
		$I_C = 30\text{ mA}; V_{CE} = 8\text{ V}; f = 2\text{ GHz}; T_{amb} = 25\text{ °C}$	–	8	–	dB
F	noise figure	$I_C = 5\text{ mA}; V_{CE} = 8\text{ V}; f = 1\text{ GHz}; \Gamma_s = \Gamma_{opt}$	–	1.5	–	dB
		$I_C = 5\text{ mA}; V_{CE} = 8\text{ V}; f = 2\text{ GHz}; \Gamma_s = \Gamma_{opt}$	–	2.1	–	dB

**Note**

1.  $G_{UM}$  is the maximum unilateral power gain, assuming  $s_{12}$  is zero and  $G_{UM} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)}$  dB.

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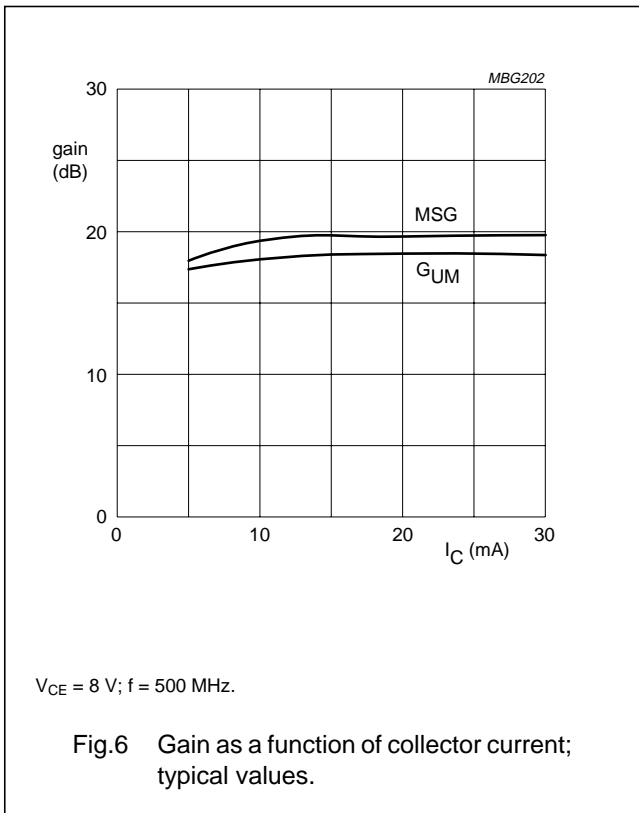


Fig.6 Gain as a function of collector current; typical values.

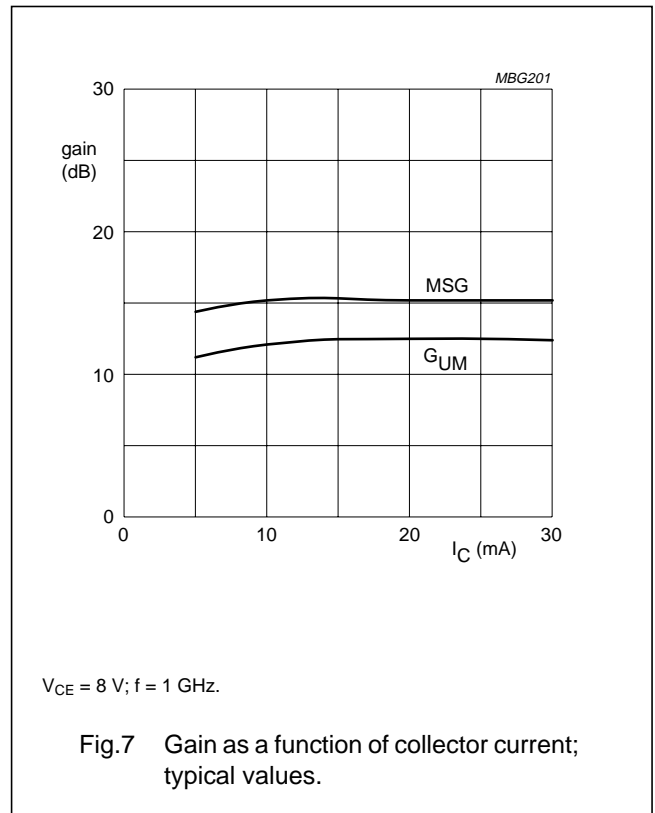


Fig.7 Gain as a function of collector current; typical values.

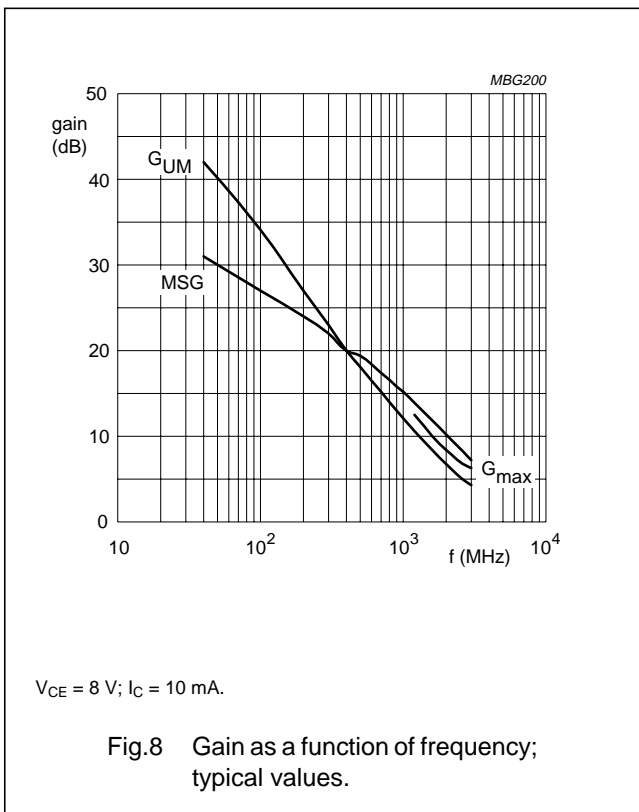


Fig.8 Gain as a function of frequency; typical values.

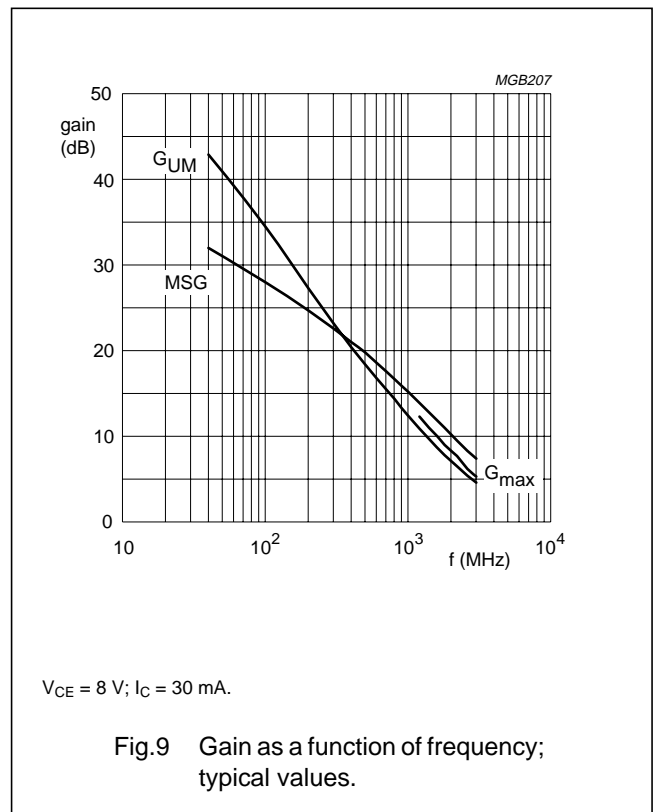
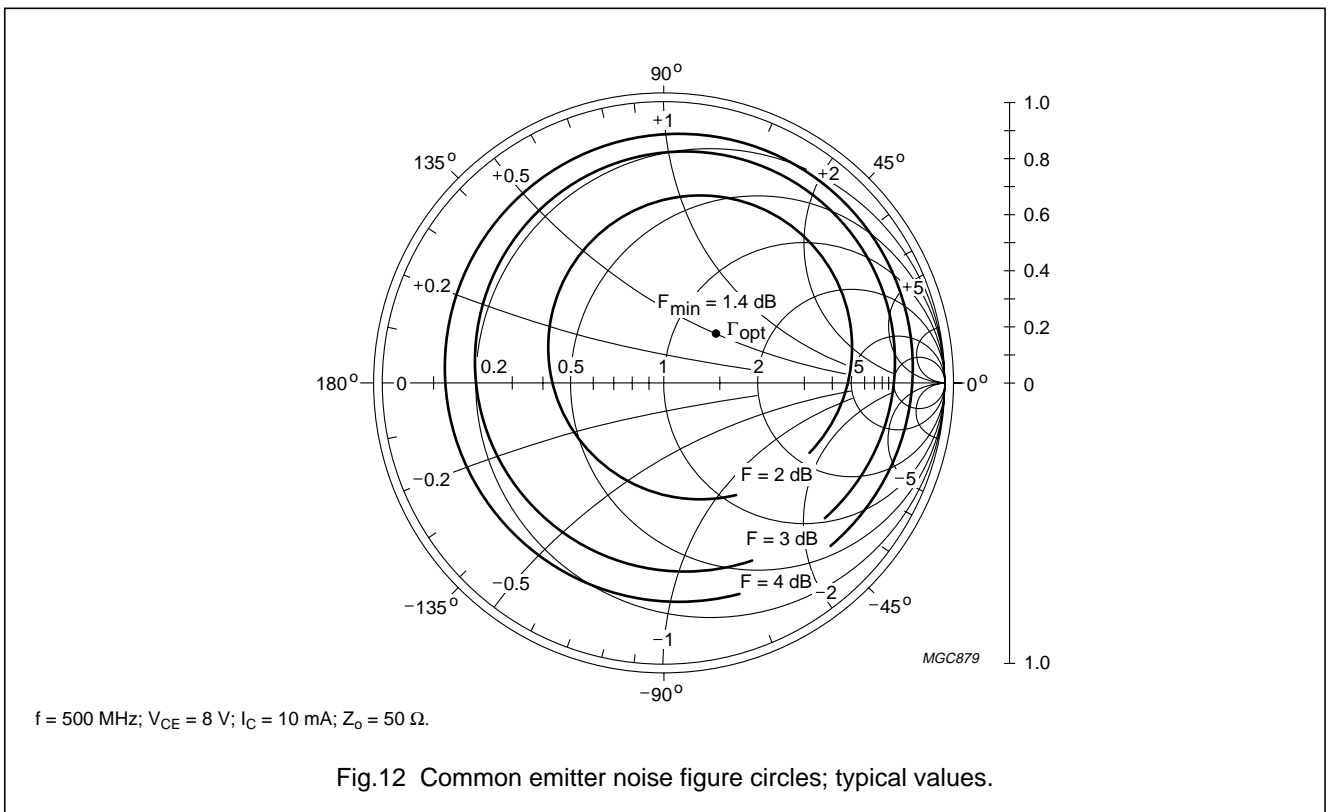
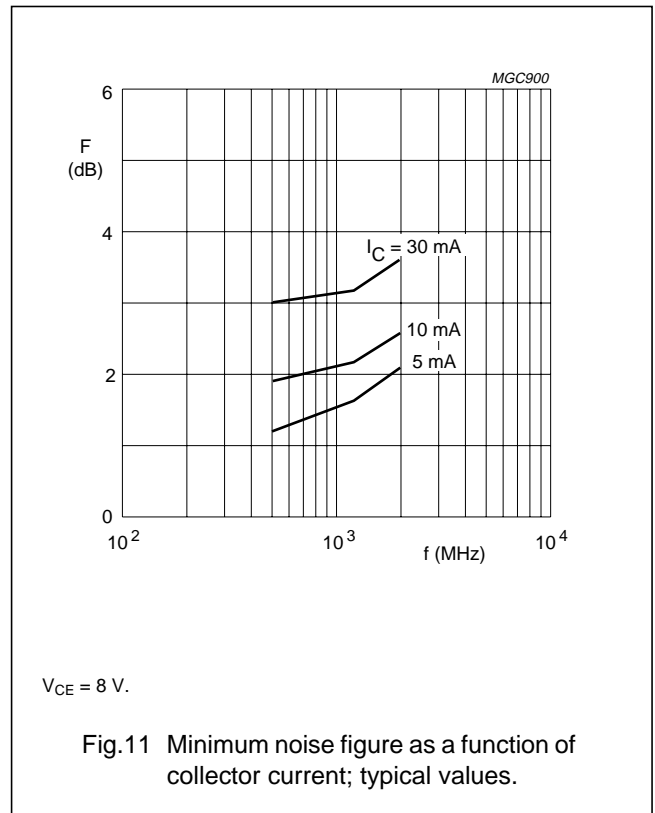
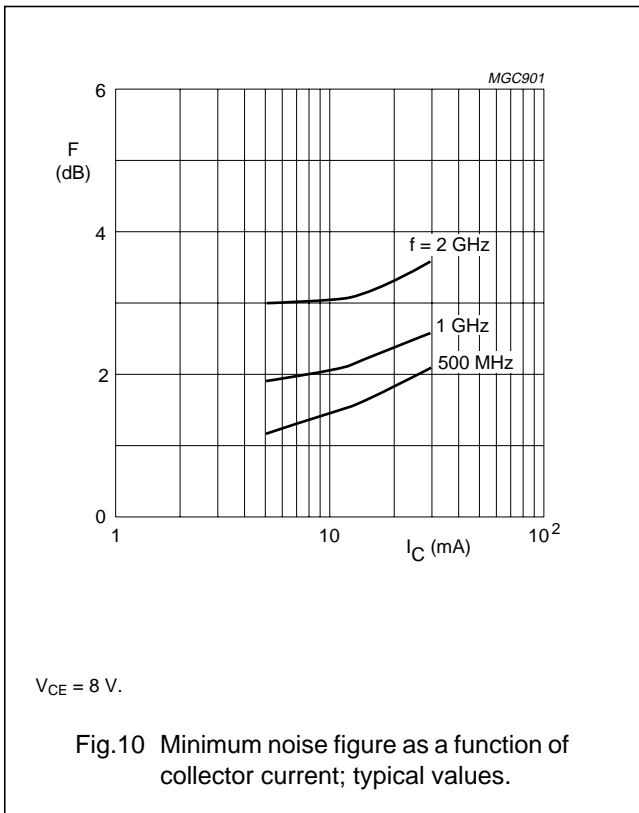


Fig.9 Gain as a function of frequency; typical values.

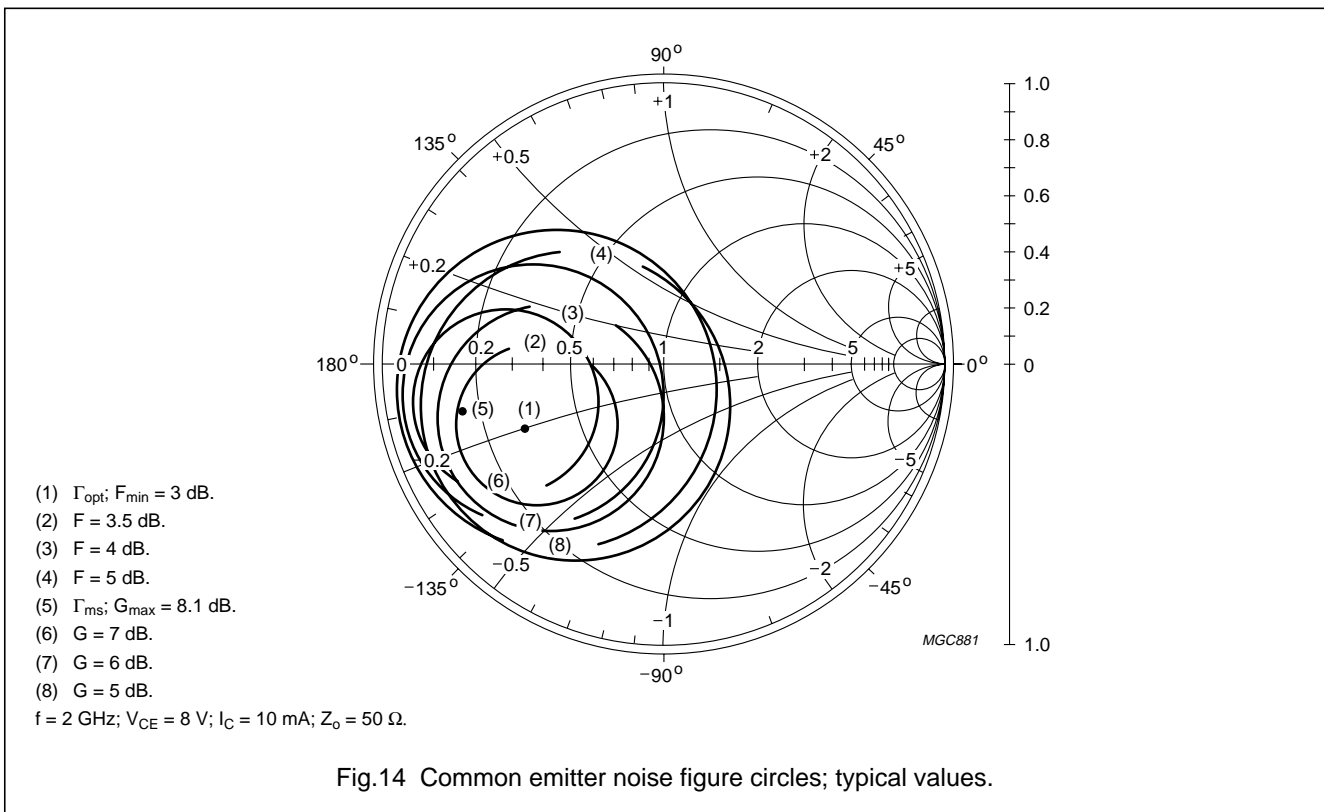
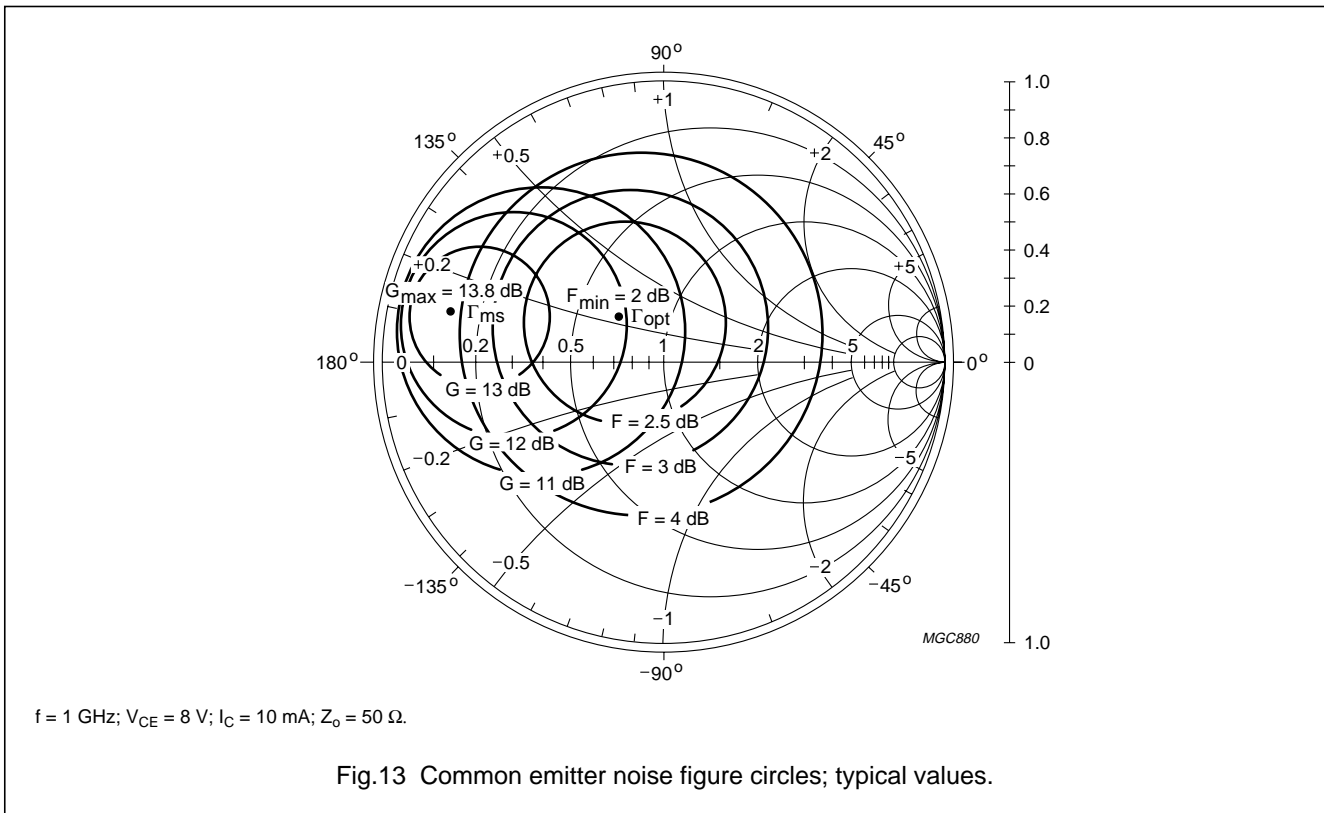
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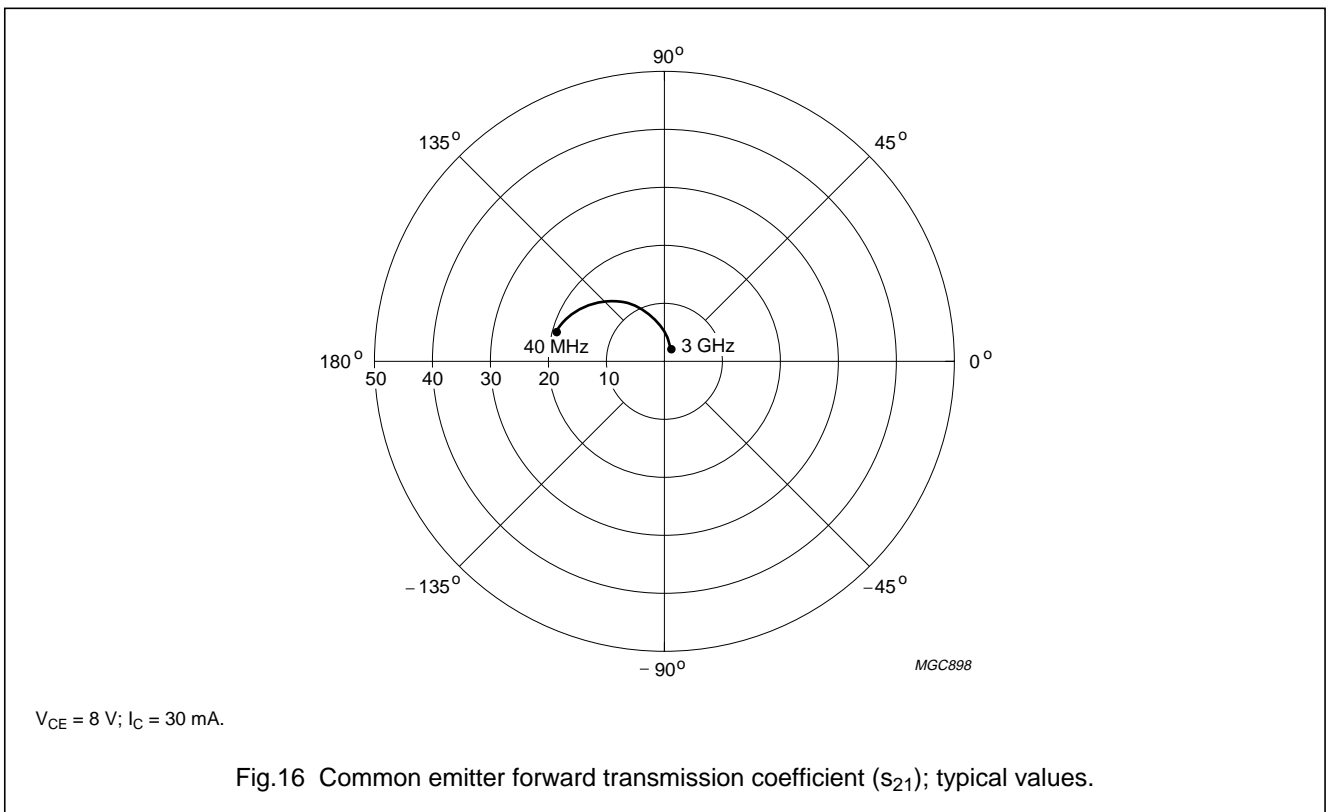
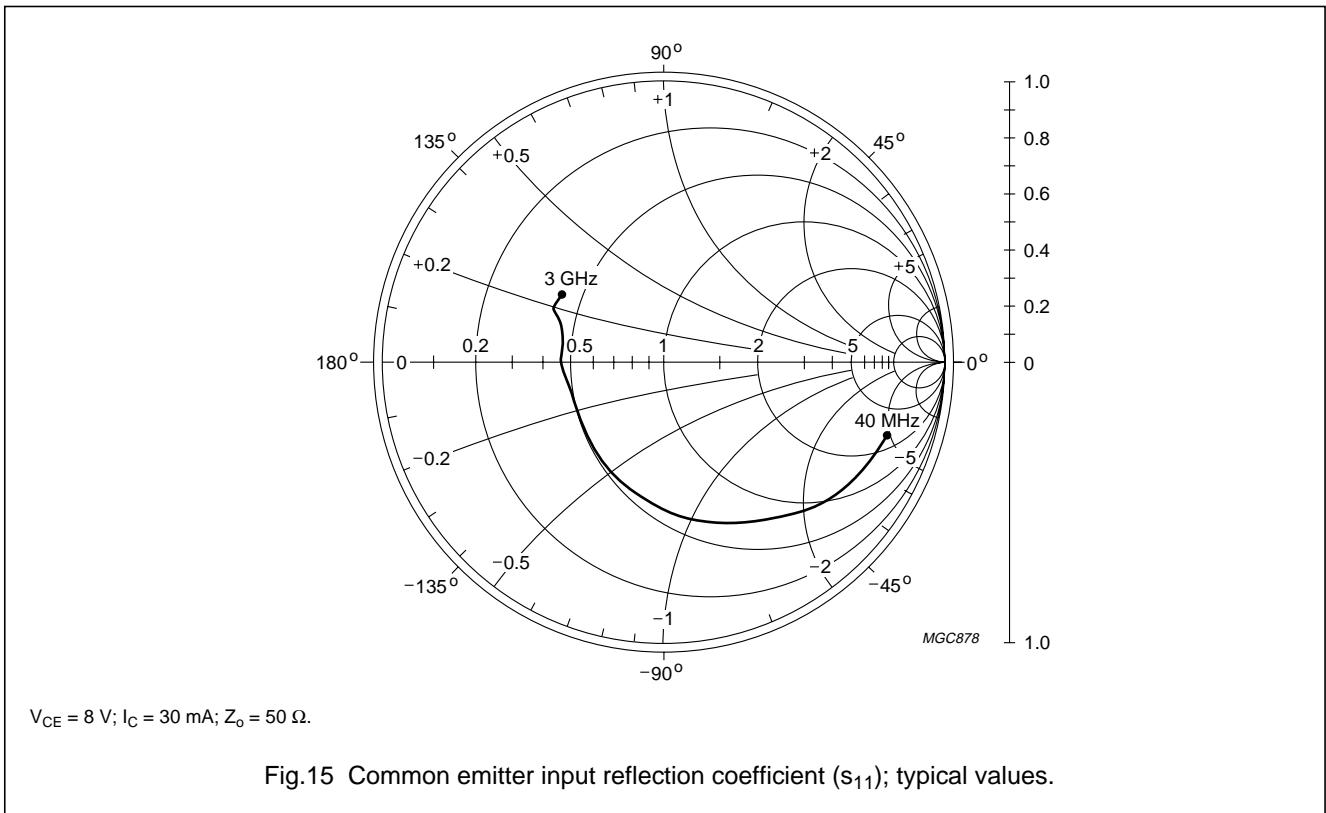
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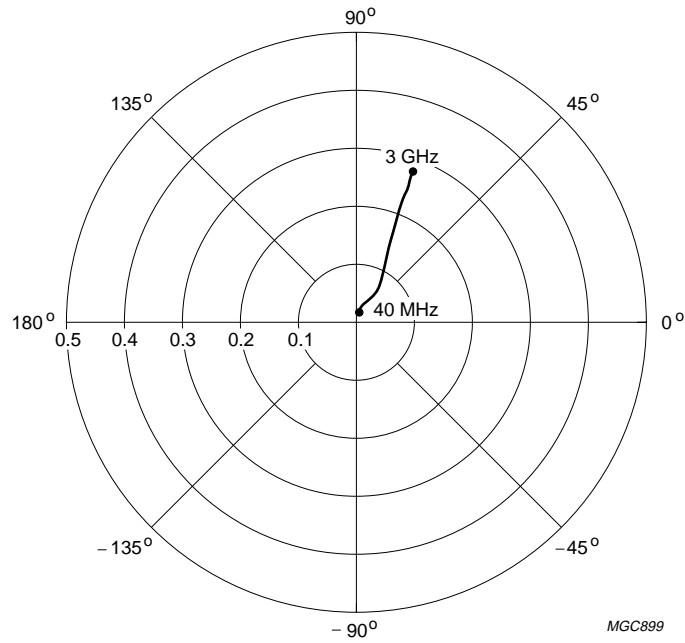
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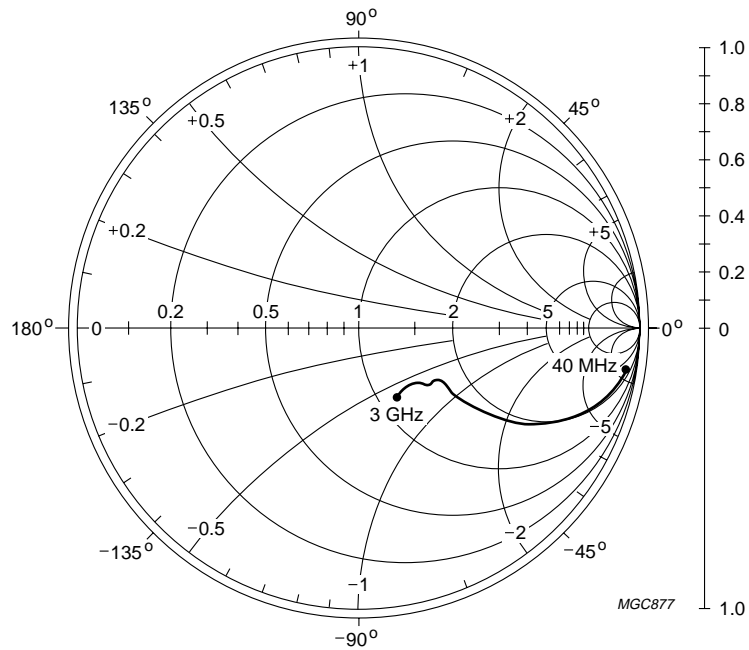
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MGC899

$V_{CE} = 8\text{ V}; I_C = 30\text{ mA}$ .

Fig.17 Common emitter reverse transmission coefficient ( $s_{12}$ ); typical values.



MGC877

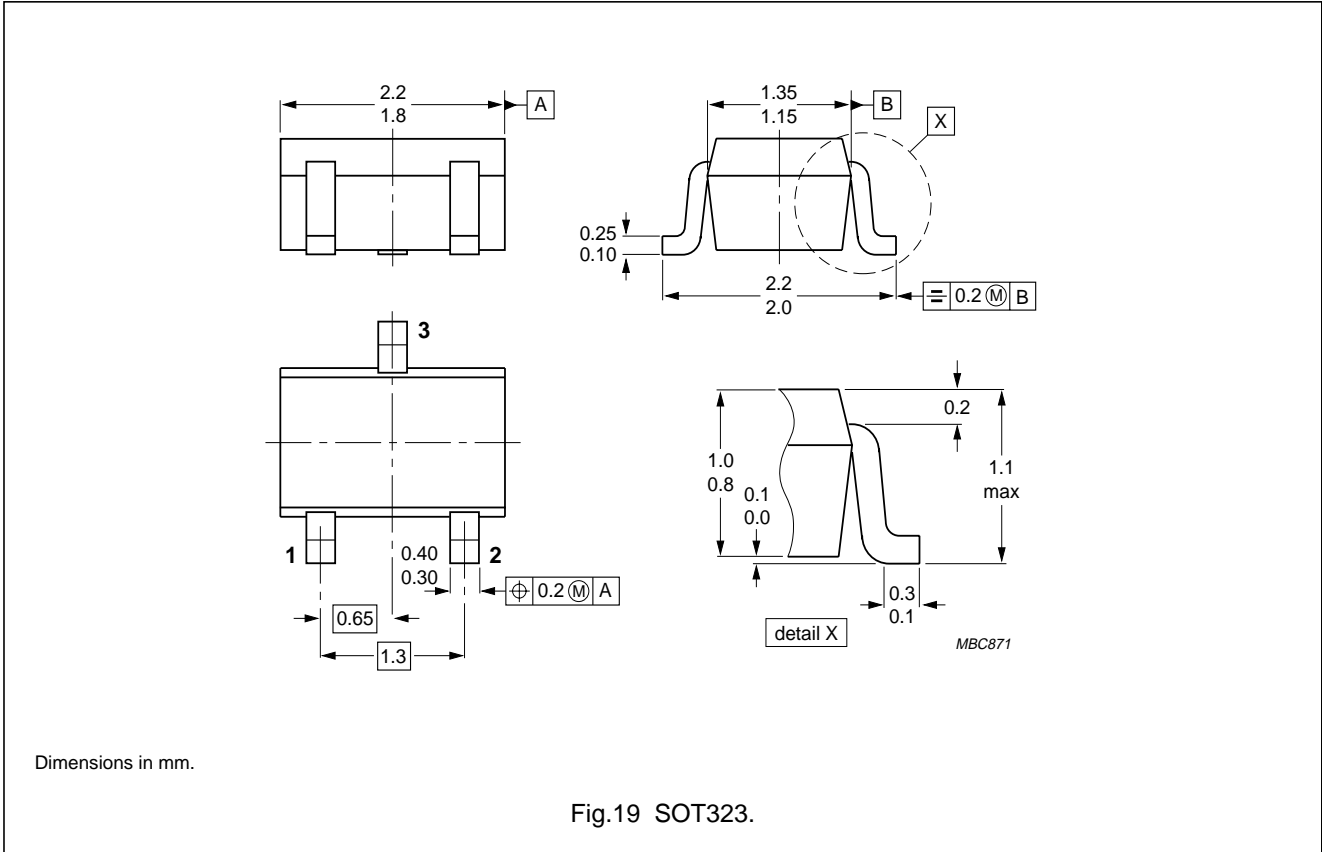
$V_{CE} = 8\text{ V}; I_C = 30\text{ mA}; Z_o = 50\ \Omega$ .

Fig.18 Common emitter output reflection coefficient ( $s_{22}$ ); typical values.

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PACKAGE OUTLINE



## NPN 5 GHz wideband transistor

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**DEFINITIONS**

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

**LIFE SUPPORT APPLICATIONS**

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