

# DATA SHEET

**BLV33F**

VHF linear power transistor

Product specification

1996 Oct 10

# VHF linear power transistor

# BLV33F

### FEATURES

- Internally matched input for wideband operation and high power gain
- Diffused emitter ballasting resistors for an optimum temperature profile
- Gold metallization ensures excellent reliability.

### APPLICATIONS

- Primarily intended for use in linear VHF amplifiers for television transmitters and transposers.

### DESCRIPTION

NPN silicon planar epitaxial transistor encapsulated in a 1/2" 6 lead SOT119A capstan package with ceramic cap. All leads are isolated from the flange.

### PINNING - SOT119A

PIN	SYMBOL	DESCRIPTION
1	e	emitter
2	e	emitter
3	b	base
4	c	collector
5	e	emitter
6	e	emitter

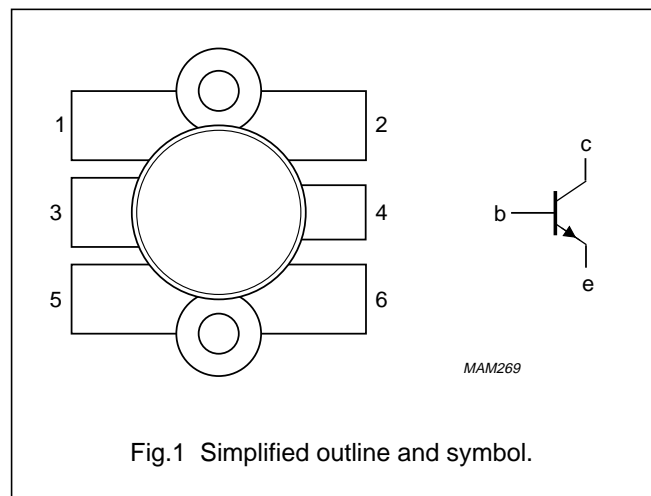


Fig.1 Simplified outline and symbol.

### QUICK REFERENCE DATA

RF performance in a common emitter push-pull test circuit.

MODE OF OPERATION	f <sub>vision</sub> (MHz)	V <sub>CE</sub> (V)	I <sub>C</sub> , I <sub>C(ZS)</sub> (A)	T <sub>h</sub> (°C)	d <sub>im</sub> <sup>(1)</sup> (dB)	P <sub>o sync</sub> <sup>(1)</sup> (W)	G <sub>P</sub> (dB)	sync compr. <sup>(2)</sup> sync in/sync out (%)
CW, class-A	224.25	25	3.2	70	-55	>13	>13.5	
				25	-55	typ. 19	typ. 14.8	
CW, class-AB	224.25	28	0.2	70	-	typ. 85	typ. 10.5	30/25

### Notes

1. Three-tone test method (vision carrier -8 dB, sound carrier -7 dB, sideband signal -16 dB), zero dB corresponds to peak sync level.
2. Television service (negative modulation, C.C.I.R. system).

<b>WARNING</b>
<b>Product and environmental safety - toxic materials</b>
This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

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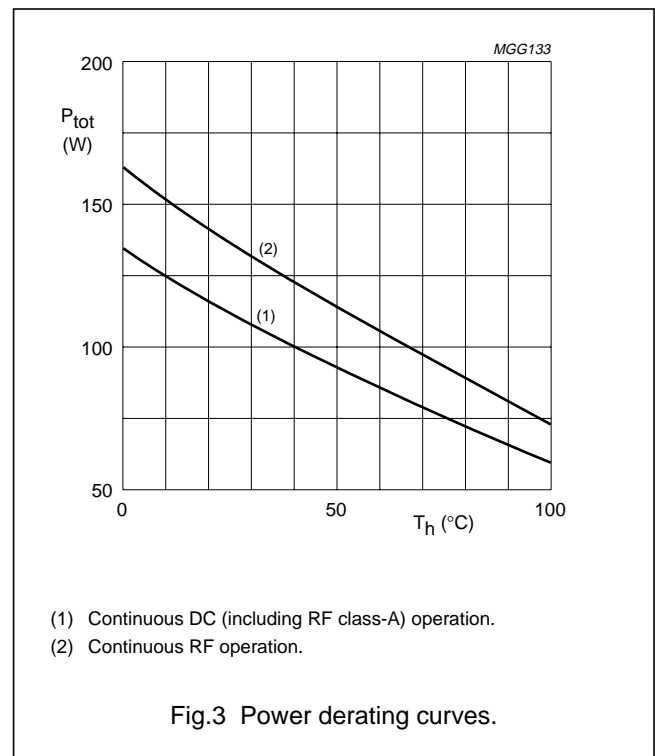
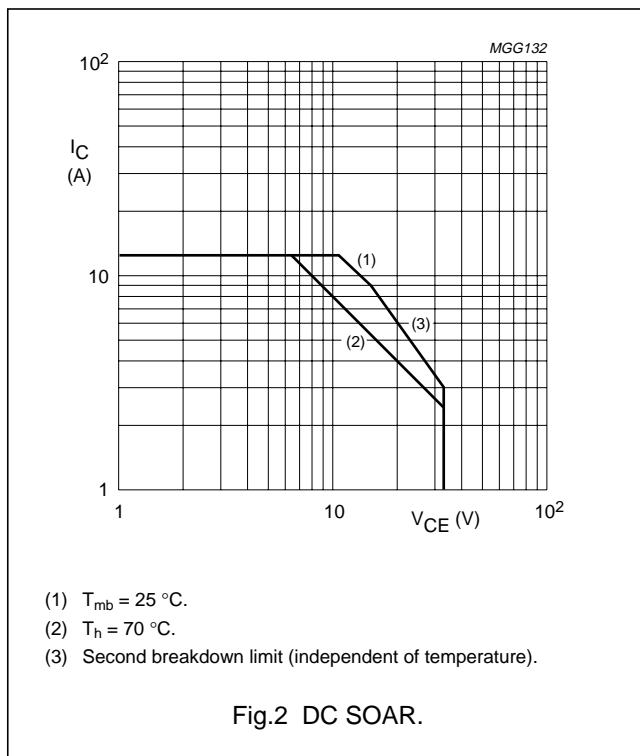
## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CESM}$	collector-emitter voltage	$V_{BE} = 0$	–	65	V
$V_{CEO}$	collector-emitter voltage	open base	–	33	V
$V_{EBO}$	emitter-base voltage	open collector	–	4	V
$I_C$	collector current (DC)		–	12.5	A
$I_{C(AV)}$	average collector current		–	12.5	A
$I_{CM}$	peak collector current	$f > 1 \text{ MHz}$	–	20	A
$P_{tot}$	total power dissipation (DC)	$T_{mb} = 25 \text{ }^\circ\text{C}$	–	133	W
$P_{rf}$	RF power dissipation	$f > 1 \text{ MHz}; T_{mb} = 25 \text{ }^\circ\text{C}$	–	162	W
$T_{stg}$	storage temperature		–65	+150	$^\circ\text{C}$
$T_j$	operating junction temperature		–	200	$^\circ\text{C}$

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-mb(dc)}$	thermal resistance from junction to mounting base (DC dissipation)	$P_{diss} = 80 \text{ W}; T_{mb} = 82 \text{ }^\circ\text{C}; T_h = 70 \text{ }^\circ\text{C}$	1.43	K/W
$R_{th\ j-mb(rf)}$	thermal resistance from junction to mounting base (RF dissipation)	$P_{diss} = 80 \text{ W}; T_{mb} = 82 \text{ }^\circ\text{C}; T_h = 70 \text{ }^\circ\text{C}$	1.17	K/W
$R_{th\ mb-h}$	thermal resistance from mounting base to heatsink	$P_{diss} = 80 \text{ W}; T_{mb} = 82 \text{ }^\circ\text{C}; T_h = 70 \text{ }^\circ\text{C}$	0.2	K/W



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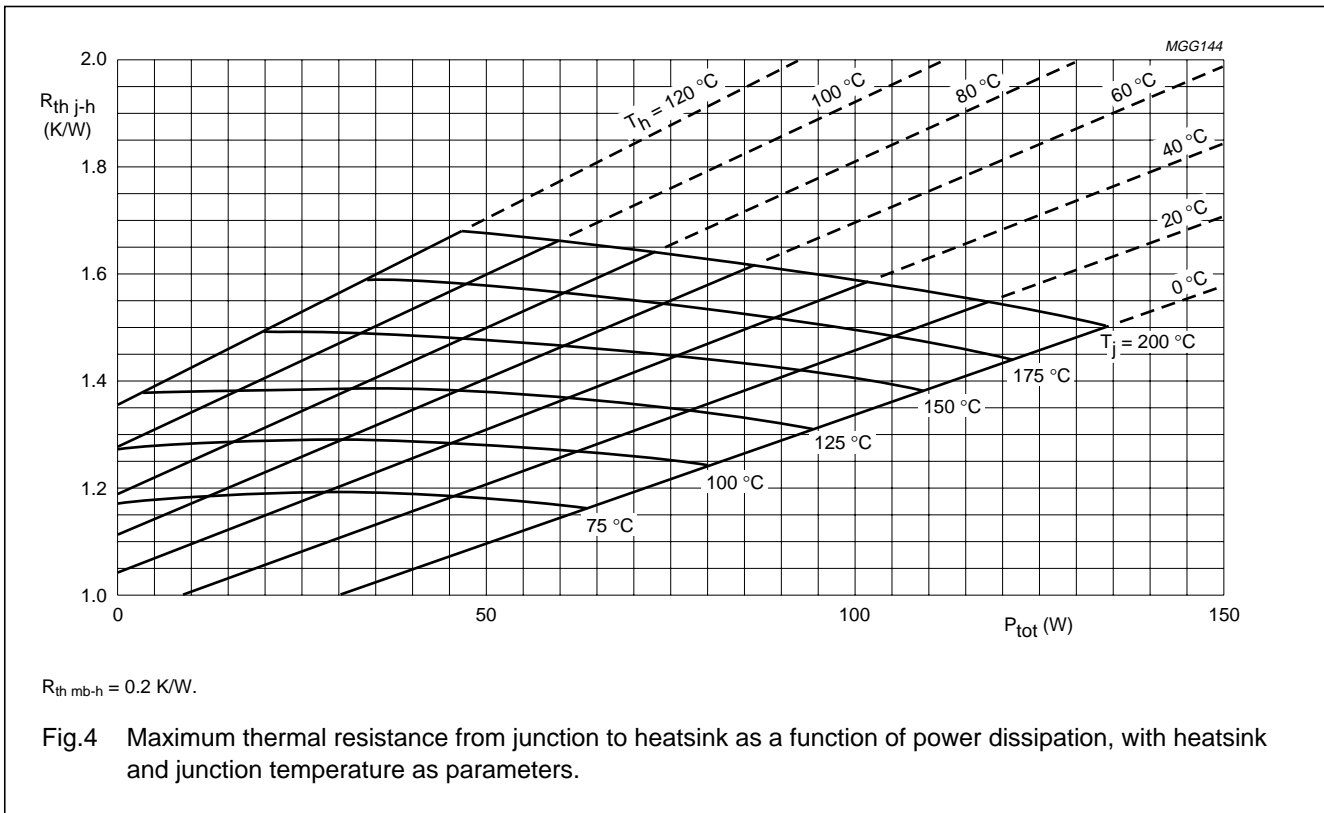


Fig.4 Maximum thermal resistance from junction to heatsink as a function of power dissipation, with heatsink and junction temperature as parameters.

**Example**

Nominal class-A operation (without RF signal):  $V_{CE} = 25\ V$ ;  $I_C = 3.2\ A$ ;  $T_h = 70\ ^\circ C$ .

Figure 4 shows:

$R_{th\ j-h} = \text{max. } 1.63\ K/W$

$T_j = \text{max. } 200\ ^\circ C$ .

Typical device:

$R_{th\ j-h} = \text{typ. } 1.53\ K/W$

$T_j = \text{typ. } 192\ ^\circ C$ .

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**CHARACTERISTICS**

$T_j = 25\text{ °C}$ ; unless otherwise specified.

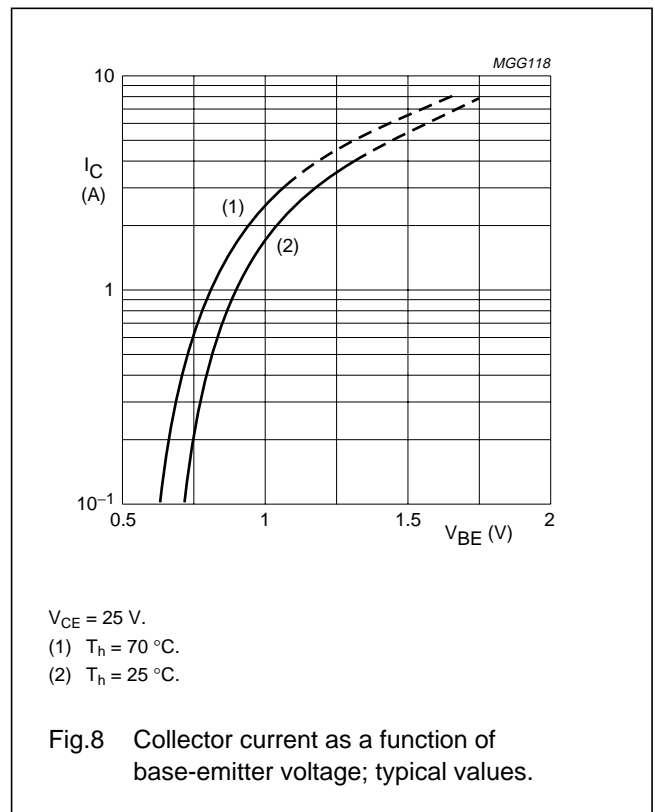
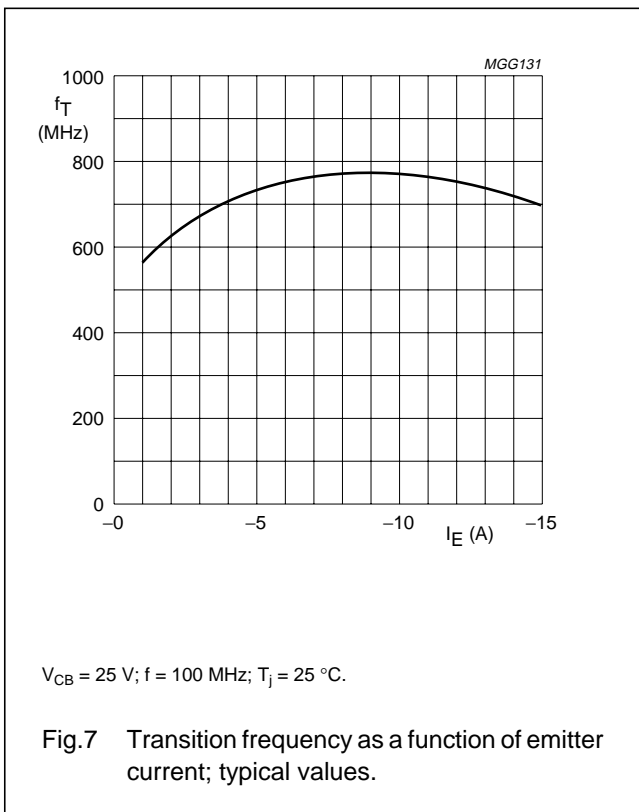
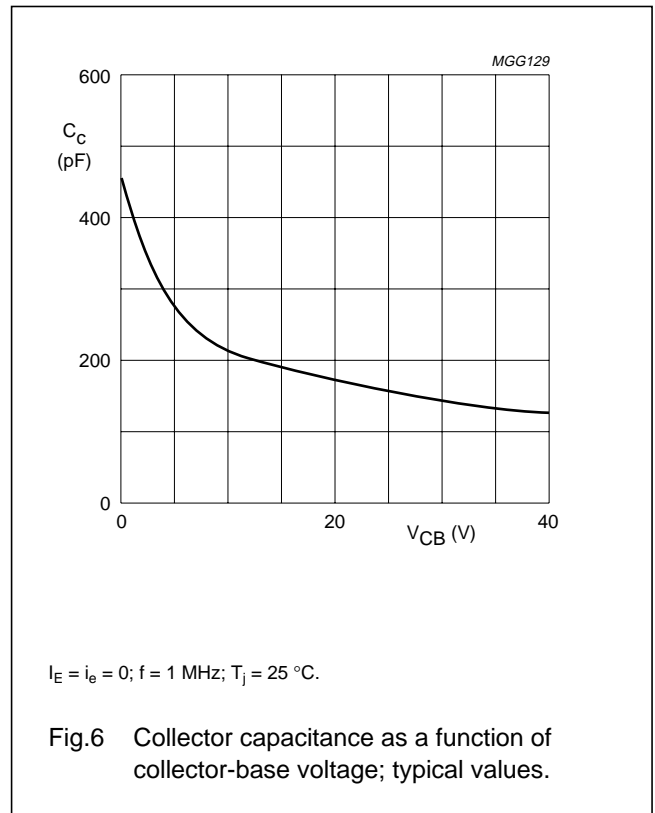
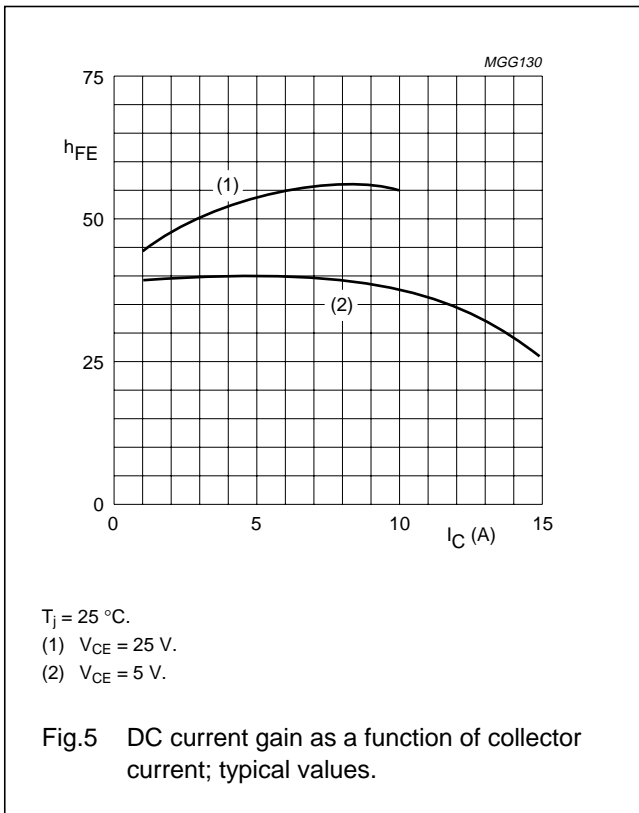
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)CES}$	collector-emitter breakdown voltage	$V_{BE} = 0$ ; $I_C = 25\text{ mA}$	65	–	–	V
$V_{(BR)CEO}$	collector-emitter breakdown voltage	open base; $I_C = 100\text{ mA}$	33	–	–	V
$V_{(BR)EBO}$	emitter-base breakdown voltage	open collector; $I_E = 10\text{ mA}$	4	–	–	V
$I_{CES}$	collector cut-off current	$V_{BE} = 0$ ; $V_{CE} = 30\text{ V}$	–	–	1	mA
$h_{FE}$	DC current gain	$V_{CE} = 25\text{ V}$ ; $I_C = 3\text{ A}$ ; note 1	15	50	100	
$V_{CEsat}$	collector-emitter saturation voltage	$I_C = 6\text{ A}$ ; $I_B = 0.6\text{ A}$ ; note 1	–	0.75	–	V
$f_T$	transition frequency	$V_{CB} = 25\text{ V}$ ; $I_E = -3\text{ A}$ ; $f = 100\text{ MHz}$ ; note 2	–	680	–	MHz
		$V_{CB} = 25\text{ V}$ ; $I_E = -6\text{ A}$ ; $f = 100\text{ MHz}$ ; note 2	–	750	–	MHz
$C_C$	collector capacitance	$V_{CB} = 25\text{ V}$ ; $I_E = i_e = 0$ ; $f = 1\text{ MHz}$	–	155	–	pF
$C_{re}$	feedback capacitance	$I_C = 50\text{ mA}$ ; $V_{CE} = 25\text{ V}$ ; $f = 1\text{ MHz}$	–	88	–	pF
$C_{cf}$	collector-flange capacitance		–	3	–	pF

**Notes**

1. Measured under pulse conditions:  $t_p \leq 300\text{ }\mu\text{s}$ ;  $\delta \leq 0.02$ .
2. Measured under pulse conditions:  $t_p \leq 50\text{ }\mu\text{s}$ ;  $\delta \leq 0.01$ .

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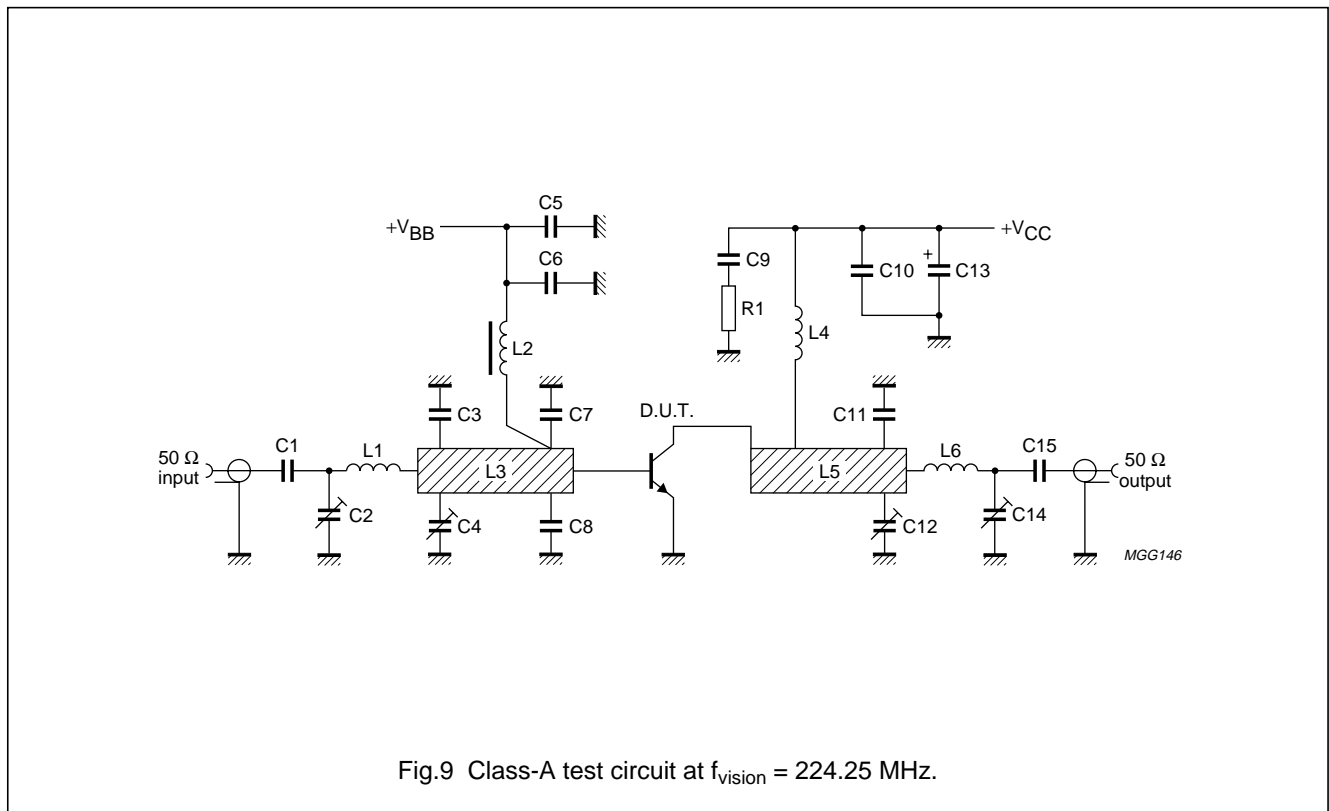
APPLICATION INFORMATION

RF performance in VHF class-A operation (linear power amplifier)

MODE OF OPERATION	$f_{\text{vision}}$ (MHz)	$V_{\text{CE}}$ (V)	$I_{\text{C}}$ (A)	$T_{\text{h}}$ (°C)	$d_{\text{im}}^{(1)}$ (dB)	$P_{\text{o sync}}^{(1)}$ (W)	$G_{\text{p}}$ (dB)
CW, class-A	224.25	25	3.2	70	-55	>13	>13.5
				70	-55	typ. 14.5	typ. 14.5
				70	-52	typ. 22	typ. 14.5
				25	-55	typ. 19	typ. 14.8

Note

1. Three-tone test method (vision carrier -8 dB, sound carrier -7 dB, sideband signal -16 dB), zero dB corresponds to peak sync level.



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List of components used in test circuit (see Figs 9 and 10).

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
C1, C15	multilayer ceramic chip capacitor; note 1	560 pF, 500 V		
C2, C4, C12, C14	film dielectric trimmer	4 to 40 pF		2222 809 08002
C3	multilayer ceramic chip capacitor; note 1	10 pF, 500 V		
C5	multilayer ceramic chip capacitor	470 nF, 50 V		2222 856 48474
C6, C10	multilayer ceramic chip capacitor	680 pF, 50 V		2222 852 13681
C7, C8	multilayer ceramic chip capacitor; note 1	47 pF, 500 V	placed 8 mm from transistor edge	
C9	polyester capacitor	330 nF		
C11	multilayer ceramic chip capacitor; note 1	68 pF, 500 V		
C13	solid tantalum capacitor	6.8 $\mu$ F, 35 V		
L1	2 turns of 1.6 mm enamelled Cu wire		int. diameter 5 mm length 5 mm leads 2 $\times$ 3 mm	
L2	microchoke	1 $\mu$ H		4322 057 01080
L3	stripline; note 2	30 $\Omega$	6 mm $\times$ 32.7 mm	
L4	2 turns of closely wound 1 mm enamelled Cu wire		int. diameter 5 mm leads 2 $\times$ 10 mm	
L5	stripline; note 2	30 $\Omega$	6 mm $\times$ 24 mm	
L6	2 turns of 1.6 mm enamelled Cu wire		int. diameter 4 mm length 4.5 mm leads 2 $\times$ 3 mm	
R1	carbon resistor	10 $\Omega$		

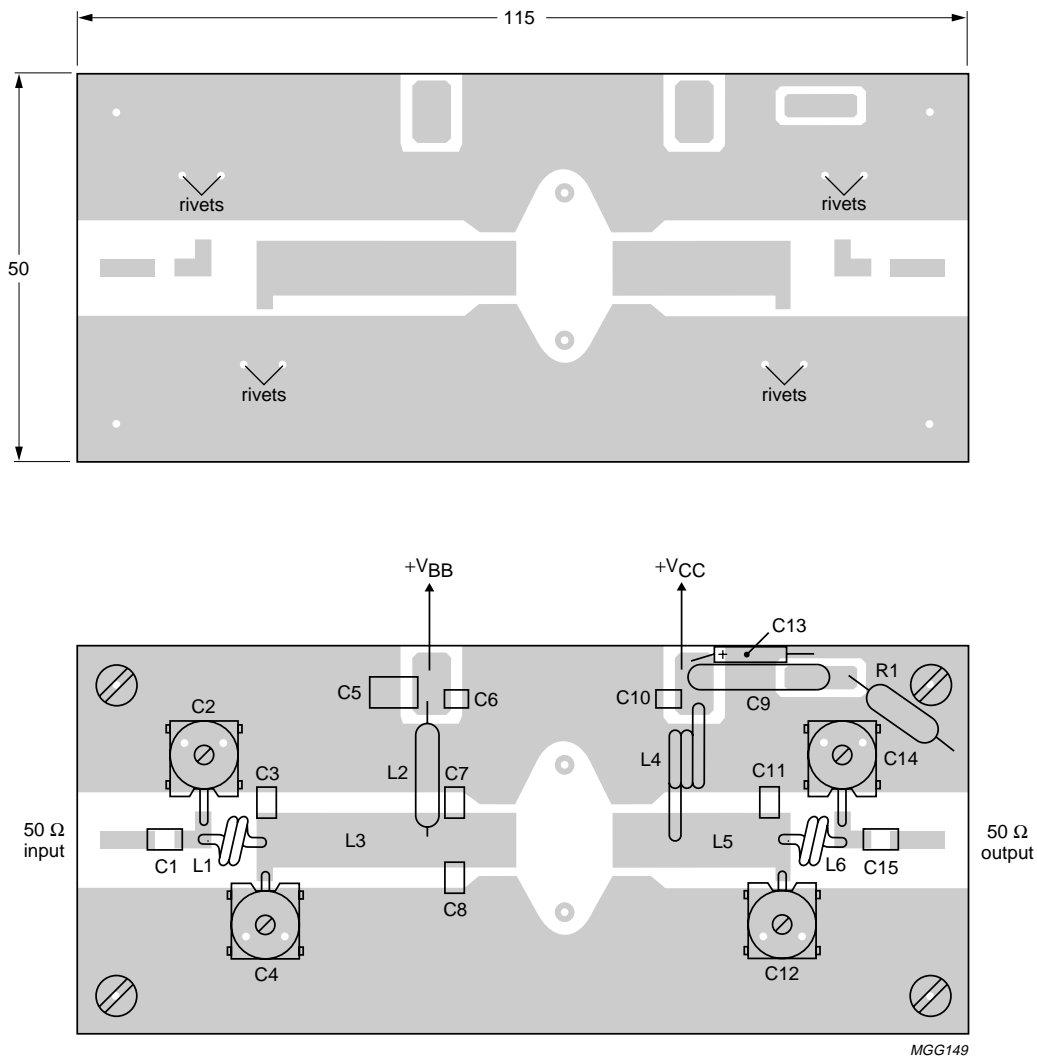
**Notes**

- American Technical Ceramics type 100B or capacitor of same quality.
- The striplines are on a double Cu-clad printed-circuit board, with epoxy fibre-glass dielectric ( $\epsilon_r = 4.5$ ); thickness  $\frac{1}{16}$ ".



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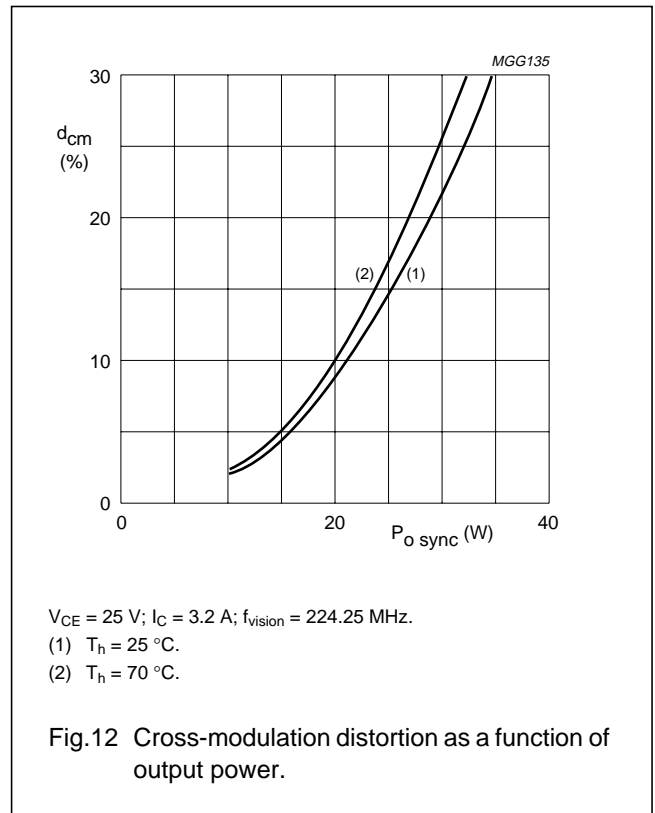
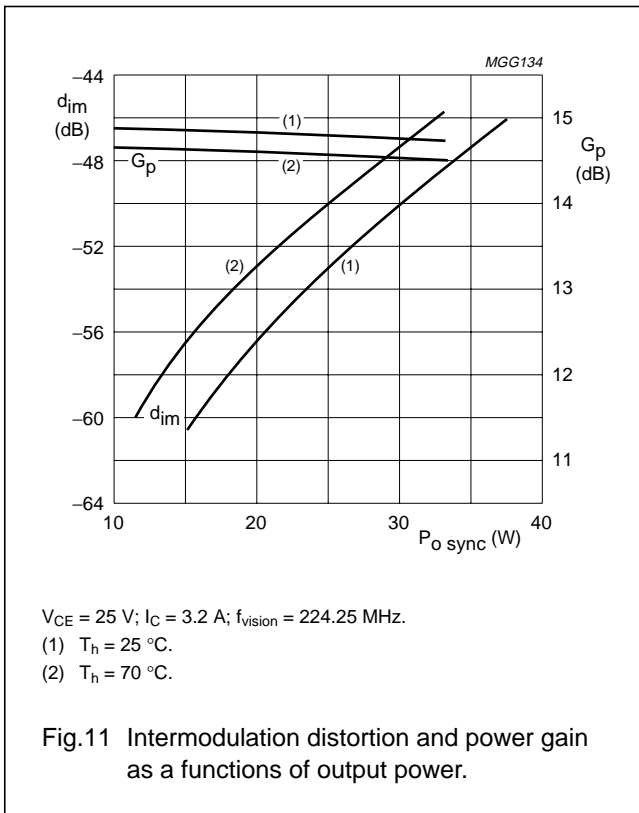
Dimensions in mm.

The circuit and the components are on one side of the epoxy fibre-glass board, the other side is unetched copper to serve as earth. Earth connections are made by hollow rivets. Additionally copper straps are used under the emitters and at the input and output to provide direct contact between the copper on the component side and the ground-plane.

Fig.10 Component layout and printed-circuit board for 224.25 MHz class-A test circuit.

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Three-tone test method (vision carrier  $-8\text{ dB}$ , sound carrier  $-7\text{ dB}$ , sideband signal  $-16\text{ dB}$ ), zero dB corresponds to peak sync level (see Fig.11). Intermodulation distortion of input signal  $\leq -70\text{ dB}$ .

Two-tone test method (vision carrier  $0\text{ dB}$ , sound carrier  $-7\text{ dB}$ ), zero dB corresponds to peak sync level.

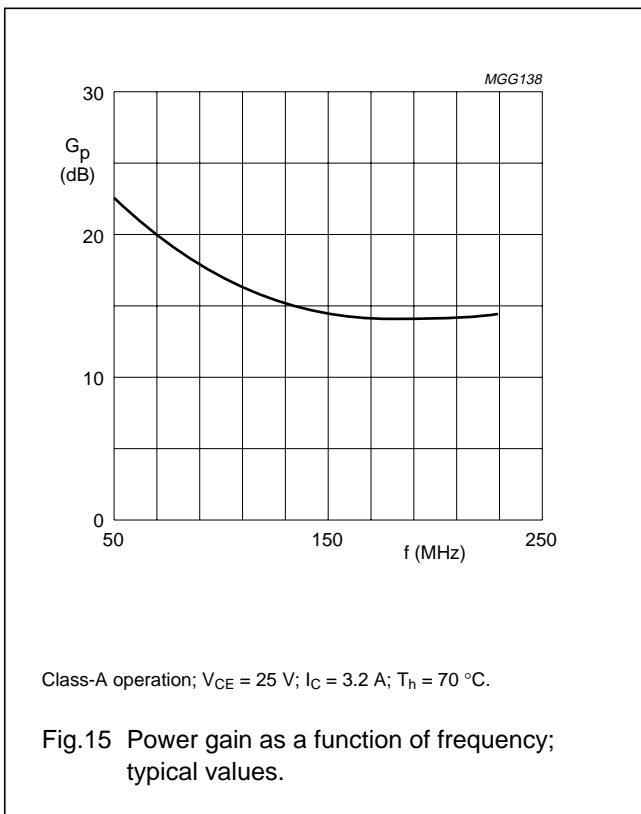
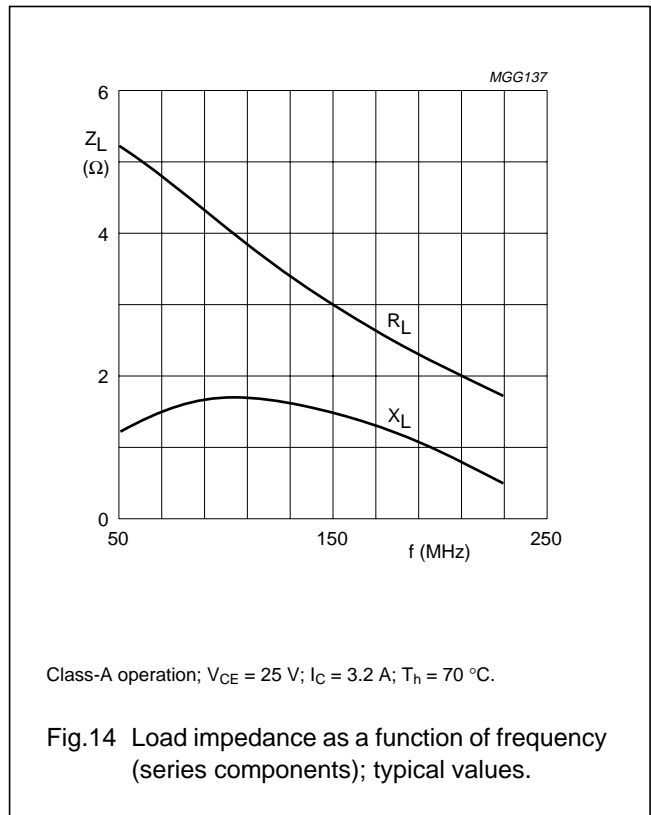
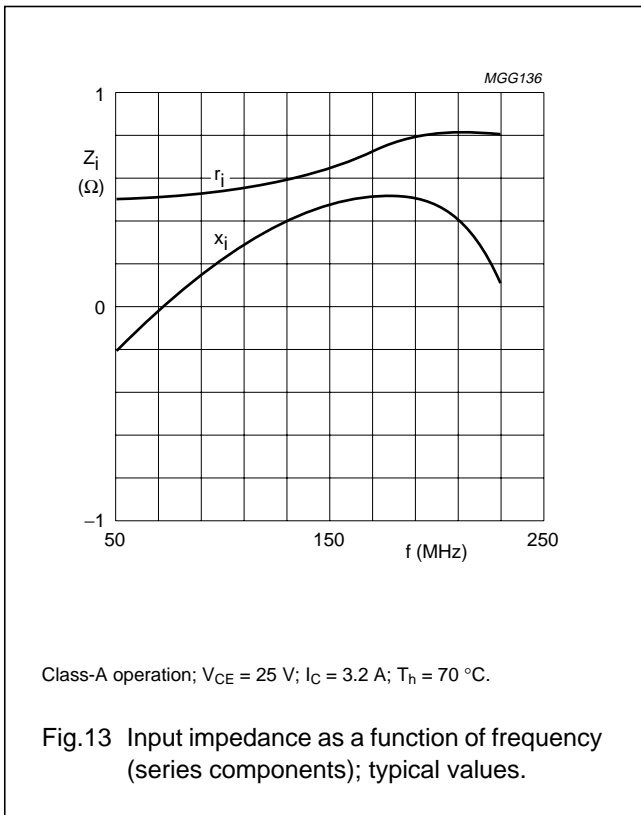
Cross-modulation distortion ( $d_{cm}$ ) is the voltage variation (%) of sound carrier when vision carrier is switched from  $0\text{ dB}$  to  $-20\text{ dB}$  (see Fig.12).

**Ruggedness in class-A operation**

The BLV33F is capable of withstanding a full load mismatch corresponding to  $VSWR = 50 : 1$  through all phases up to  $30\text{ W}$  (RMS) or  $40\text{ W}$  (PEP) under the following conditions:  $V_{CE} = 25\text{ V}; I_C = 3.2\text{ A}; T_h = 70\text{ }^\circ\text{C}; f = 224.25\text{ MHz}; R_{th\text{ mb-h}} = 0.2\text{ K/W}.$

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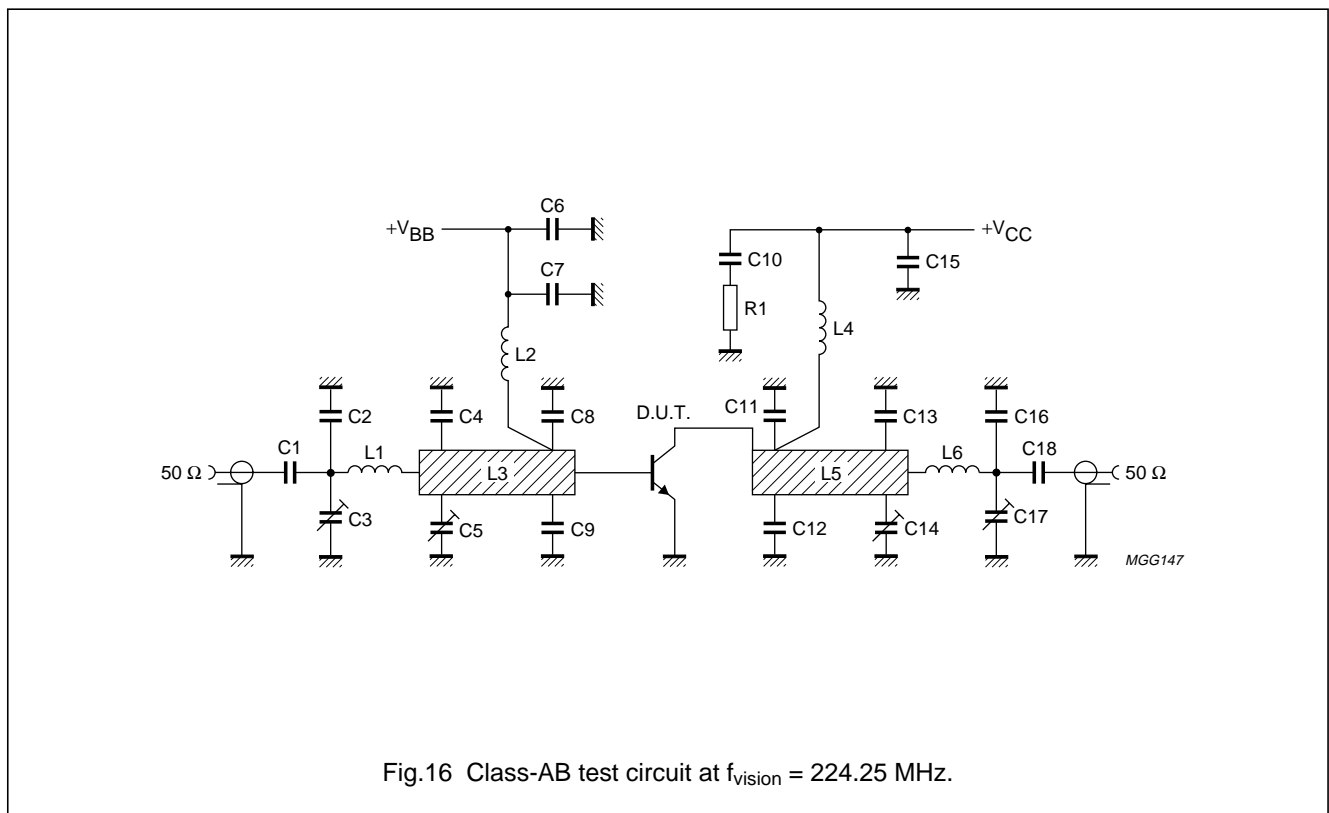
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RF performance in VHF class-AB operation (C.W.).

MODE OF OPERATION	f (MHz)	V <sub>CE</sub> (V)	I <sub>c</sub> , I <sub>c(2S)</sub> (A)	T <sub>h</sub> (°C)	P <sub>L</sub> (W)	I <sub>c</sub> (A)	η <sub>c</sub> (%)	G <sub>p</sub> (dB) <sup>(1)</sup>
CW, class-AB	224.25	28	0.2	70	40 85	typ. 2.75 typ. 4.25	typ. 52 typ. 71	typ. 11.5 typ. 10.5

Note

- Gain compression point of 1 dB is at typical 85 W (minimum 75 W). Using a 3rd-order amplitude transfer characteristic, 1 dB compression corresponds with 30 % sync input / 25 % sync output compression in television service (negative modulation, C.C.I.R. system).



## VHF linear power transistor

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List of components used in test circuit (see Figs 16 and 17).

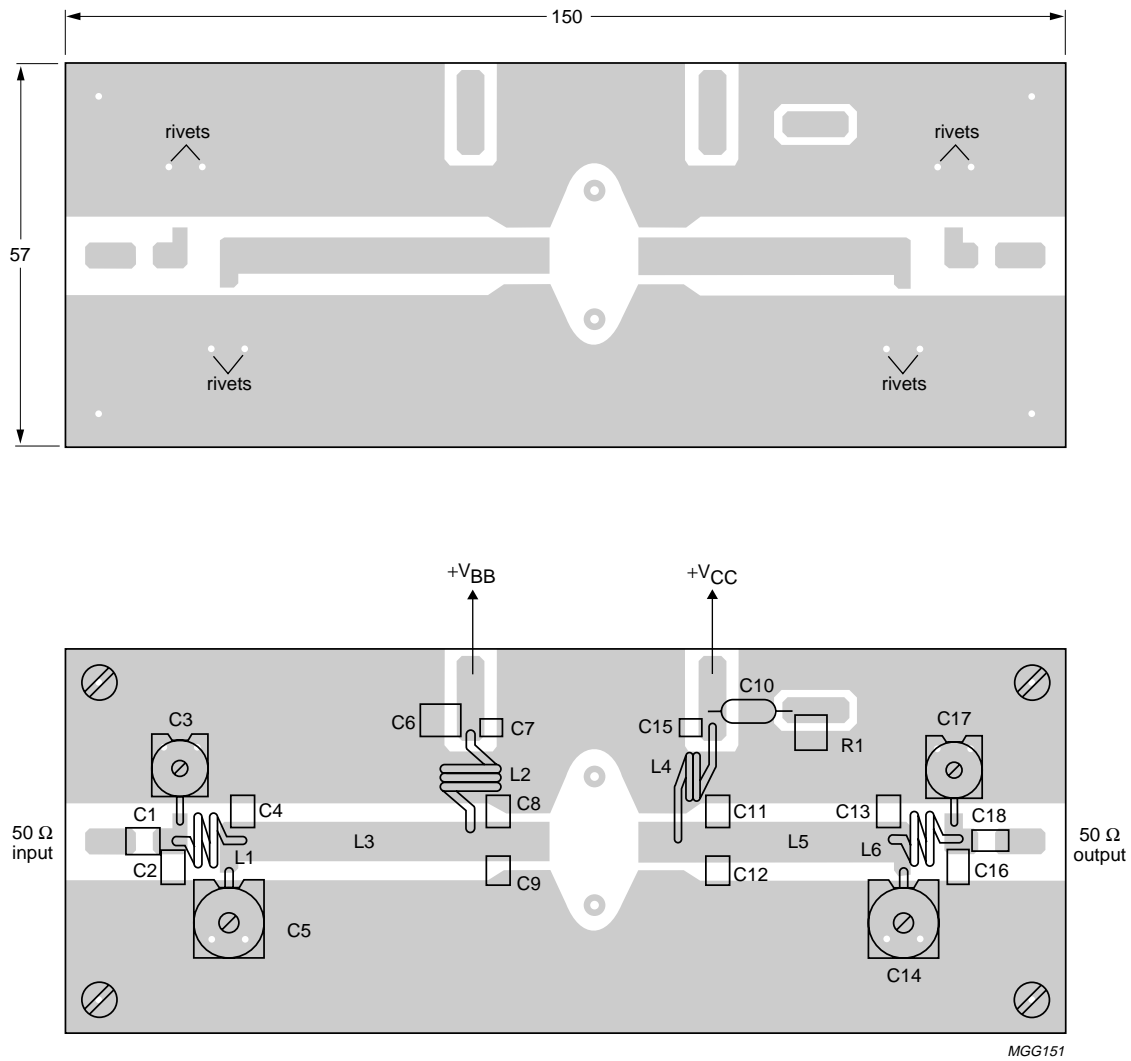
COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE No.
C1, C18	multilayer ceramic chip capacitor; note 1	620 pF, 100 V		
C2	multilayer ceramic chip capacitor; note 1	27 pF, 500 V		
C3	film dielectric trimmer	2 to 18 pF		2222 809 09003
C4	multilayer ceramic chip capacitor; note 1	30 pF, 500 V		
C5, C14	film dielectric trimmer	4 to 40 pF		2222 809 08002
C6, C10	multilayer ceramic chip capacitor	470 nF, 50 V		2222 856 48474
C7, C15	multilayer ceramic chip capacitor	680 pF, 50 V		2222 852 13681
C8, C9	multilayer ceramic chip capacitor; note 1	68 pF, 500 V	placed 6.4 mm from transistor edge	
C11, C12	multilayer ceramic chip capacitor; note 1	43 pF, 500 V	placed 10 mm from transistor edge	
C13	multilayer ceramic chip capacitor; note 1	39 pF, 500 V		
C16	multilayer ceramic chip capacitor; note 1	3.3 pF, 500 V		
C17	film dielectric trimmer	1.4 to 5.5 pF		2222 809 09001
L1	2 turns of 1.6 mm enamelled Cu wire		int. diameter 4.5 mm length 4 mm leads 2 × 4 mm	
L2	3 turns of 1 mm closely wound enamelled Cu wire		int. diameter 5 mm leads 2 × 7 mm	
L3	stripline; note 2	30 Ω	6 mm × 47.8 mm	
L4	2 turns of 1 mm closely wound enamelled Cu wire		int. diameter 5 mm leads 2 × 8 mm	
L5	stripline; note 2	30 Ω	6 mm × 42.9 mm	
L6	2 turns of 1.6 mm enamelled Cu wire		int. diameter 4 mm length 4 mm leads 2 × 3 mm	
R1	carbon resistor	10 Ω		

## Notes

- American Technical Ceramics type 100B or capacitor of same quality.
- The striplines are on a double Cu-clad printed-circuit board, with epoxy fibre-glass dielectric ( $\epsilon_r = 4.5$ ); thickness  $\frac{1}{16}$ ".

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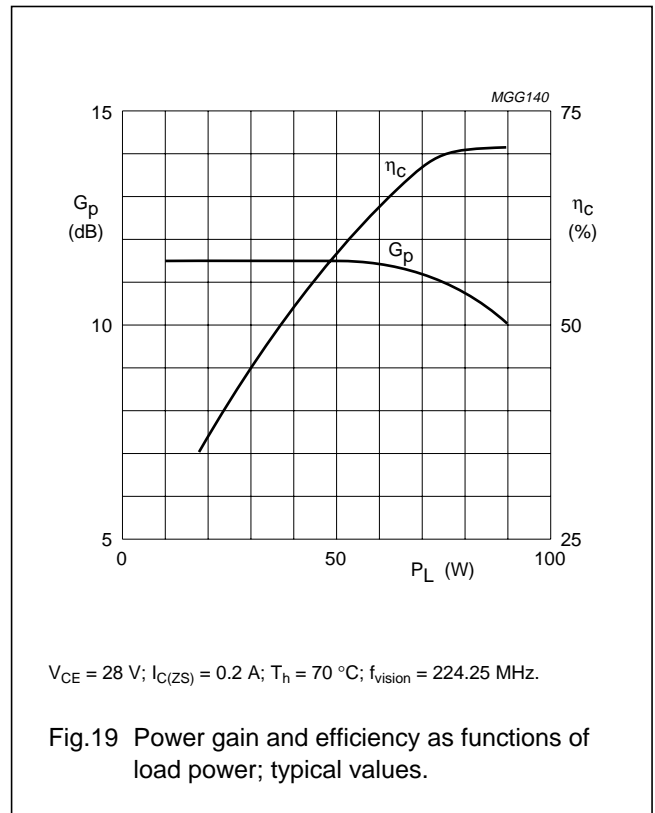
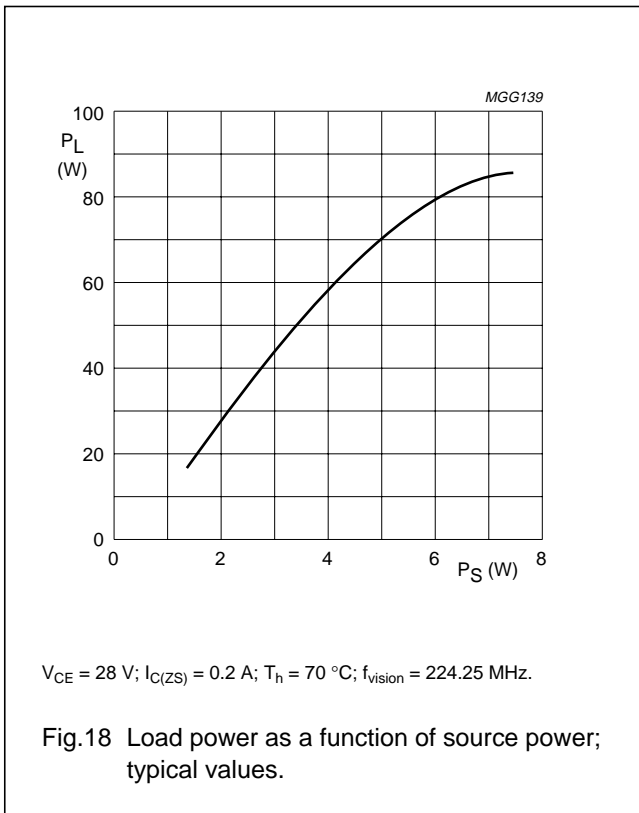
Dimensions in mm.

The circuit and the components are on one side of the epoxy fibre-glass board, the other side is unetched copper to serve as earth. Earth connections are made by hollow rivets. Additionally copper straps are used under the emitters and at the input and output to provide direct contact between the copper on the component side and the ground-plane.

Fig.17 Component layout and printed-circuit board for 224.25 MHz class-AB test circuit.

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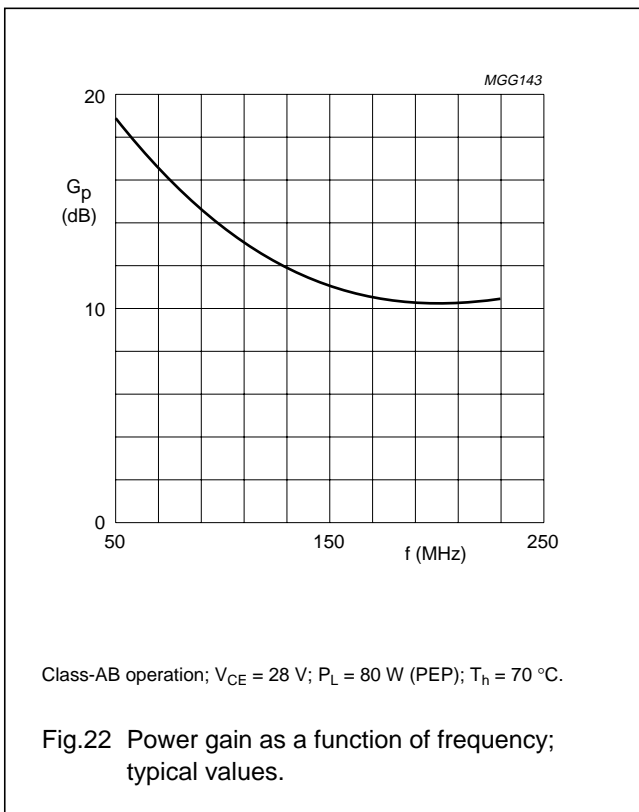
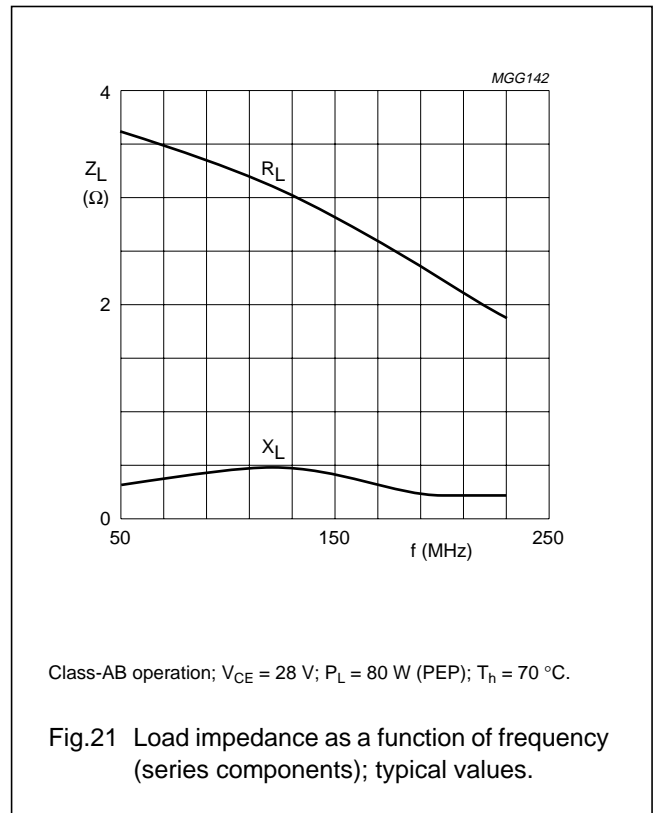
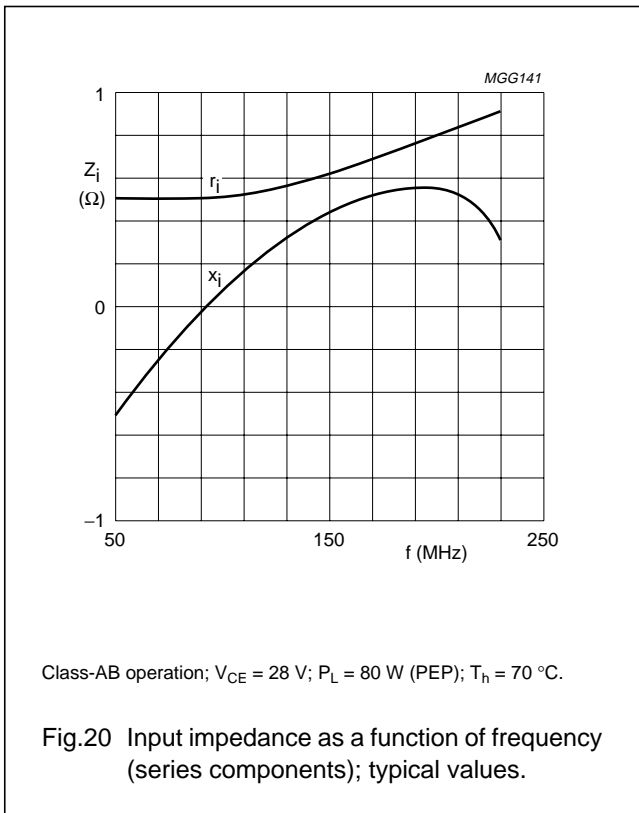


**Ruggedness in class-AB operation**

The BLV33F is capable of withstanding a full load mismatch corresponding to VSWR  $\leq 2$  through all phases) up to 60 W (RMS) and 85 W (PEP) under the following conditions:  $V_{CE} = 28 \text{ V}; T_h = 70 \text{ }^\circ\text{C}; f = 224.25 \text{ MHz}; R_{th\text{ mb-h}} = 0.2 \text{ K/W.}$

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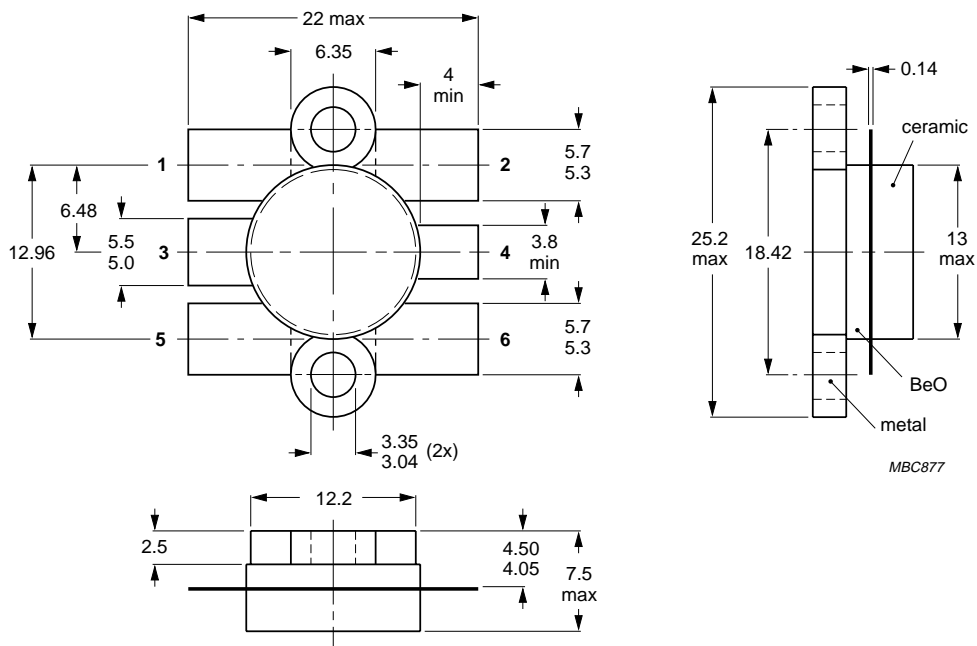




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PACKAGE OUTLINE



Dimensions in mm.  
 Torque on screw: min. 0.6 Nm; max. 0.75 Nm.  
 Recommended screw: cheese-head 4-40 UNC/2A.  
 Heatsink compound must be applied sparingly and evenly distributed.

Fig.23 SOT119A.

## VHF linear power transistor

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**DEFINITIONS**

<b>Data Sheet Status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	

**LIFE SUPPORT APPLICATIONS**

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

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# Philips Semiconductors – a worldwide company

**Argentina:** see South America

**Australia:** 34 Waterloo Road, NORTH RYDE, NSW 2113,  
Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

**Austria:** Computerstr. 6, A-1101 WIEN, P.O. Box 213,  
Tel. +43 1 60 101, Fax. +43 1 60 101 1210

**Belarus:** Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,  
220050 MINSK, Tel. +375 172 200 733, Fax. +375 172 200 773

**Belgium:** see The Netherlands

**Brazil:** see South America

**Bulgaria:** Philips Bualgaria Ltd., Energoproject, 15th floor,  
51 James Bourchier Blvd., 1407 SOFIA,  
Tel. +359 2 689 211, Fax. +359 2 689 102

**Canada:** PHILIPS SEMICONDUCTORS/COMPONENTS,  
Tel. +1 800 234 7381

**China/Hong Kong:** 501 Hong Kong Industrial Technology Centre,  
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,  
Tel. +852 2319 7888, Fax. +852 2319 7700

**Colombia:** see South America

**Czech Republic:** see Austria

**Denmark:** Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S,  
Tel. +45 32 88 2636, Fax. +45 31 57 1949

**Finland:** Sinikalliontie 3, FIN-02630 ESPOO,  
Tel. +358 9 615800, Fax. +358 9 61580/xxx

**France:** 4 Rue du Port-aux-Vins, BP317, 92156 SURESNES Cedex,  
Tel. +33 1 40 99 6161, Fax. +33 1 40 99 6427

**Germany:** Hammerbrookstraße 69, D-20097 HAMBURG,  
Tel. +49 40 23 53 60, Fax. +49 40 23 536 300

**Greece:** No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,  
Tel. +30 1 4894 339/239, Fax. +30 1 4814 240

**Hungary:** see Austria

**India:** Philips INDIA Ltd, Shivsagar Estate, A Block, Dr. Annie Besant Rd.  
Worli, MUMBAI 400 018, Tel. +91 22 4938 541, Fax. +91 22 4938 722

**Indonesia:** see Singapore

**Ireland:** Newstead, Clonskeagh, DUBLIN 14,  
Tel. +353 1 7640 000, Fax. +353 1 7640 200

**Israel:** RAPAC Electronics, 7 Kehilat Saloniki St, TEL AVIV 61180,  
Tel. +972 3 645 0444, Fax. +972 3 649 1007

**Italy:** PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3,  
20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

**Japan:** Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,  
Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

**Korea:** Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,  
Tel. +82 2 709 1412, Fax. +82 2 709 1415

**Malaysia:** No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,  
Tel. +60 3 750 5214, Fax. +60 3 757 4880

**Mexico:** 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,  
Tel. +9-5 800 234 7381

**Middle East:** see Italy

**Netherlands:** Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,  
Tel. +31 40 27 82785, Fax. +31 40 27 88399

**New Zealand:** 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,  
Tel. +64 9 849 4160, Fax. +64 9 849 7811

**Norway:** Box 1, Manglerud 0612, OSLO,  
Tel. +47 22 74 8000, Fax. +47 22 74 8341

**Philippines:** Philips Semiconductors Philippines Inc.,  
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,  
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

**Poland:** Ul. Lukiska 10, PL 04-123 WARSZAWA,  
Tel. +48 22 612 2831, Fax. +48 22 612 2327

**Portugal:** see Spain

**Romania:** see Italy

**Russia:** Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,  
Tel. +7 095 247 9145, Fax. +7 095 247 9144

**Singapore:** Lorong 1, Toa Payoh, SINGAPORE 1231,  
Tel. +65 350 2538, Fax. +65 251 6500

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**South Africa:** S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,  
2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000,  
Tel. +27 11 470 5911, Fax. +27 11 470 5494

**South America:** Rua do Rocio 220, 5th floor, Suite 51,  
04552-903 São Paulo, SÃO PAULO - SP, Brazil,  
Tel. +55 11 821 2333, Fax. +55 11 829 1849

**Spain:** Balmes 22, 08007 BARCELONA,  
Tel. +34 3 301 6312, Fax. +34 3 301 4107

**Sweden:** Kottbygatan 7, Akalla, S-16485 STOCKHOLM,  
Tel. +46 8 632 2000, Fax. +46 8 632 2745

**Switzerland:** Allmendstrasse 140, CH-8027 ZÜRICH,  
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**Turkey:** Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,  
Tel. +90 212 279 2770, Fax. +90 212 282 6707

**Ukraine:** PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,  
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

**United Kingdom:** Philips Semiconductors Ltd., 276 Bath Road, Hayes,  
MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

**United States:** 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,  
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