DISCRETE SEMICONDUCTORS

DATA SHEET

BLV99/SLUHF power transistor

Product specification

September 1991





BLV99/SL

FEATURES

- Emitter-ballasting resistors for an optimum temperature profile
- Gold metallization ensures excellent reliability.

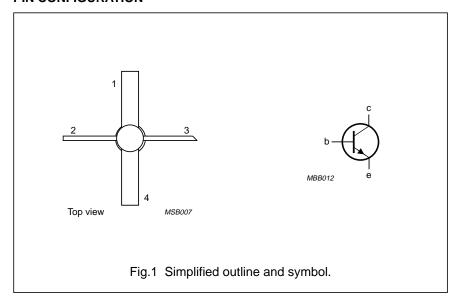
DESCRIPTION

NPN silicon planar epitaxial transistor encapsulated in a 4-lead SOT172D envelope with a ceramic cap. It is designed primarily for use as a driver stage in base stations in the 900 MHz communications band. All leads are isolated from the mounting base.

PINNING - SOT172D

PIN	DESCRIPTION			
1	emitter			
2	base			
3	collector			
4	emitter			

PIN CONFIGURATION



WARNING

Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

QUICK REFERENCE DATA

RF performance at T_{mb} = 25 $^{\circ}C$ in a common emitter class-B test circuit.

MODE OF	f	V _{CE}	P _L	G _p	η _c
OPERATION	(MHz)	(V)	(W)	(dB)	(%)
c.w. narrow band	900	24	2	> 8	> 55

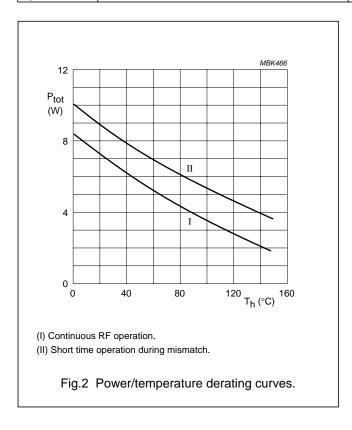
UHF power transistor

BLV99/SL

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	50	V
V _{CEO}	collector-emitter voltage	open base	_	27	V
V _{EBO}	emitter-base voltage	open collector	_	3.5	V
I _C	collector current	DC value	_	200	mA
I _{CM}	collector current	peak value f > 1 MHz	_	600	mA
P _{tot}	total power dissipation	f > 1 MHz; T _{mb} = 50 °C	_	6	W
T _{stg}	storage temperature range		-65	150	°C
Tj	junction operating temperature		_	200	°C



THERMAL RESISTANCE

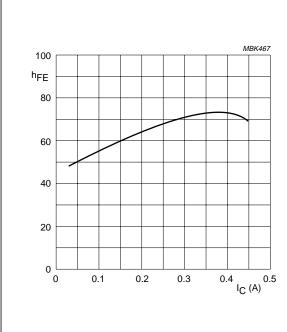
SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
R _{th j-mb(RF)}	from junction to mounting base	$P_L = 4.5 \text{ W}; T_{mb} = 25 ^{\circ}\text{C}$	20	K/W

BLV99/SL

CHARACTERISTICS

 $T_j = 25 \, ^{\circ}C$.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{(BR)CBO}	collector-base breakdown voltage	open emitter; I _C = 5 mA	50	_	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$V_{BE} = 0;$ $I_{C} = 10 \text{ mA}$	27	_	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	open collector; I _E = 0.5 mA	3.5	_	-	V
I _{CES}	collector-emitter leakage current	V _{BE} = 0; V _{CE} = 27 V	_	_	2	mA
h _{FE}	DC current gain	V _{CE} = 20 V; I _C = 150 mA	25	_	-	
E _{SBR}	second breakdown energy	$L = 25 \text{ mH};$ $R_{BE} = 10 \Omega;$ $f = 50 \text{ Hz}$	0.5	-	_	mJ
C _c	collector capacitance	$V_{CB} = 24 \text{ V};$ $I_E = I_e = 0;$ $f = 1 \text{ MHz}$	_	3	_	pF
C _{re}	feedback capacitance	$V_{CE} = 24 \text{ V};$ $I_{C} = 0;$ $f = 1 \text{ MHz}$	-	1.3	-	pF



 V_{CE} = 20 V; T_j = 25 °C.

Fig.3 DC current gain as a function of collector current, typical values.

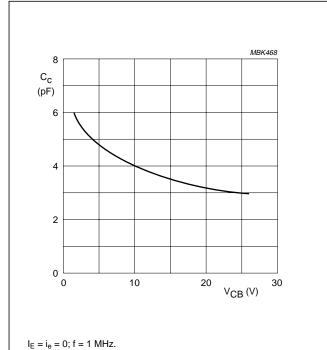


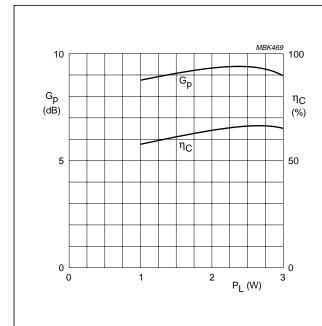
Fig.4 Collector capacitance as a function of collector-base voltage, typical values.

BLV99/SL

APPLICATION INFORMATION

RF performance T_{mb} = 25 °C in a common emitter class-B test circuit.

MODE OF OPERATION	f	V _{CE}	P _L	G _p	η _c
	(MHz)	(V)	(W)	(dB)	(%)
c.w. narrow band	900	24	2	> 8 typ. 9.3	> 55 typ. 63



Class-B operation; V_{CE} = 24 V; f = 900 MHz; T_{mb} = 25 °C.

Fig.5 Gain and efficiency as functions of load power, typical values.

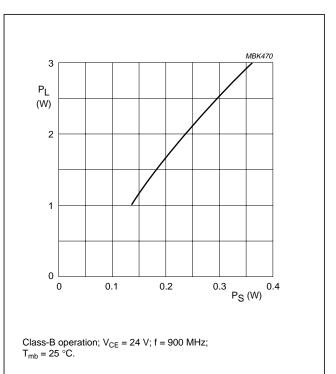


Fig.6 Load power as a function of drive power, typical values.

Ruggedness in class-B operation

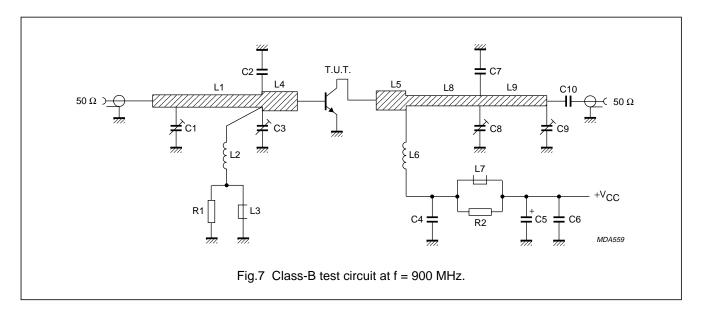
The BLV99/SL is capable of withstanding a full load mismatch corresponding to VSWR = 50:1 through all phases under the following conditions:

 $V_{CE} = 24 \text{ V}, f = 900 \text{ MHz},$

 T_{mb} = 25 °C, and rated output power.

UHF power transistor

BLV99/SL



List of components (see test circuit)

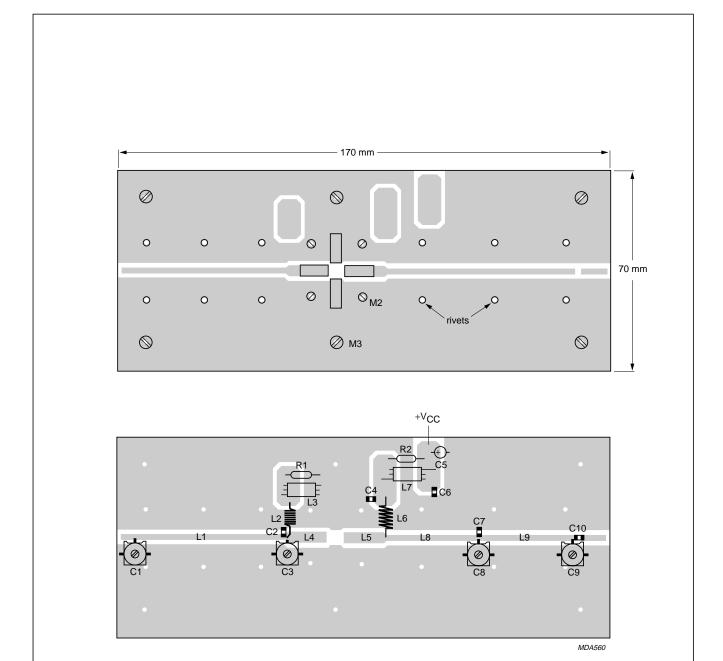
COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C1, C3, C8, C9	film dielectric trimmer	1.4 to 5.5 pF		2222 809 09001
C2	multilayer ceramic chip capacitor (note 1)	4.7 pF		
C4, C6, C10	multilayer ceramic chip capacitor	220 pF		
C5	63 V electrolytic capacitor	1 μF		
C7	multilayer ceramic chip capacitor (note 1)	2.2 pF		
L1	stripline (note 2)	50 Ω	48 mm × 2.4 mm	
L2	7 turns enamelled 0.4 mm copper wire	50 nH	int. dia. 2 mm; leads 2 × 5 mm	
L3, L7	grade 3B Ferroxcube wideband HF choke			4312 020 36642
L4, L5	stripline (note 2)	35 Ω	14 mm × 4 mm;	
L6	6 turns enamelled 1 mm copper wire	120 nH	int. dia. 6 mm; length 10 mm; leads 2 × 5 mm	
L8	stripline (note 2)	50 Ω	31 mm × 2.4 mm	
L9	stripline (note 2)	50 Ω	29 mm × 2.4 mm	
R1, R2	0.4 W metal film resistor	10 Ω, 5%		

Notes

- 1. American Technical Ceramics type 100A or capacitor of the same quality.
- 2. The striplines are on a double copper-clad printed circuit board, with PTFE fibre-glass dielectric (ϵ_r = 2.2), thickness 1/32 inch.

UHF power transistor

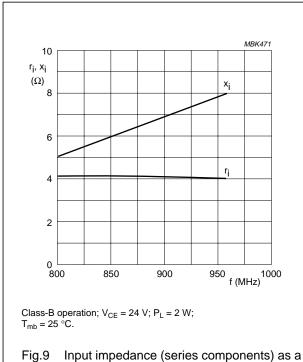
BLV99/SL



The components are mounted on one side of a copper clad PTFE fibre-glass board; the other side is unetched and serves as a ground plane. Earth connections from the component side to the ground plane are made by fixing screws, hollow rivets and copper straps under the emitters.

Fig.8 Component layout for 900 MHz class-B test circuit.

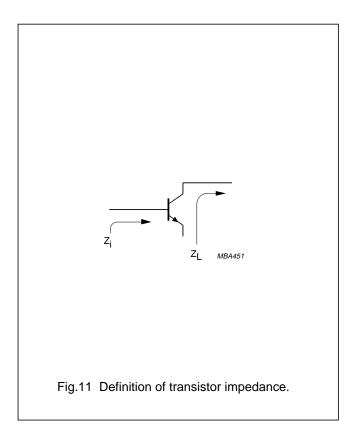
BLV99/SL

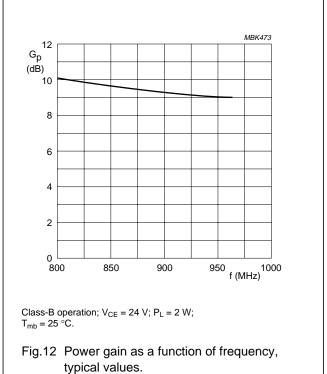


80
R_L, X_L
(Ω)
60
40
40
20
800
850
900
950
f (MHz)
1000
Class-B operation; V_{CE} = 24 V; P_L = 2 W; T_{mb} = 25 °C.

Input impedance (series components) as a function of frequency, typical values.

Fig.10 Load impedance (series components) as a function of frequency, typical values.



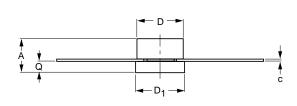


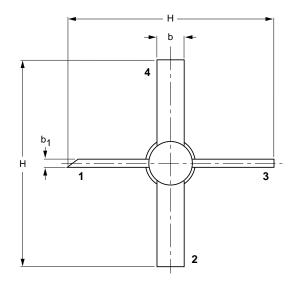
BLV99/SL

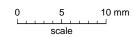
PACKAGE OUTLINE

Studless ceramic package; 4 leads

SOT172D







DIMENSIONS (millimetre dimensions are derived from the original inch dimensions)

UNIT	A	b	b ₁	С	D	D ₁	н	Q
mm	3.71 2.89	3.31 3.04	0.89 0.63	0.16 0.10	5.20 4.95	5.33 5.08	26.17 24.63	1.15 0.88
inches	0.146 0.114	0.13 0.12	0.035 0.025	0.006 0.004	0.205 0.195		1.03 0.97	0.045 0.035

OUTLINE		REFERENCES EUROPEAN ISSUE			ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT172D						97-06-28

UHF power transistor

BLV99/SL

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.