Breakover diodes

BR211SM series

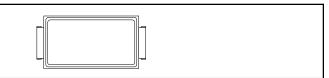
GENERAL DESCRIPTION

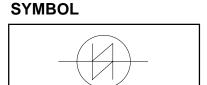
TION QUICK REFERENCE DATA

A range of bidirectional, breakover diodes in a two terminal, surface mounting, plastic envelope. These devices feature controlled breakover voltage and high holding current together with high peak current handling capability. Typical application is transient overvoltage protection in telecommunications equipment.

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V _(BO) I _H I _{TSM}	BR211SM-140 to BR211SM-280 Breakover voltage Holding current Non-repetitive peak current	140 150 -	280 - 40	V mA A

OUTLINE - SOD106





LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_D	Continuous voltage		-	75% of	V
I _{TSM1}	Non repetitive peak current	10/320 μs impulse equivalent to 10/700 μs, 1.6 kV voltage impulse (CCITT K17)	-	V _{(BO)typ} 40	А
I _{TSM2}	Non repetitive on-state current	half sine wave; t = 10 ms; T _i = 70 °C prior to surge	-	15	A
l ² t	I ² t for fusing	$t_{p} = 10 \text{ ms}^{-1}$	-	1.1	A ² s
dl _⊤ /dt	Rate of rise of on-state current after V _(BO) turn-on	$t_p^r = 10 \mu s$	-	50	A/μs
P_{tot}	Continuous dissipation	$T_a = 25^{\circ}C$ $t_p = 1 \text{ ms; } T_a = 25^{\circ}C$	-	1.2	W
P _{-M}	Peak dissipation	$t_p = 1 \text{ ms}; T_a = 25^{\circ}\text{C}$	-	50	W
T_{stg}	Storage temperature	i e	- 40	150	°C
T_{a}	Operating ambient temperature	off-state	-	70	°C
T _{stg} T _a T _{vj}	Overload junction temperature	on-state	-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-sp}	Thermal resistance junction to solder point		-	-	12	K/W
R _{th j-a}		pcb mounted; minimum footprint	-	100	-	K/W
Z _{th j-a}	Thermal impedance junction to ambient	$t_p = 1 \text{ ms}$	-	2.62	1	K/W

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STATIC CHARACTERISTICS

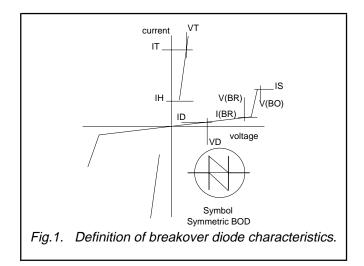
 $T_i = 25$ °C unless otherwise stated

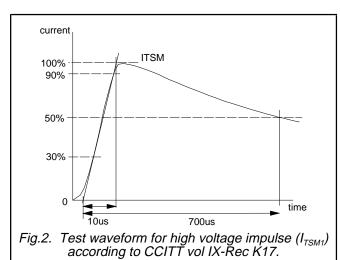
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{TM}^{-1}	On-state voltage	I _{TM} = 2 A	-	-	2.5	V
V _(BR)	Avalanche voltage (min)	$I_{(BR)}^{(m)} = 10_{mA}$ $I \le I_S$, $t_p = 100 \mu s$				
$V_{(BO)}$	Breakover voltage (max)	$ I \le I_S, t_p = 100 \ \mu S$				
, ,		BR211SM-140	123	140	157	V
		BR211SM-160	140	160	180	V
		BR211SM-180	158	180	202	V
		BR211SM-200	176	200	224	V
		BR211SM-220	193	220	247	V
		BR211SM-240	211	240	269	V
		BR211SM-260	228	260	292	V
		BR211SM-280	246	280	314	V
S _(br)	Temperature coefficient of V _(BR)		-	+0.1	-	%/K
$\left \begin{array}{l} S_{(br)} \\ I_{H}^{2} \end{array}\right $	Holding current	$T_i = 25^{\circ}C$	150	-	-	mΑ
		$ T_i = 70^{\circ}C$	100	-	-	mΑ
$I_{\rm S}^3$	Switching current	t _o '= 100 μs	10	200	1000	mΑ
I _S ³ I _D	Off-state current	$\dot{V}_{D} = 85\% V_{(BR)min}, T_{j} = 70^{\circ}C$	-	-	10	μΑ

DYNAMIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Linear rate of rise of off-state voltage that will not trigger any device	$V_{(DM)} = 85\% V_{(BR)min}; T_j = 70 \text{ °C}$	ı	ı	2000	V/μs
C_{j}	Off-state capacitance	$V_D = 0 \text{ V}; f = 1 \text{ kHz to } 1 \text{ MHz}$	-	-	100	pF





¹ Measured under pulsed conditions to avoid excessive dissipation

² The minimum current at which the diode will remain in the on-state

³ The avalanche current required to switch the diode to the on-state

⁴ Measured at maximum recommended continuous voltage. Relative humidity < 65%.

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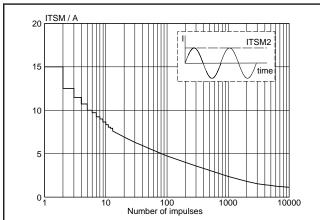


Fig.3. Maximum permissible non-repetitive on-state current based on sinusoidal currents; f = 50 Hz; device triggered at the start of each pulse; $T_j = 70 \,^{\circ}\text{C}$ prior to surge.

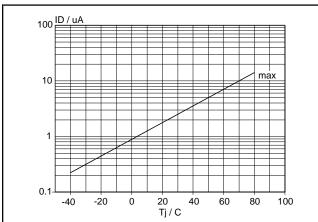


Fig.6. Maximum off-state current as a function of temperature.

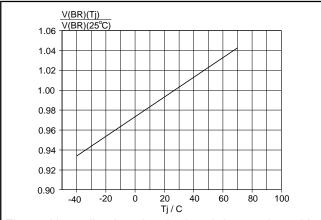


Fig.4. Normalised avalanche breakdown voltage $V_{(BR)}$ and $V_{(BO)}$ as a function of temperature.

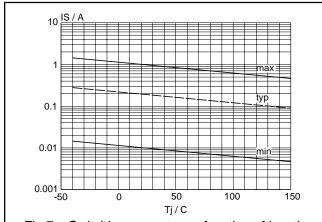


Fig.7. Switching current as a function of junction temperature.

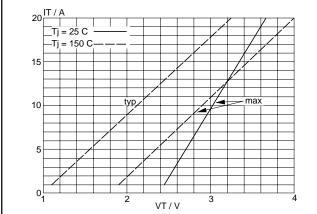


Fig.5. On-state current as a function of on-state voltage; $t_p = 200 \,\mu s$ to avoid excessive dissipation.

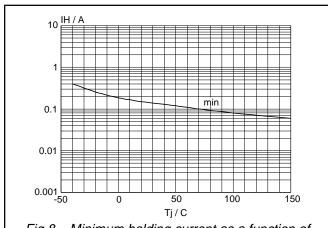


Fig.8. Minimum holding current as a function of temperature.

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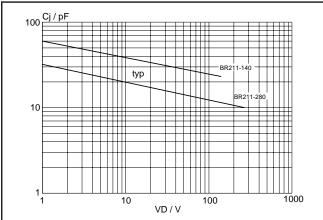


Fig.9. Typical junction capacitance as a function of off-state voltage, f = 1 MHz; $T_j = 25$ °C.

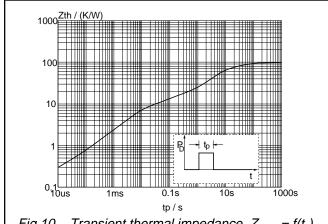
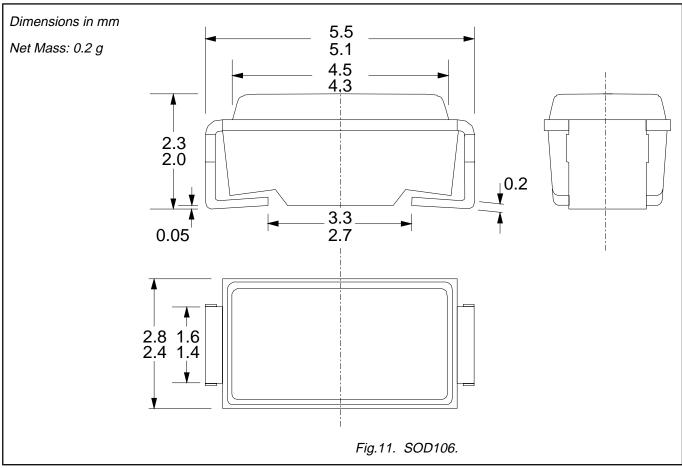


Fig. 10. Transient thermal impedance. $Z_{th j-a} = f(t_p)$.

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MECHANICAL DATA



Notes

1. For mounting and soldering instructions refer to publication SC18 "SMD Footprint Design and Soldering Guidelines". Order code:9397 750 00505.

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DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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