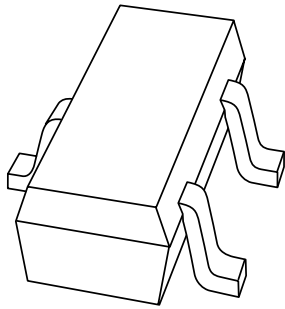


DATA SHEET



BFR92AT NPN 5 GHz wideband transistor

Preliminary specification

1999 Oct 18

NPN 5 GHz wideband transistor

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FEATURES

- High power gain
- Gold metallization ensures excellent reliability
- SOT416 (SC75) package.

APPLICATIONS

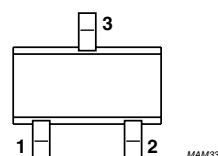
It is designed for use in RF amplifiers, mixers and oscillators with signal frequencies up to 1 GHz.

DESCRIPTION

Silicon NPN transistor encapsulated in a plastic SOT416 (SC75) package. The BFR92AT uses the same crystal as the SOT23 version, BFR92A.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



Marking code: P2.

Fig.1 SOT416

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–	20	V
V_{CEO}	collector-emitter voltage	open base	–	–	15	V
I_C	collector current (DC)		–	–	25	mA
P_{tot}	total power dissipation	up to $T_s = 93\text{ °C}$; note 1	–	–	300	mW
h_{FE}	current gain	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$	40	90	–	
C_{re}	feedback capacitance	$I_C = 0$; $V_{CE} = 10\text{ V}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	0.35	–	pF
f_T	transition frequency	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 500\text{ MHz}$	3.5	5	–	GHz
G_{UM}	maximum unilateral power gain	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	14	–	dB
		$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 2\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	8	–	dB
F	noise figure	$I_C = 5\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $\Gamma_s = \Gamma_{opt}$	–	2	–	dB
T_j	junction temperature		–	–	150	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

NPN 5 GHz wideband transistor

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	–	20	V
V _{CEO}	collector-emitter voltage	open base	–	15	V
V _{EBO}	emitter-base voltage	open collector	–	2	V
I _C	collector current (DC)		–	25	mA
P _{tot}	total power dissipation	up to T _s = 93 °C; see Fig.2; note 1	–	300	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	up to T _s = 93 °C; note 1	190	K/W

Note to the Limiting values and Thermal characteristics

1. T_s is the temperature at the soldering point of the collector pin.

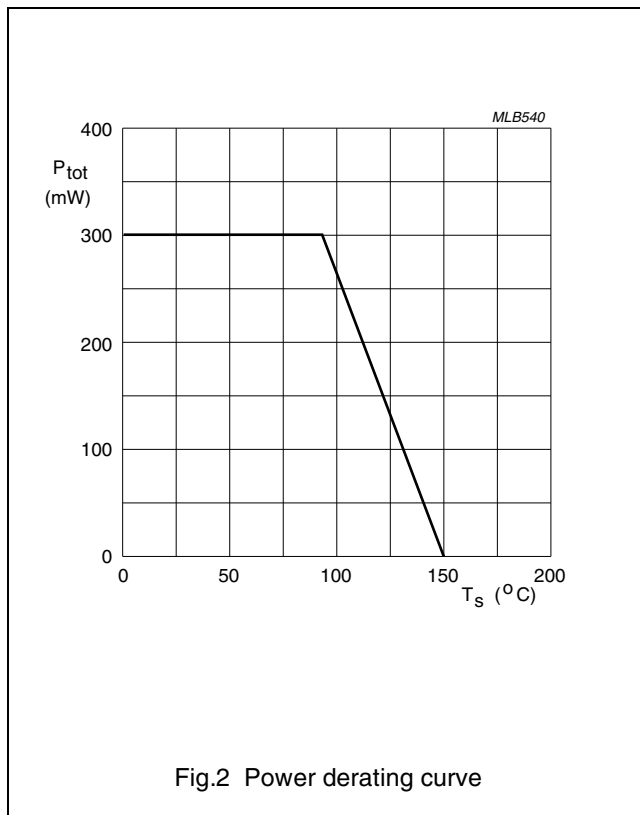


Fig.2 Power derating curve

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CHARACTERISTICST_j = 25 °C (unless otherwise specified).

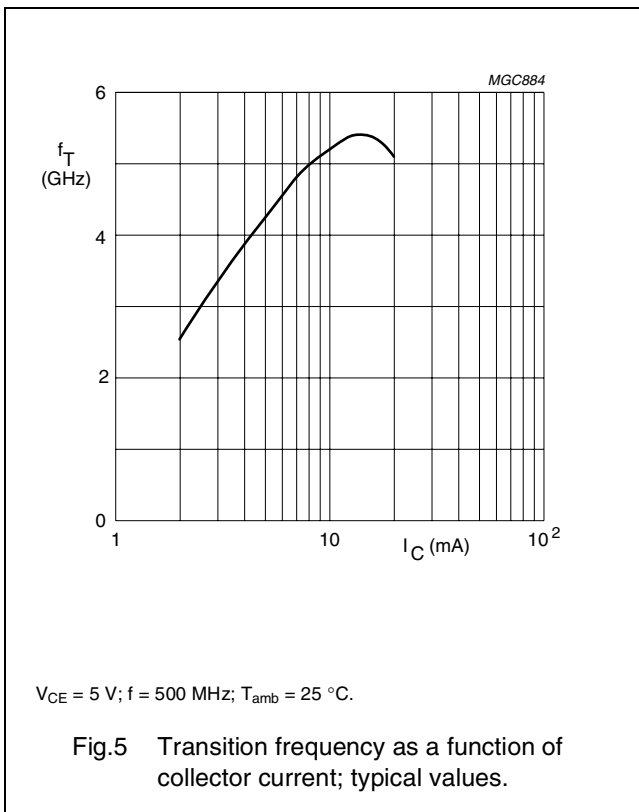
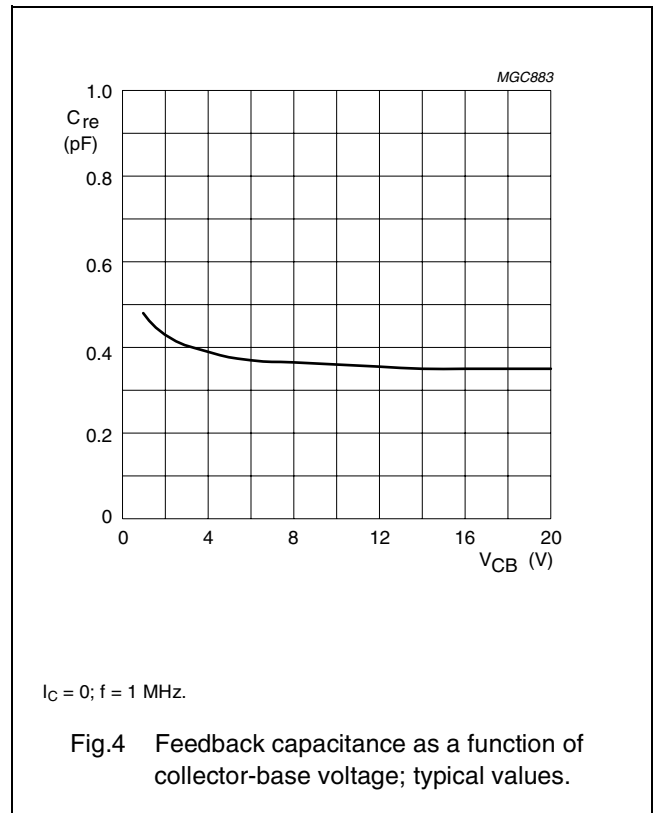
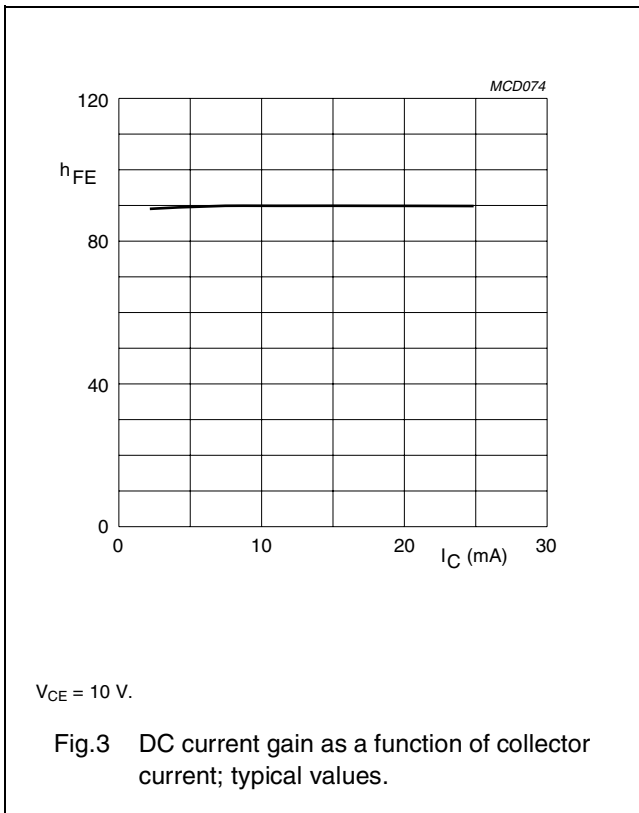
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector leakage current	I _E = 0; V _{CB} = 10 V	–	–	50	nA
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 10 V	40	90	–	
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	–	0.6	–	pF
C _e	emitter capacitance	I _C = i _c = 0; V _{EB} = 0.5 V; f = 1 MHz	–	0.9	–	pF
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 1 MHz	–	0.35	–	pF
f _T	transition frequency	I _C = 15 mA; V _{CE} = 10 V; f = 500 MHz	3.5	5	–	GHz
G _{UM}	maximum unilateral power gain; note 1	I _C = 15 mA; V _{CE} = 10 V; f = 1 GHz; T _{amb} = 25 °C	–	14	–	dB
		I _C = 15 mA; V _{CE} = 10 V; f = 2 GHz; T _{amb} = 25 °C	–	8	–	dB
F	noise figure	I _C = 5 mA; V _{CE} = 10 V; f = 1 GHz; Γ _s = Γ _{opt}	–	2	–	dB
		I _C = 5 mA; V _{CE} = 10 V; f = 2 GHz; Γ _s = Γ _{opt}	–	3	–	dB

Note

1. G_{UM} is the maximum unilateral power gain, assuming s₁₂ is zero and $G_{UM} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)}$ dB.

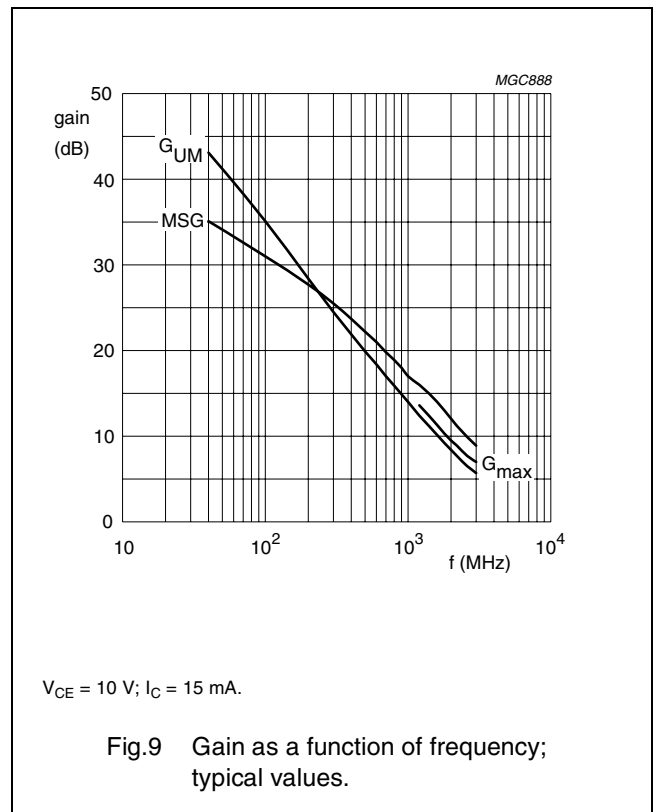
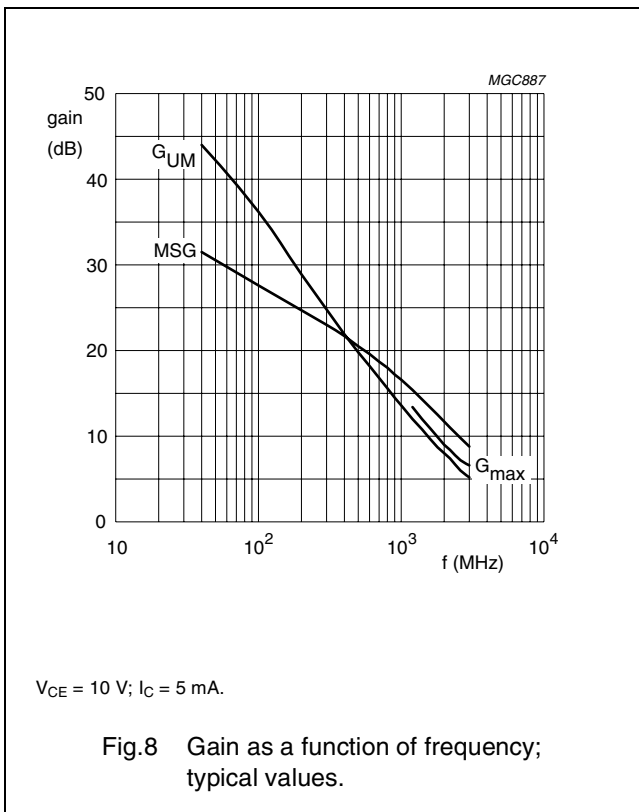
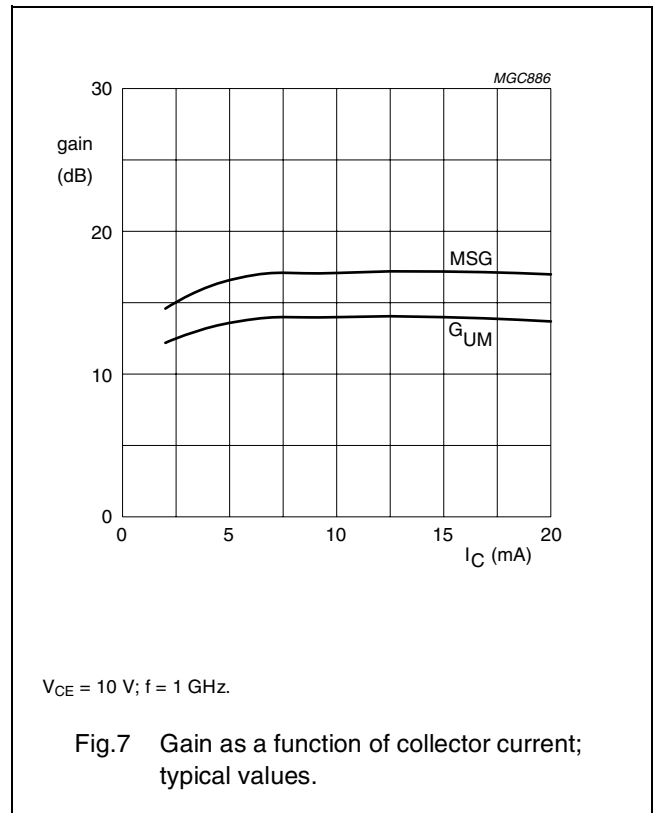
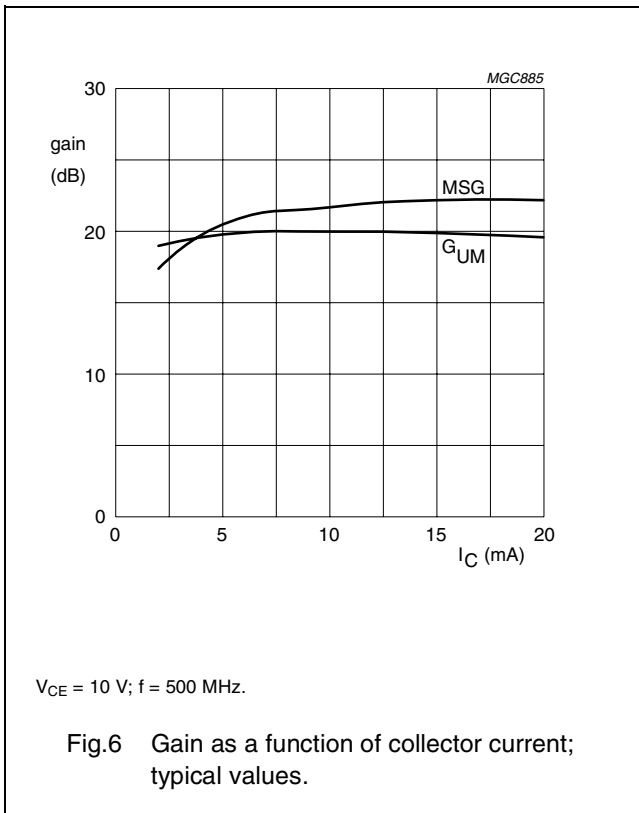
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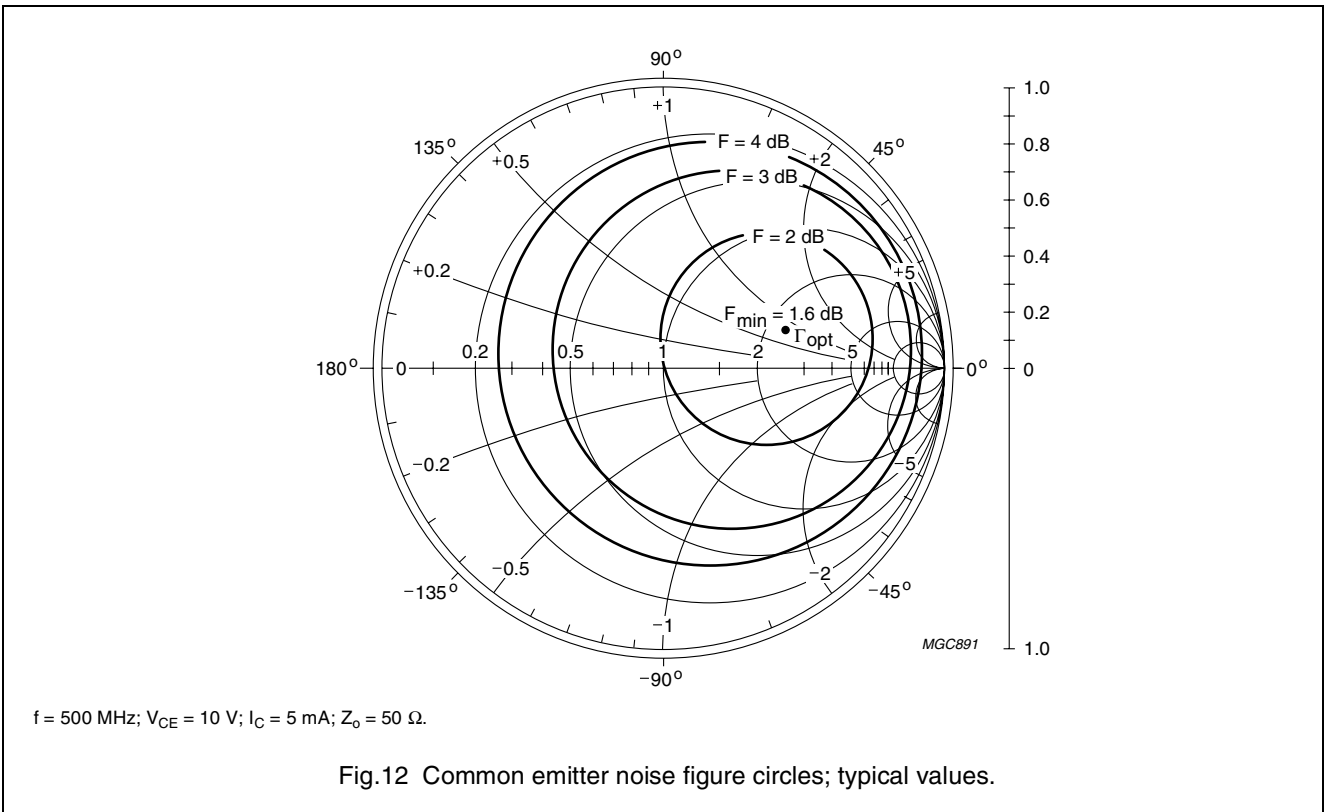
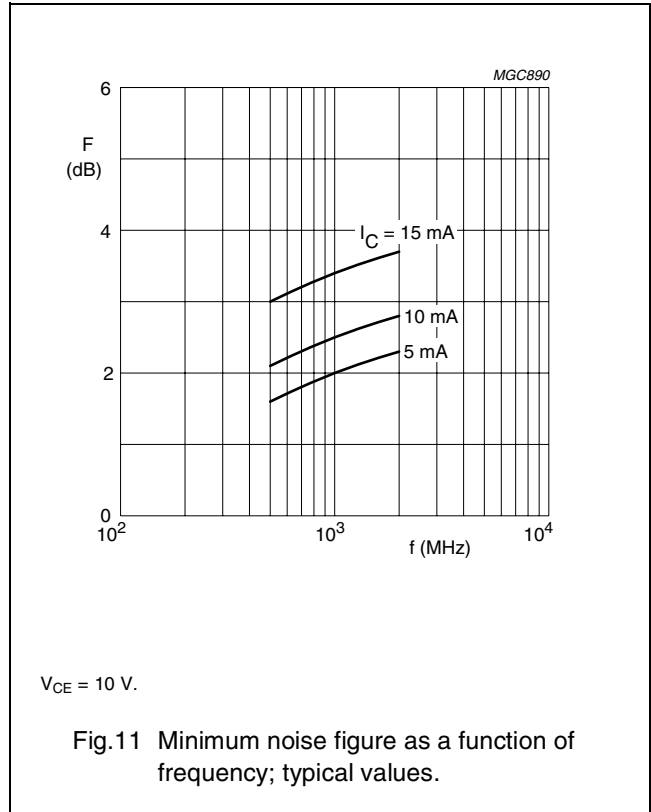
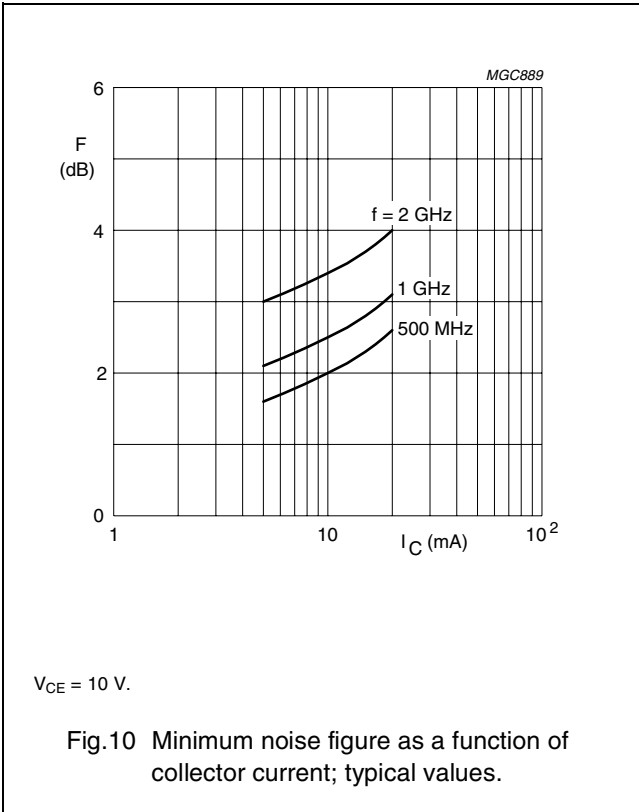
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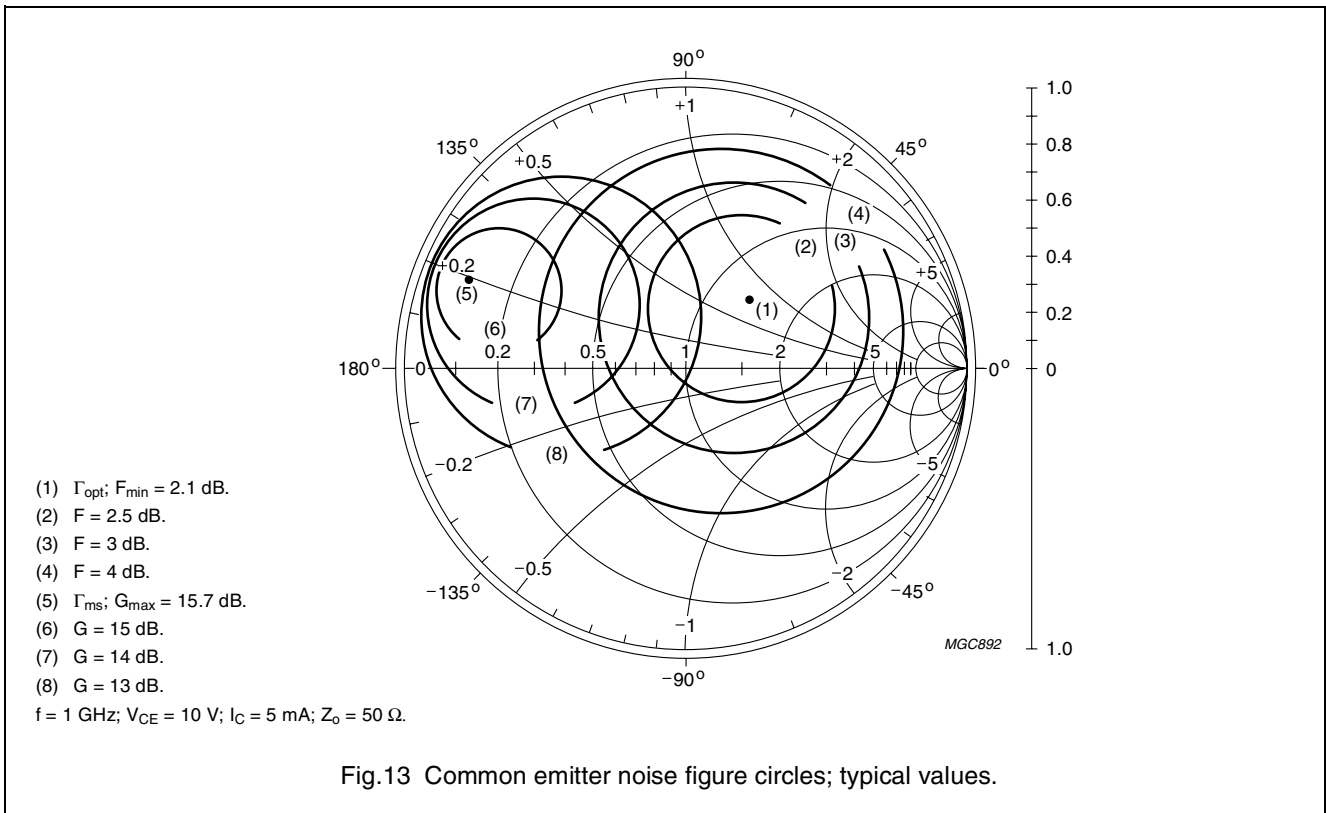


Fig.13 Common emitter noise figure circles; typical values.

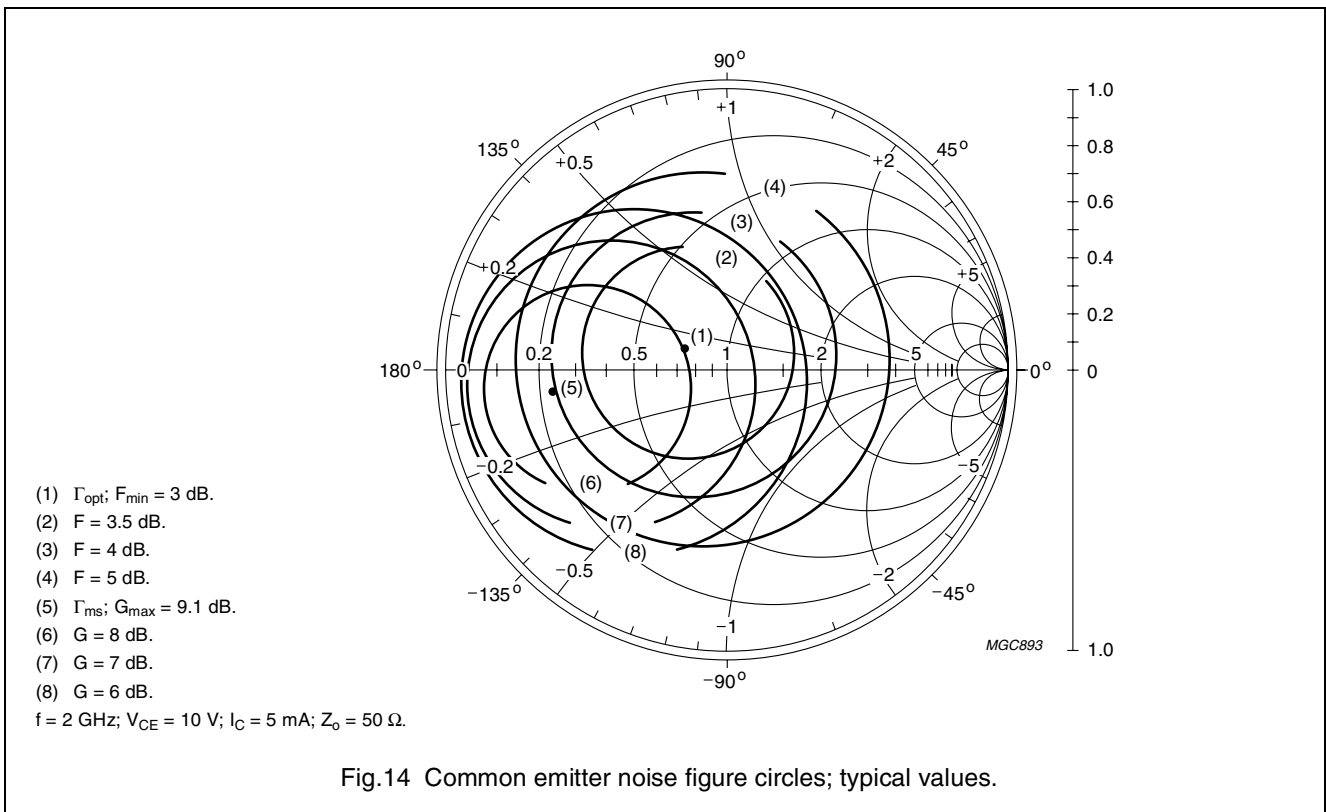
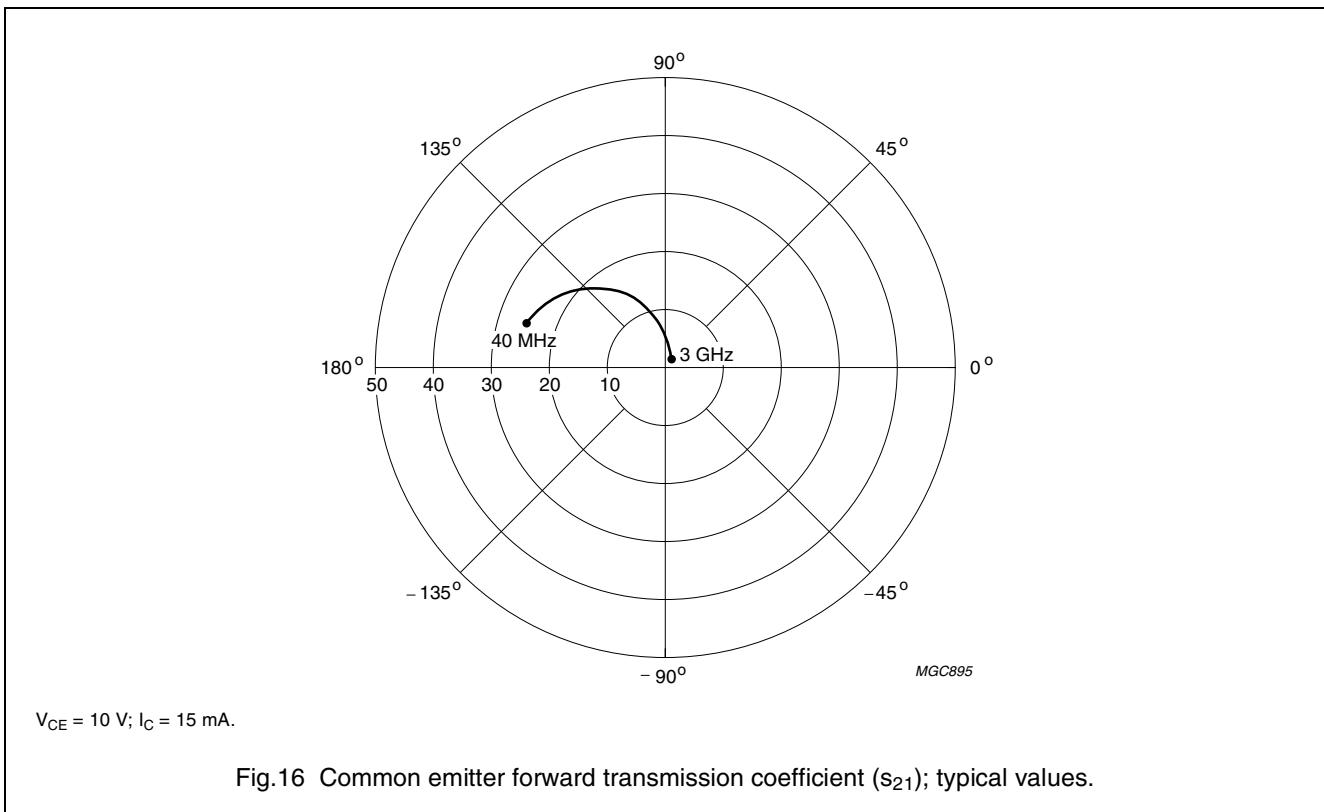
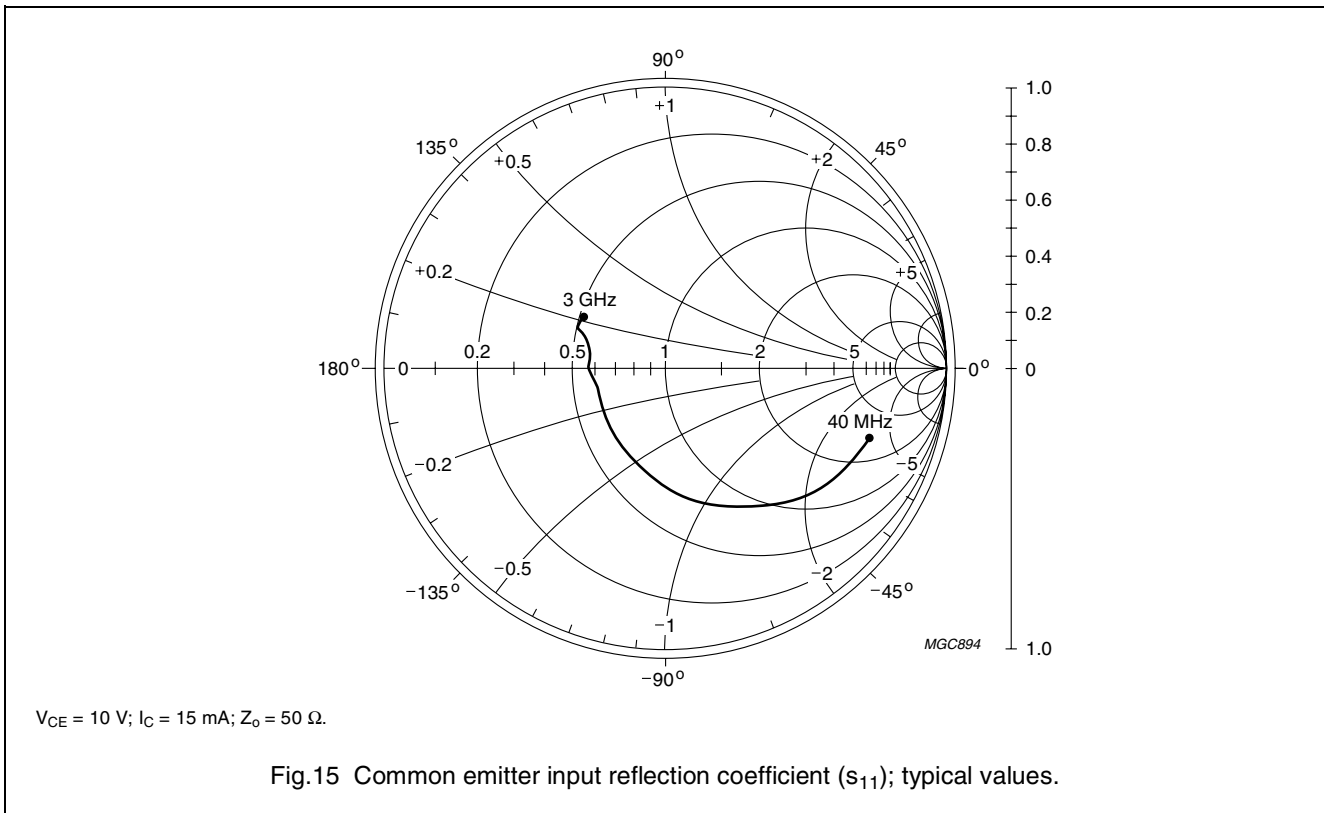


Fig.14 Common emitter noise figure circles; typical values.

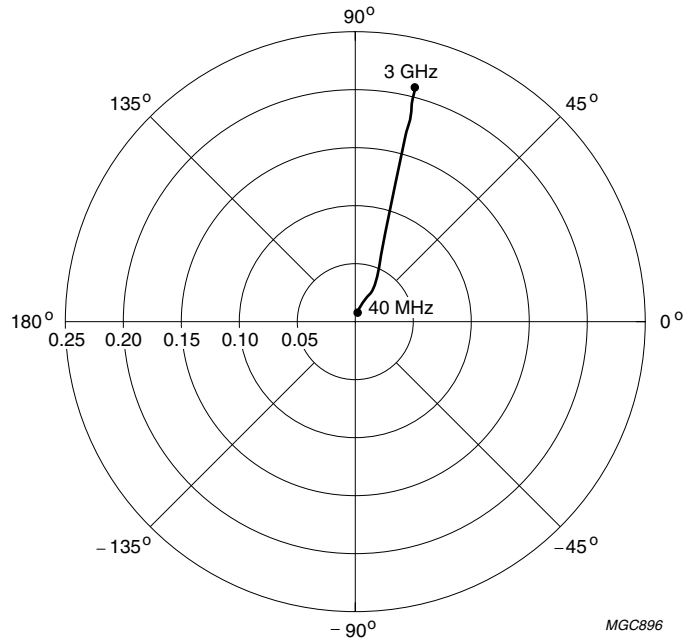
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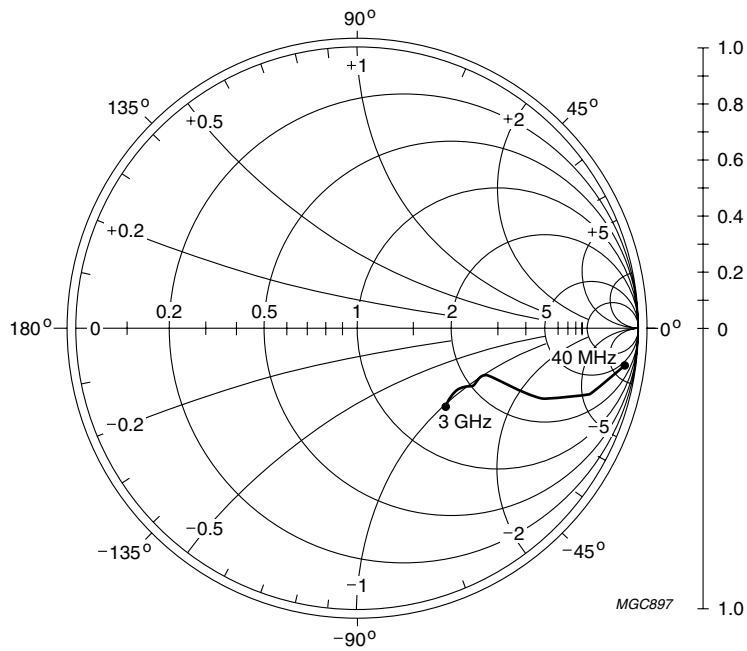
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$V_{CE} = 10\text{ V}; I_C = 15\text{ mA}$.

Fig.17 Common emitter reverse transmission coefficient (s_{12}); typical values.



$V_{CE} = 10\text{ V}; I_C = 15\text{ mA}; Z_o = 50\ \Omega$.

Fig.18 Common emitter output reflection coefficient (s_{22}); typical values.

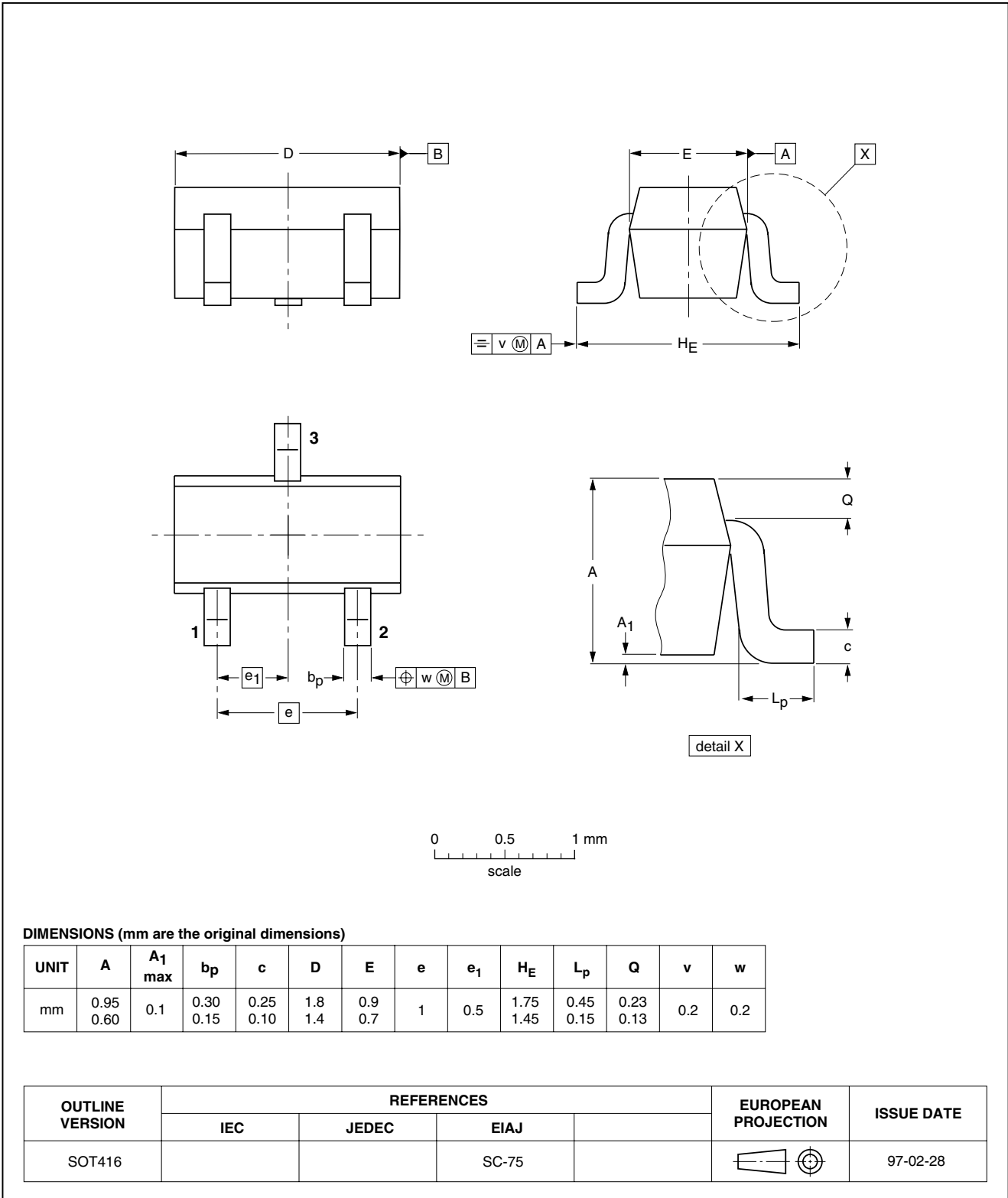
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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT416



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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Short-form specification	The data in this specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.
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