MAX.

800E

800F

800

16

140

UNIT

V

А

A

## Three quadrant triacs guaranteed commutation

### BTA216B series D, E and F

MAX.

600D

600E

600F

600

16

140

#### GENERAL DESCRIPTION

Passivated guaranteed commutation triacs in a plastic envelope suitable for surface mounting, intended for use in motor control circuits or with other highly inductive loads. These devices balance the requirements of commutation performance and gate sensitivity. The "sensitive gate" E series are intended for interfacing with low power drivers, including micro controllers.

#### **PINNING - SOT404**

## current

QUICK REFERENCE DATA

PARAMETER

voltäges

SYMBOL

V<sub>DRM</sub>

I<sub>T(RMS)</sub>

 $I_{TSM}$ 

#### SYMBOL

**BTA216B-**

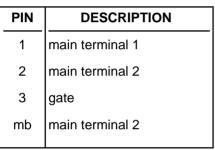
**BTA216B-**

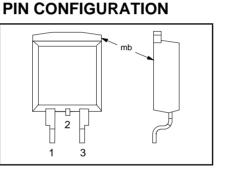
**BTA216B-**

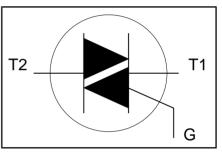
Repetitive peak off-state

Non-repetitive peak on-state

RMS on-state current







#### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MA	UNIT	
V <sub>DRM</sub>	Repetitive peak off-state voltages		-	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	v
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave; T <sub>mb</sub> ≤ 99 °C	-	16	3	A
I <sub>TSM</sub>	Non-repetitive peak on-state current	full sine wave; $T_j = 25$ °C prior to surge t = 20 ms t = 16.7 ms	-	14 15		A
l²t dl <sub>⊤</sub> /dt	I <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after triggering	t = 10  ms t = 10  ms $I_{TM} = 20 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu \text{s}$	-	98 10	3	Α <sup>2</sup> s Α/μs
I <sub>GM</sub> V <sub>GM</sub> P <sub>GM</sub> P <sub>G(AV)</sub>	Peak gate current Peak gate voltage Peak gate power Average gate power	over any 20 ms period	- - -	2 5 5 0.9		A V W W
T <sub>stg</sub> T <sub>j</sub>	Storage temperature Operating junction temperature	penou	-40 -	15 12		Ĵ Ĵ

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15  $A/\mu s$ .

# Three quadrant triacs guaranteed commutation

### BTA216B series D, E and F

#### THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-mb</sub> R <sub>th j-a</sub>	Thermal resistance junction to mounting base Thermal resistance junction to ambient	full cycle half cycle minimum footprint, FR4 board	- -	- - 55	1.2 1.7 -	K/W K/W K/W

#### STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.			UNIT
		BTA216B-			D	E	F	
I <sub>GT</sub> I <sub>L</sub>	Gate trigger current <sup>2</sup> Latching current	$V_{D} = 12 \text{ V}; \text{ I}_{T} = 0.1 \text{ A}$ T2+ G+ T2+ G- T2- G- $V_{D} = 12 \text{ V}; \text{ I}_{GT} = 0.1 \text{ A}$ T2+ G+	- - -	- - -	5 5 5 15	10 10 10 20	25 25 25 25	mA mA mA
		T2+ G- T2- G-	-	-	25 25	30 30	40 40	mA mA
I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm GT} = 0.1 \text{ A}$	-	-	15	25	30	mA
V <sub>T</sub> V <sub>GT</sub>	On-state voltage Gate trigger voltage	$ I_{T} = 20 \text{ A}  V_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}  V_{D} = 400 \text{ V}; I_{T} = 0.1 \text{ A};  T_{i} = 125 ^{\circ}\text{C} $	- - 0.25	1.2 0.7 0.4		1.5 1.5 -		V V V
I <sub>D</sub>	Off-state leakage current	$V_{D} = V_{DRM(max)};$ $T_{j} = 125 °C$	-	0.1		0.5		mA

#### **DYNAMIC CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise stated

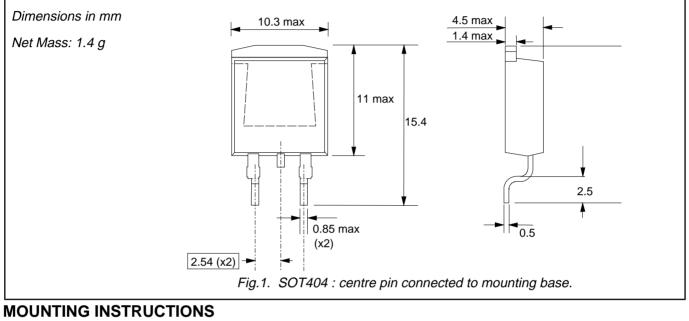
SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
		BTA216B-	D	E	F			
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)};$ $T_j = 110 °C; exponential waveform; gate open circuit$	30	60	70	-	-	V/µs
dl <sub>com</sub> /dt	Critical rate of change of commutating current	$V_{DM} = 400 \text{ V}; \text{ T}_{j} = 110 \text{ °C};$ $I_{T(RMS)} = 16 \text{ A};$ $dV_{com}/dt = 20v/\mu s;$ gate open circuit	1.8	3.5	4.5	-	-	A/ms
dl <sub>com</sub> /dt	Critical rate of change of commutating current		4.3	5.3	6.3	-	-	A/ms
t <sub>gt</sub>	Gate controlled turn-on time	$I_{TM} = 20 \text{ A}; V_D = V_{DRM(max)};$ $I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu s$	-	-	-	2	-	μs

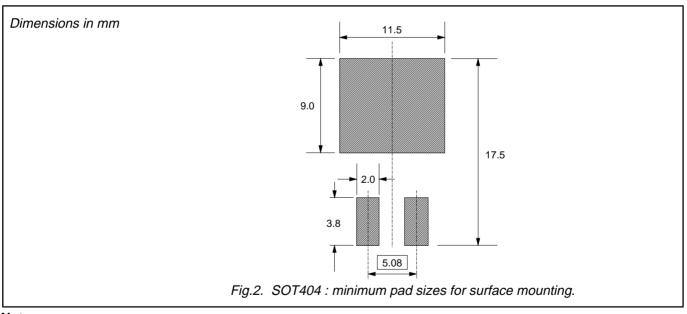
<sup>2</sup> Device does not trigger in the T2-, G+ quadrant.

## Three quadrant triacs guaranteed commutation

### BTA216B series D, E and F

#### **MECHANICAL DATA**





#### Notes

1. Plastic meets UL94 V0 at 1/8".

## Three quadrant triacs guaranteed commutation

### BTA216B series D, E and F

#### DEFINITIONS

Data sheet status					
Objective specificationThis data sheet contains target or goal specifications for product development.					
Preliminary specification	v specification This data sheet contains preliminary data; supplementary data may be published later				
Product specification	This data sheet contains final product specifications.				
Limiting values					
or more of the limiting val operation of the device at this specification is not in	in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one lues may cause permanent damage to the device. These are stress ratings only and t these or at any other conditions above those given in the Characteristics sections of pplied. Exposure to limiting values for extended periods may affect device reliability.				
Application information					
Where application information is given, it is advisory and does not form part of the specification.					
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