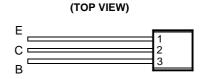
- 20 W Pulsed Power Dissipation
- 100 V Capability
- 2 A Continuous Collector Current
- 4 A Peak Collector Current



LP PACKAGE

MDTRAB

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	TIPP110		60	
Collector-base voltage (I _E = 0)	TIPP111	V_{CBO}	80	V
	TIPP112		100	
	TIPP110		60	
Collector-emitter voltage (I _B = 0)	TIPP111	V _{CEO}	80	V
	TIPP112		100	
Emitter-base voltage			5	V
Continuous collector current			2	Α
Peak collector current (see Note 1)			4	Α
Continuous base current			50	mA
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			0.8	W
Pulsed power dissipation (see Note 3)			20	W
Operating junction temperature range			-55 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C	
Lead temperature 3.2 mm from case for 10 seconds			260	°C

NOTES: 1. This value applies for $t_p \leq 0.3$ ms, duty cycle $\leq 10\%.$

- 2. Derate linearly to 150°C case temperature at the rate of 0.32 W/°C.
- 3. $V_{CE} = 20 \text{ V}$, $I_{C} = 1 \text{ A}$, $P_{W} = 10 \text{ ms}$, duty cycle $\leq 2\%$.



TIPP110, TIPP111, TIPP112 NPN SILICON POWER DARLINGTONS

MAY 1989 - REVISED MARCH 1997

electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT	
V _{(BR)CEO}	Collector-emitter breakdown voltage			TIPP110	60			
		$I_C = 10 \text{ mA}$	$I_B = 0$	TIPP111	80			V
		(see Note 4)		TIPP112	100			
I _{CEO}	Collector-emitter cut-off current	V _{CE} = 30 V	$V_{BE} = 0$	TIPP110			2	
		V _{CE} = 40 V	$V_{BE} = 0$	TIPP111			2	mA
		V _{CE} = 50 V	$V_{BE} = 0$	TIPP112			2	
I _{CBO}	Collector-base cut-off current	V _{CE} = 60 V	I _B = 0	TIPP110			1	
		$V_{CE} = 80 V$	$I_B = 0$	TIPP111			1	mA
		V _{CE} = 100 V	$I_B = 0$	TIPP112			1	
I _{EBO}	Emitter cut-off	V _{EB} = 5 V	I _C = 0				2	mA
	current	AEB - 2 A						IIIA
h _{FE}	Forward current	V _{CE} = 4 V	I _C = 1 A	(see Notes 4 and 5)	1000			
	transfer ratio	$V_{CE} = 4 V$	$I_C = 2 A$		500			
V _{CE(sat)}	Collector-emitter	I _B = 8 mA	I _C = 2 A	(see Notes 4 and 5)			2.5	V
	saturation voltage	IB - OWK					2.0	v
V _{BE}	Base-emitter	V _{CE} = 4 V	I _C = 2 A	(see Notes 4 and 5)			2.8	V
	voltage						2.0	•
V _{EC}	Parallel diode	I _E = 4 A	I _B = 0	(see Notes 4 and 5)			3.5	V
	forward voltage						0.0	V

NOTES: 4. These parameters must be measured using pulse techniques, t_p = 300 μ s, duty cycle \leq 2%.

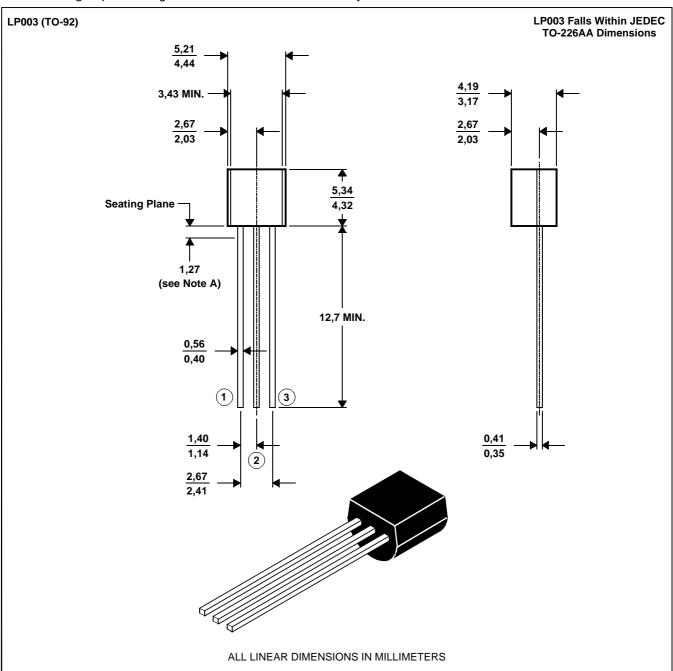
^{5.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts and located within 3.2 mm from device body.

MECHANICAL DATA

LP003 (TO-92)

3-pin cylindical plastic package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: Lead dimensions are not controlled in this area.

MDXXAX



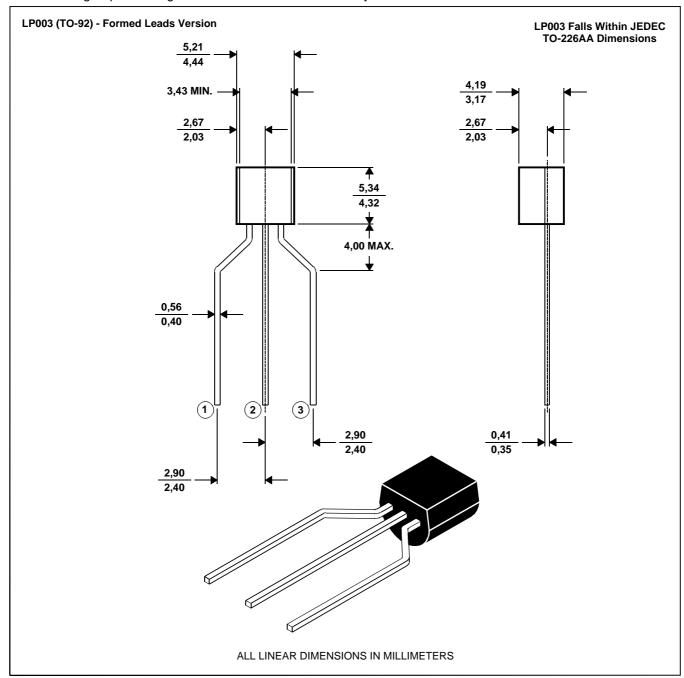
MAY 1989 - REVISED MARCH 1997

MECHANICAL DATA

LP003 (TO-92)

3-pin cylindical plastic package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.

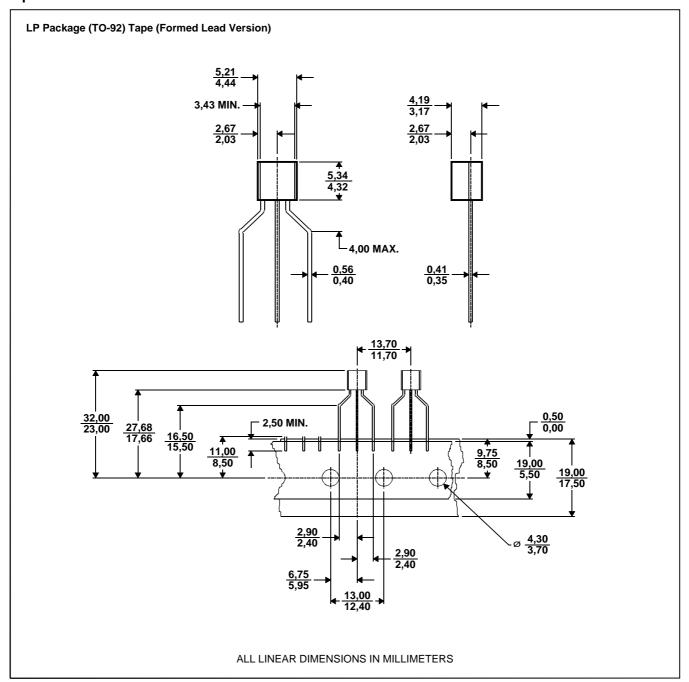


MDXXAR

PRODUCT INFORMATION

MECHANICAL DATA

LPR tape dimensions



MDXXAS



TIPP110, TIPP111, TIPP112 NPN SILICON POWER DARLINGTONS

MAY 1989 - REVISED MARCH 1997

IMPORTANT NOTICE

Power Innovations Limited (PI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to verify, before placing orders, that the information being relied on is current.

PI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with PI's standard warranty. Testing and other quality control techniques are utilized to the extent PI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except as mandated by government requirements.

PI accepts no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor is any license, either express or implied, granted under any patent right, copyright, design right, or other intellectual property right of PI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

PI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS.

Copyright © 1997, Power Innovations Limited