Power supply PCMCIA / flash memory BP5320

The BP5320 is a DC / DC converter for supplying power to PCMCIA flash memory. From a power supply (+5V) for PCMCIA operations, the IC supplies a voltage for programming operations (+12V). Compact and thin surface-mounted package with embossing tape for automatic mounting.

Applications

Personal computers, CD-ROM players, portable information devices, and other PCMCIA-slot equipped devices

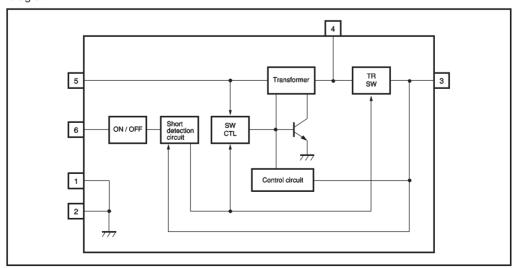
Features

- Designed to provide power to PCMCIA programming operations.
- 2) The 5V operating voltage is same as the IC memory card operating voltage.
- The large current enables power to be supplied to sound ICs.
- 4) Compact and thin SMD package.
- Supplied with embossing tape for automatic mounting by the mounter.
- 6) Built-in short-circuit protection circuit.

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vin	7.0	V
Output current	Іоυт	170	mA
Operating temperature	Topr	0~+60	c
Storage temperature	Tstg	−20~+80	°C

Block diagram



●Electrical characteristics (unless otherwise noted, Ta=25°C and Vctl=5V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VIN	4.5	5.0	5.5	V	
Output current	Іоит	_	_	150	mA	
Output voltage	Vоит	11.4	12.0	12.6	V	VIN=4.5~5.5V, VOUT=0~170mA
Ripple noise voltage	ν	_	_	200	mV _{P-P}	VIN=5V, IOUT=170mA
Efficiency	η	73	79	_	%	VIN=5V, IOUT=170mA
ON / OFF CTL voltage when ON	Vctl	3.3	_	6.0	V	V _{IN} =5V, V _{OUT} ≥11.4V
ON / OFF CTL voltage when OFF	VctL	_	_	0.4	V	V _{IN} =5V, Vouт≦0.3V
ON / OFF CTL current	Ість	_	_	0.2	mA	V _{IN} =5V

^{*} Measured with a band width of 20MHz.

Measurement circuit

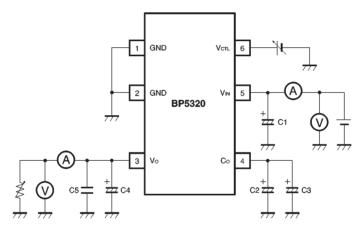


Fig.1

 $\begin{array}{lll} {\rm C1:22\,\mu\,F\,/\,10V} & {\rm (MSVB21A226M:NEC)} \\ {\rm C2:10\,\mu\,F\,/\,35V} & {\rm (MSVD21V106M:NEC)} \\ {\rm C3:47\,\mu\,F\,/\,16V} & {\rm (UZS1C470MCR1GB:NICHICON)} \\ {\rm C4:47\,\mu\,F\,/\,16V} & {\rm (UZS1C470MCR1GB:NICHICON)} \\ \end{array}$

(Ceramic capacitor)

Pin descriptions

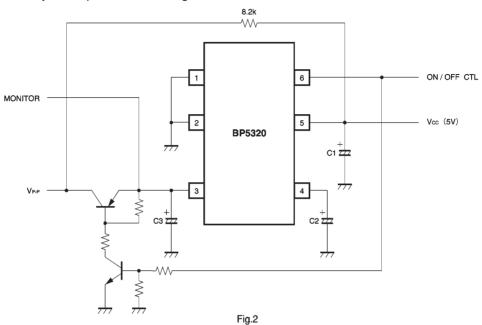
Pin No.	Pin name	Function		
1,2	GND	Ground		
3	V ouт	Output ; connect an output capacitor with a recommended capacitance off 2.2 μ F between this pin and GND		
4	Со	Output smoothing capacitor connection; connect a low-impedance capacitor with a recommended capacitance of 47 μ F between this pin and GND		
5	Vin	Input; connect a low-impedance capacitor with a recommended capacitance of 100 μ F between this pin and GND		
6	VcTL	Output ON / OFF control; output starts when the pin is HIGH level, and stops at Low level		

C5:0.1 µF

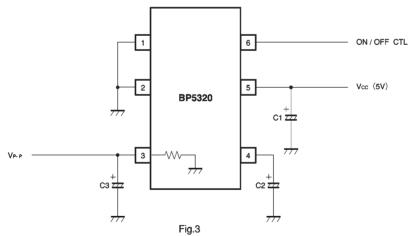
Regulator ICs BP5320

Application example

(1) Flash memory that requires 5V for reading



(2) Pull-down of V_{P-P}



The OFF output is pulled down through an effective resistance of $30k\Omega$.

Operation notes

- (1) Place I/O external capacitors as near as possible to the connection pins. In particular, make sure to minimize the impedance between the input-side capacitor (C1) and pin 5. a length less than 50mm is recommended for a copper foil of 1.0mm wide and 35μ m thick.
- (2) Avoid frequent switching using the ON/OFF CTL pin (five times per second at the maximum).

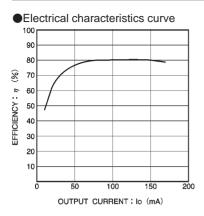
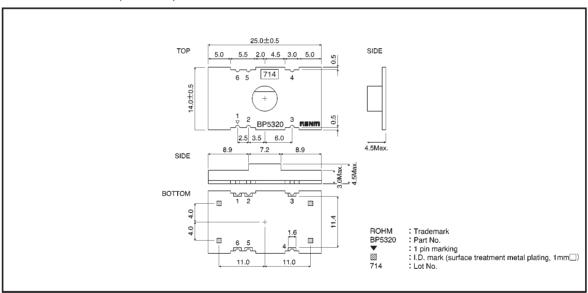


Fig. 4 Efficiency

External dimensions (Units: mm)



• The soldering used inside the unit is equivalent to H63 solder, so it will remelt during reflow. Be sure not to subject the unit to any vibrations when passing through the reflow furnace.