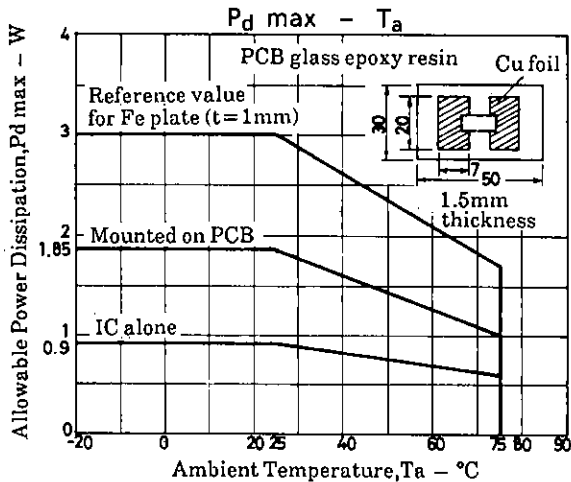


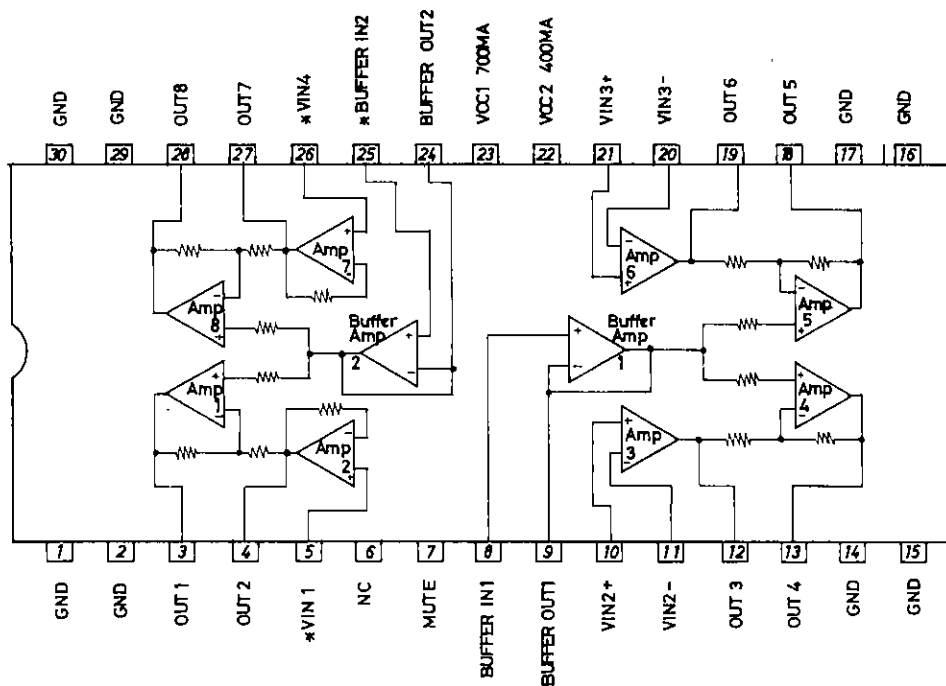
Continued from preceding page.

| | | | min | typ | max | unit |
|--|------------|------------------------------------|-----|--------------|-----|------|
| Buffer 1 Input-Output Voltage Difference | V_{BIO1} | Buffer amp 1 | -30 | | 30 | mV |
| Buffer 2 Input-Output Voltage Difference | V_{BIO2} | Buffer amp 2 | 0.5 | 0.6 | 0.8 | V |
| Amp 2 Input-Output Voltage Difference | V_{IO2} | Amp 2 | 0.5 | 0.6 | 0.8 | V |
| Amp 7 Input-Output Voltage Difference | V_{IO7} | Amp 7 | 0.5 | 0.6 | 0.8 | V |
| Input Bias Current | I_B | Note 6 | | 100 | 500 | nA |
| Buffer Input Voltage Range | V_{BICM} | Buffer amp | 1.5 | $V_{CC}-1.5$ | | V |
| Common-Mode Input Voltage Range | V_{ICM} | | 1.0 | $V_{CC}-1.5$ | | V |
| Output Source Voltage | V_{O1} | $R_L=8.0\Omega$ 700mA amp (Note 7) | 3.4 | 3.6 | | V |
| Output Sink Voltage | V_{O2} | $R_L=8.0\Omega$ 700mA amp (Note 8) | | 1.0 | 1.4 | V |
| Output Source Voltage | V_{O3} | $R_L=8.0\Omega$ 400mA amp (Note 7) | 2.8 | 3.4 | | V |
| Output Sink Voltage | V_{O4} | $R_L=8.0\Omega$ 400mA amp (Note 8) | | 1.6 | 2.2 | V |
| Closed-Circuit Voltage Gain | V_G | | | 6.0 | | dB |
| Output Limiting Voltage | V_{OL} | Amp 3, amp 6 | | 5.0 | | V |
| Muting Pin OFF-State Voltage | V_{Mute} | | | 2.2 | | V |
| Muting Pin OFF-State Current | I_{Mute} | | | 80 | | A |

- Note 1 Muting OFF. Buffer 22k Ω across V_{IN-} and V_O . V_{IN+} pin grounded
 - Note 2 Muting ON. Buffer 22k Ω across V_{IN-} and V_O . V_{IN+} pin grounded
 - Note 3 Muting OFF. Buffer 22k Ω across V_{IN-} and V_O . V_{IN+} pin connected to $1/2V_{CC}$
 - Note 4 Muting ON. Buffer 22k Ω across V_{IN-} and V_O . V_{IN+} pin connected to $1/2V_{CC}$
 - Note 5 For bridge amp, represents the difference between outputs.
 - Note 6 All V_{IN} connected to $1/2V_{CC}$. 100k Ω connected to the input. Measure the voltage difference. V_{IN} and V_O connected through 100k Ω . Measure the voltage difference between pins.
 - Note 7 Voltage (source) relative to GND when 8 Ω load is connected across outputs of bridge amp
 - Note 8 Voltage (sink) relative to GND when 8 Ω load is connected across outputs of bridge amp
- ※ : Be carefull in handling the LA6532M, because dielectric breakdown is liable to occur.



Equivalent Circuit Block Diagram



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