

**SANYO**

No. 4349A

**Dot Matrix LCD Driver**

## Overview

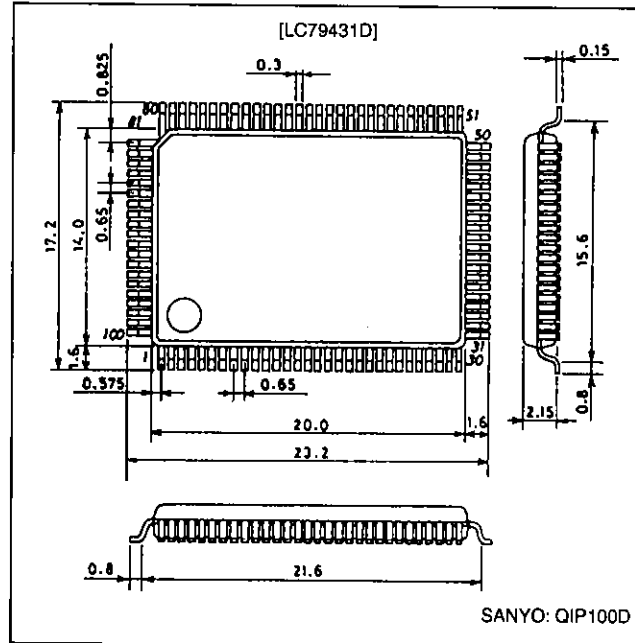
The LC79431D is a large-scale dot matrix LCD common driver LSI. The LC79431D contains an 80-bit bidirectional shift register and a 4-level LCD driver. The input/output pins for cascade connection can be used to further increase the bit count. The LC79431D can be used in conjunction with segment driver LC79400D or LC79401D (QIP100D) to drive a wide-screen LCD panel.

## Features

- On-chip LCD drive circuit (80 bits)
- Display duty of 1/64 to 1/256 selectable
- On-chip input/output pins support a further increase in bit count
- Supports externally supplied bias voltage
- Operating supply voltage/operating temperature are:  
 $V_{DD}$  (logic block) : 5 V  $\pm$ 10 % / -20 to +75 °C  
 $V_{DD}-V_{EE}$  (LCD block) : 12 V to 32 V / -20 to +75°C
- CMOS process

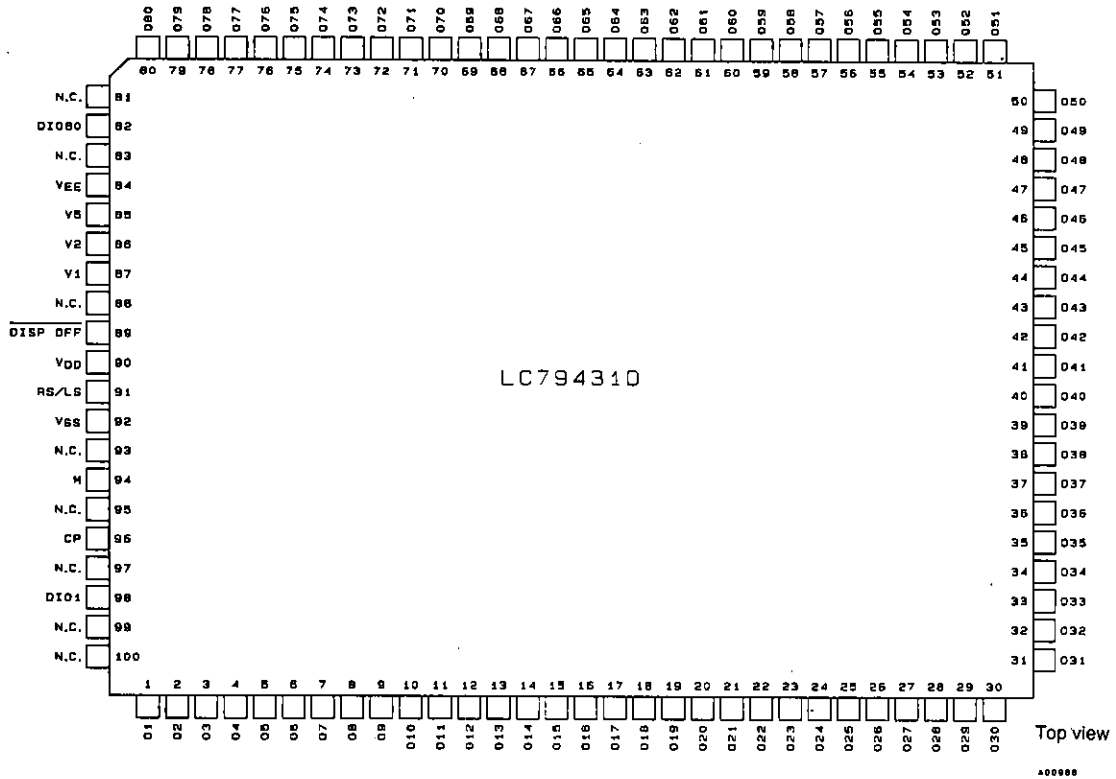
## Package Dimensions

unit : mm

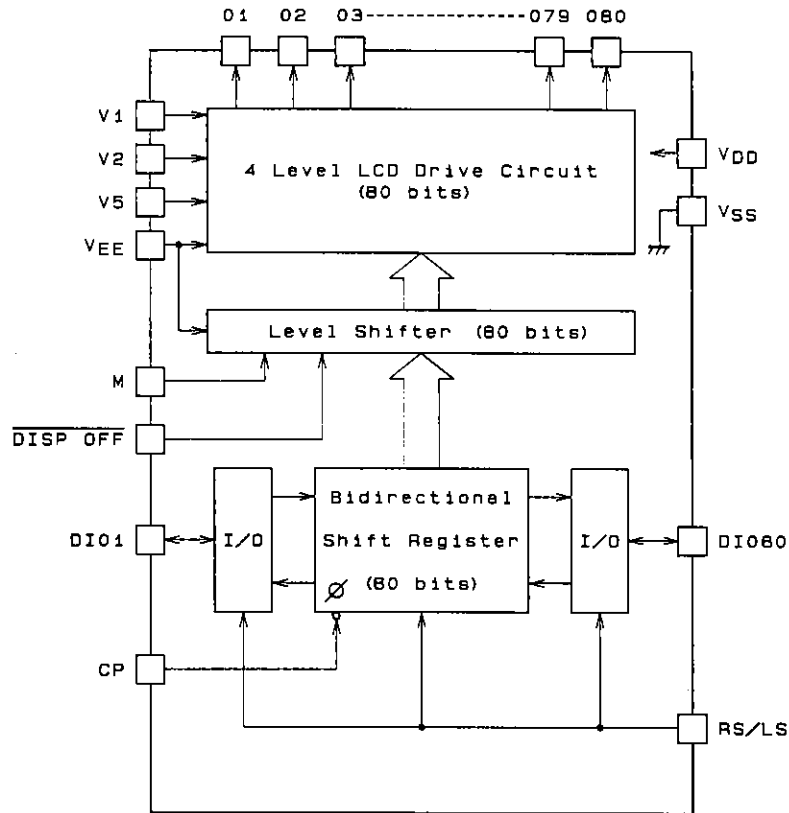
**3180-QFP100D**

# LC79431D

## Pin Assignment



## Equivalent Circuit Block Diagram



# LC79431D

## Pin Descriptions

Pin No	Pin name	Input/Output	Functions																								
90	V <sub>DD</sub>	Power supply	V <sub>DD</sub> to V <sub>SS</sub> : Power supply for logic block																								
92	V <sub>SS</sub>																										
84	V <sub>EE</sub>																										
87	V1	Power supply	V <sub>DD</sub> to V <sub>EE</sub> : Power supply for LCD drive block  LCD drive level power supply V1 to V <sub>EE</sub> : Select level V2 to V5 : Nonselect level																								
86	V2																										
85	V5																										
96	CP	Input	Bidirectional shift register's shift clock (triggering on the trailing edge)																								
98	DIO1	Input/Output	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 15%;">RS/LS</th> <th style="width: 35%;">Data Transfer Direction</th> <th style="width: 15%;">DIO1</th> <th style="width: 35%;">DIO80</th> </tr> </thead> <tbody> <tr> <td>L (Shift right)</td> <td>O1 → O80</td> <td style="text-align: center;">IN</td> <td style="text-align: center;">OUT</td> </tr> <tr> <td>H (Shift left)</td> <td>O80 → O1</td> <td style="text-align: center;">OUT</td> <td style="text-align: center;">IN</td> </tr> </tbody> </table>	RS/LS	Data Transfer Direction	DIO1	DIO80	L (Shift right)	O1 → O80	IN	OUT	H (Shift left)	O80 → O1	OUT	IN												
RS/LS	Data Transfer Direction	DIO1		DIO80																							
L (Shift right)	O1 → O80	IN		OUT																							
H (Shift left)	O80 → O1	OUT	IN																								
82	DIO80	Input/Output																									
91	RS/LS	Input																									
94	M	Input	LCD drive output alternating signal																								
89	DISP OFF	Input	O1 to O80 output controlling input pin																								
1 ⋮ 80	O1 ⋮ O80	Output	LCD drive output The combination of scanning data, M signal, and $\overline{\text{DISP OFF}}$ signal can be used to create output levels as shown below. <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 15%;">M</th> <th style="width: 15%;">Data</th> <th style="width: 15%;">DISP OFF</th> <th style="width: 55%;">Output</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">L</td> <td style="text-align: center;">L</td> <td style="text-align: center;">H</td> <td style="text-align: center;">V2</td> </tr> <tr> <td style="text-align: center;">L</td> <td style="text-align: center;">H</td> <td style="text-align: center;">H</td> <td style="text-align: center;">V<sub>EE</sub></td> </tr> <tr> <td style="text-align: center;">H</td> <td style="text-align: center;">L</td> <td style="text-align: center;">H</td> <td style="text-align: center;">V5</td> </tr> <tr> <td style="text-align: center;">H</td> <td style="text-align: center;">H</td> <td style="text-align: center;">H</td> <td style="text-align: center;">V1</td> </tr> <tr> <td style="text-align: center;">*</td> <td style="text-align: center;">*</td> <td style="text-align: center;">L</td> <td style="text-align: center;">V1</td> </tr> </tbody> </table> <p style="font-size: small; margin-top: 5px;">* Don't care (To be set to either "H" or "L")</p>	M	Data	DISP OFF	Output	L	L	H	V2	L	H	H	V <sub>EE</sub>	H	L	H	V5	H	H	H	V1	*	*	L	V1
M	Data	DISP OFF	Output																								
L	L	H	V2																								
L	H	H	V <sub>EE</sub>																								
H	L	H	V5																								
H	H	H	V1																								
*	*	L	V1																								

## Specifications

### Absolute Maximum Ratings at Ta = 25±2°C, V<sub>SS</sub> = 0V

Parameter	Symbol	Ratings	Unit
Maximum supply voltage (logic)	V <sub>DD max</sub>	-0.3 to +7.0	V
Maximum supply voltage (LCD)	V <sub>DD-V<sub>EE</sub> max</sub> *	0 to 35	V
Maximum input voltage	V <sub>IN max</sub>	-0.3 to V <sub>DD</sub> + 0.3	V
Storage temperature range	T <sub>stg</sub>	-40 to +125	°C

Note: \* The voltages V1, V2, and V5 must obey the relationships: V<sub>DD</sub> ≥ V1 > V2 > V5 > V<sub>EE</sub>; V<sub>DD</sub> - V2 ≤ 7V, V5 - V<sub>EE</sub> ≤ 7V.

## LC79431D

### Allowable Operating Ranges at Ta = -20 to +75°C, VSS = 0V

Parameter	Symbol	Conditions	min	typ	max	Unit
Supply voltage (logic)	V <sub>DD</sub>		4.5		5.5	V
Supply voltage (LCD)	V <sub>DD</sub> -V <sub>EE</sub>	*1, *2	12		32	V
Input high level voltage	V <sub>IH</sub>	DIO1, DIO80, CP, M, RS/LS, DISP OFF	0.8V <sub>DD</sub>			V
Input low level voltage	V <sub>IL</sub>	DIO1, DIO80, CP, M, RS/LS, DISP OFF			0.2V <sub>DD</sub>	V
CP (shift clock)	f <sub>CP</sub>	CP			1	MHz
CP (pulse width)	t <sub>WC</sub>	CP	63			ns
Setup time	t <sub>SETUP</sub>	DIO1 → CP, DIO80 → CP,	100			ns
Hold time	t <sub>HOLD</sub>	DIO1 → CP, DIO80 → CP,	100			ns
CP rise/fall time	t <sub>R</sub>	CP			50	ns
	t <sub>F</sub>	CP			50	ns

Note: 1. The voltages V1, V2, and V5 must obey the relationships: V<sub>DD</sub> ≥ V1 > V2 > V5 > V<sub>EE</sub>, V<sub>DD</sub> - V2 ≤ 7V, V5 - V<sub>EE</sub> ≤ 7V.

2. When applying power, apply power to the LCD drive block after applying power to the logic block or apply power to both the blocks simultaneously. When turning off power, turn off power to the logic block after turning off power to the LCD drive block or turn off power to both the blocks simultaneously.

### Electrical Characteristics at Ta = 25±2°C, VSS = 0V, VDD = 5V±10%

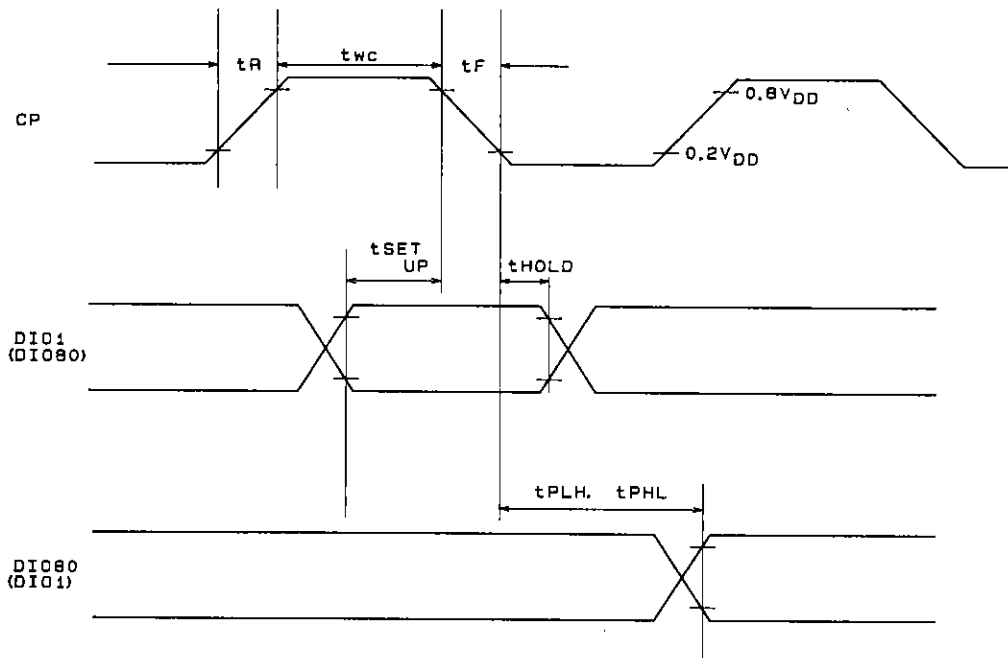
Parameter	Symbol	Conditions	min	typ	max	Unit
Input high level current	I <sub>IH</sub>	V <sub>IN</sub> = V <sub>DD</sub> , V <sub>DD</sub> = 5.5 V; DIO1, DIO80, CP, M, RS/LS, DISP OFF			1	μA
Input low level current	I <sub>IL</sub>	V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub> = 5.5 V; DIO1, DIO80, CP, M, RS/LS, DISP OFF	-1			μA
Output high level voltage	V <sub>OH</sub>	I <sub>OH</sub> = -0.4 mA, V <sub>DD</sub> = 4.5 V; DIO1, DIO80	V <sub>DD</sub> -0.4			V
Output low level voltage	V <sub>OL</sub>	I <sub>OL</sub> = 0.4 mA, V <sub>DD</sub> = 4.5 V; DIO1, DIO80			0.4	V
Drive-on resistor	R <sub>ON</sub> (1)	V <sub>DD</sub> -V <sub>EE</sub> = 30 V,  V <sub>DE</sub> -V <sub>O</sub>   = 0.5V, V <sub>DD</sub> = 4.5 V *; O1 to O80			1.0	kΩ
	R <sub>ON</sub> (2)	V <sub>V</sub> -V <sub>EE</sub> = 20 V,  V <sub>DE</sub> -V <sub>O</sub>   = 0.5V, V <sub>DD</sub> = 4.5 V *; O1 to O80			1.0	kΩ
Current drain (1)	I <sub>SS</sub>	V <sub>DD</sub> -V <sub>EE</sub> = 30 V, CP = 14 kHz, no load, V <sub>DD</sub> = 5.5 V; V <sub>SS</sub>			100	μA
Current drain (2)	I <sub>EE</sub>	V <sub>DD</sub> -V <sub>EE</sub> = 30V, CP = 14 kHz, no load, V <sub>DD</sub> = 5.5 V; V <sub>EE</sub>			100	μA
Input capacitance	C <sub>I</sub>	f = 1 MHz; CP		5		pF

Note: \* V<sub>DE</sub> = V1 or V2 or V5 or V<sub>EE</sub>, V1 = V<sub>DD</sub>, V2 = 16/17 (V<sub>DD</sub>-V<sub>EE</sub>), V5 = 1/17 (V<sub>DD</sub>-V<sub>EE</sub>)

### Switching Characteristics at Ta = 25±2°C, VSS = 0V, VDD = 5V±10%

Parameter	Symbol	Conditions	min	typ	max	Unit
Output delay time	t <sub>PLH</sub>	C <sub>L</sub> = 15pF; CP → DIO1, CP → DIO80			250	ns
	t <sub>PHL</sub>	C <sub>L</sub> = 15pF; CP → DIO1, CP → DIO80			250	ns

## Switching Characteristics Diagram



A00989

- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
  - ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
  - ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of August, 1996. Specifications and information herein are subject to change without notice.