



No. 3570

2SK1432

N-Channel MOS Silicon FET

**Very High-Speed
Switching Applications**

Features

- Low ON-state resistance.
- Very high-speed switching
- Converters.
- Micaless package facilitating easy mounting.

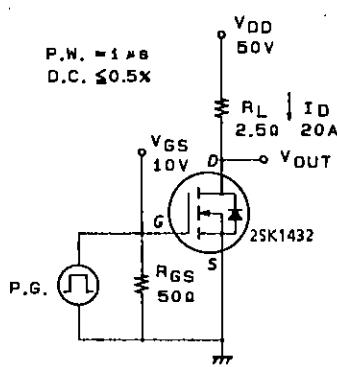
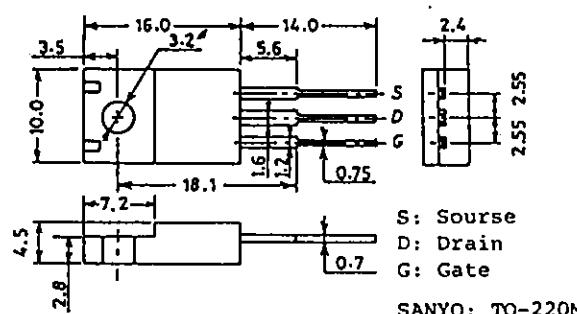
Absolute Maximum Ratings at Ta = 25°C

		unit
Drain to Source Voltage	V _{DSS}	100 V
Gate to Source Voltage	V _{GSS}	±20 V
Drain Current(DC)	I _D	25 A
Drain Current(Pulse)	I _{DP}	PW ≤ 10μs, duty cycle ≤ 1% 100 A
Allowable Power Dissipation	P _D	T _c = 25°C 40 W
Channel Temperature	T _{ch}	2.0 W
Storage Temperature	T _{stg}	150 °C
		-55 to +150 °C

Electrical Characteristics at Ta = 25°C

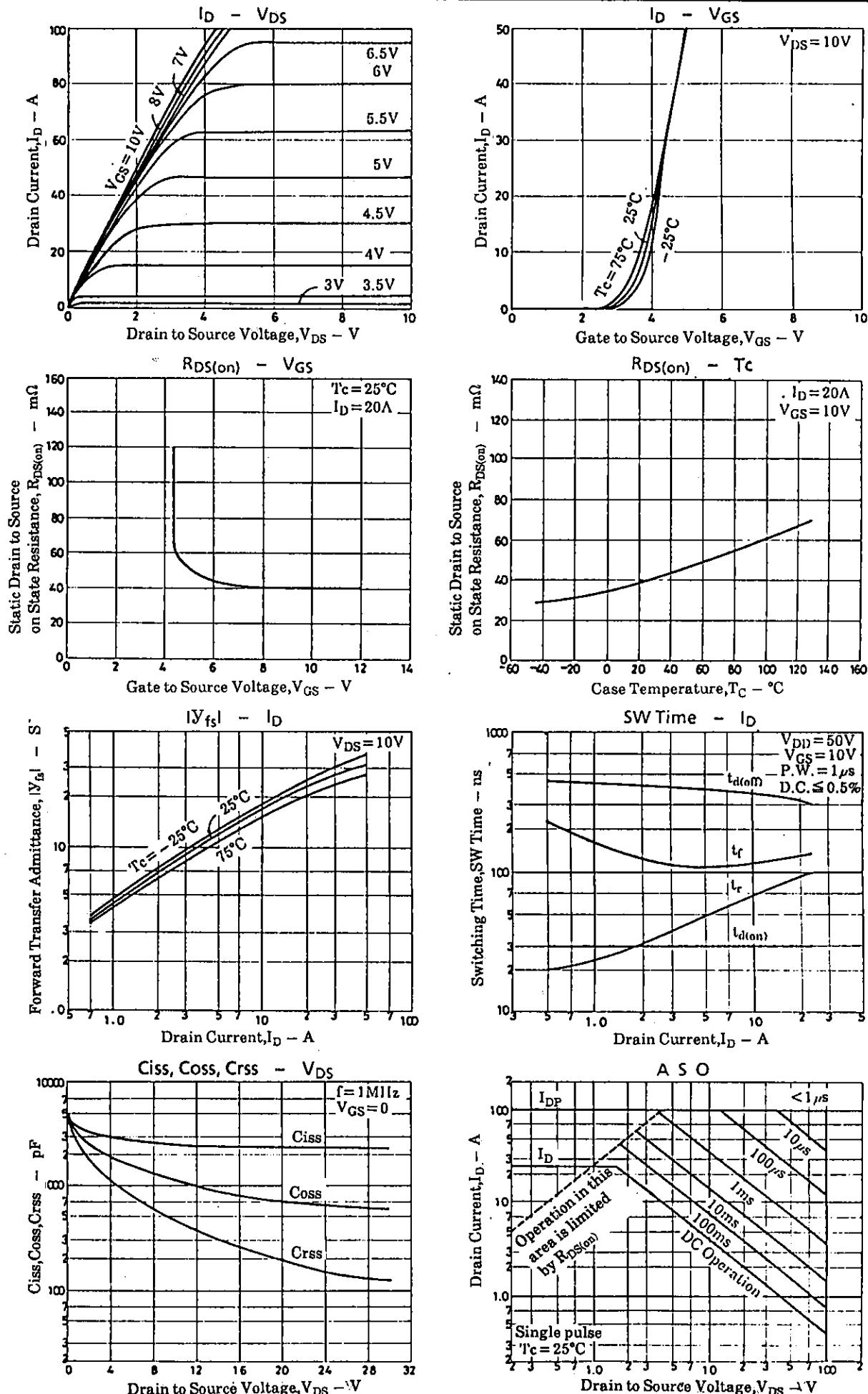
		min	typ	max	unit
D-S Breakdown Voltage	V _{(BR)DSS}	100			V
Zero Gate Voltage Drain Current	I _{DSS}			100	μA
Gate to Source Leakage Current	I _{GSS}			±100	nA
Cutoff Voltage	V _{GS(off)}			1.5	V
Forward Transfer Admittance	Y _{fs}			2.5	S
Static Drain to Source on State Resistance	R _{DS(on)}	I _D = 20A, V _{GS} = 10V	0.040	0.055	Ω
Input Capacitance	C _{iss}	V _{DS} = 20V, f = 1MHz	2400		pF
Output Capacitance	C _{oss}	V _{DS} = 20V, f = 1MHz	700		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} = 20V, f = 1MHz	200		pF
Turn-ON Delay Time	t _{d(on)}			30	ns
Rise Time	t _r	I _D = 20A, V _{GS} = 10V	90		ns
Turn-OFF Delay Time	t _{d(off)}	V _{DD} = 50V, R _{GS} = 50Ω	320		ns
Fall Time	t _f			130	ns
Diode Forward Voltage	V _{SD}	I _S = 25A, V _{GS} = 0		1.8	V

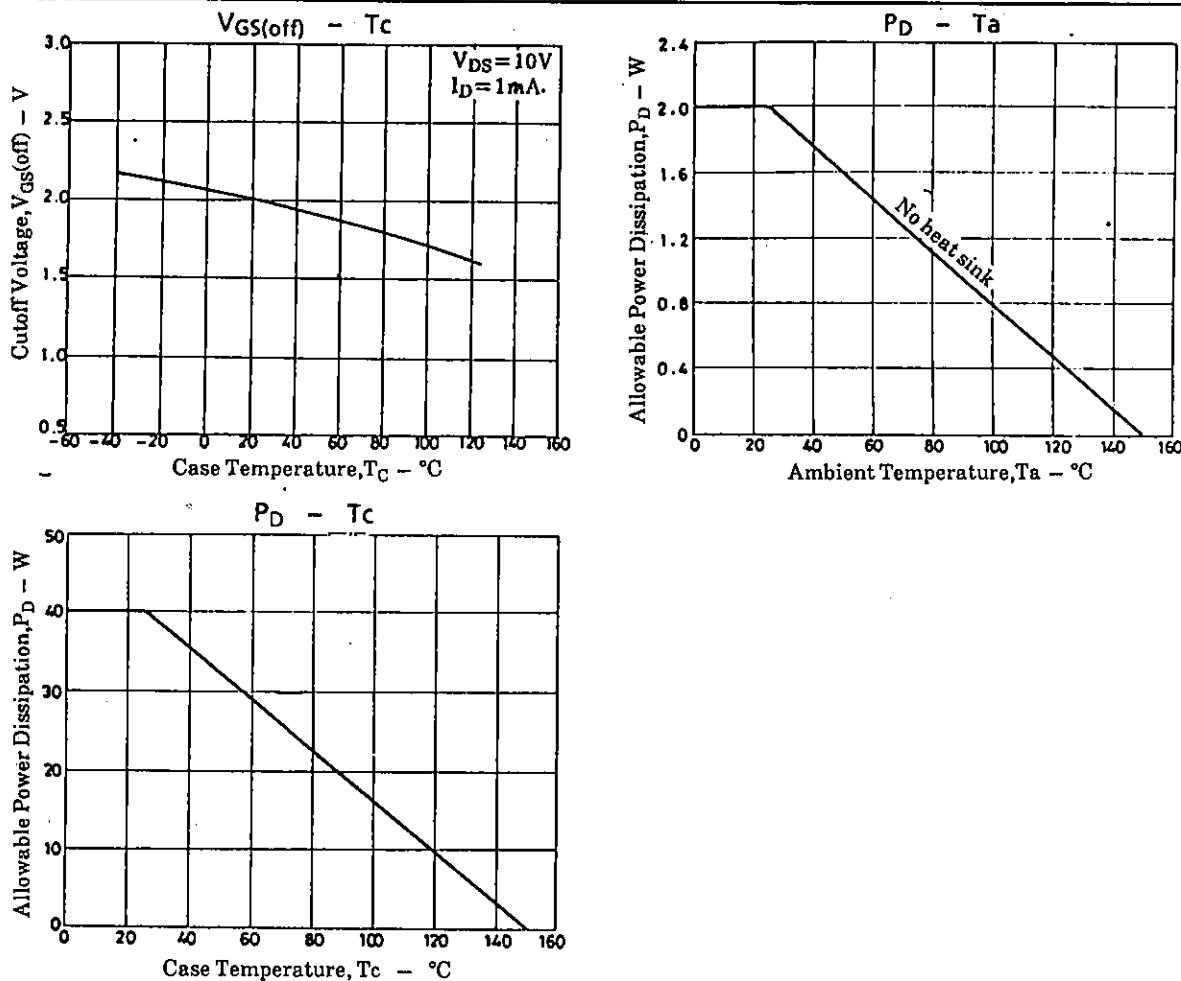
(Note) Be careful in handling the 2SK1432 because it has no protection diode between gate and source.

Switching Time Test Circuit**Package Dimensions 2063**
(unit : mm)

S: Source
D: Drain
G: Gate
SANYO: TO-220ML

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