

**LB1651D****Dual Bidirectional Motor Driver****Overview**

The LB1651D is a dual bidirectional motor driver that is designed to drive motors directly by TTL outputs. It provides the functions of bidirectional motor drive, brake that are determined by two inputs and the inhibit function that brings the output to a high impedance state.

**Applications**

- Multi DC motor driver
- Bidirectional motor driver
- Bipolar stepping motor driver

**Features**

- High output current (1 A/ch)
- Wide operating voltage range (4.5 to 36 V)
- Inhibit function
- Direct drive made possible by TTL, CMOS IC
- High noise margin

**Specifications****Absolute Maximum Ratings at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC1</sub>		36	V
Logic supply voltage	V <sub>CC2</sub>		36	V
Input voltage	V <sub>IN</sub>		7	V
Inhibit voltage	V <sub>inh</sub>		7	V
Peak output current	I <sub>OUT</sub>	1 ms non-repetitive	2	A
Allowable power dissipation	P <sub>d max</sub>	* With specified board	2.5	W
Operating temperature	T <sub>opr</sub>		-20 to +80	°C
Storage temperature	T <sub>stg</sub>		-40 to +150	°C

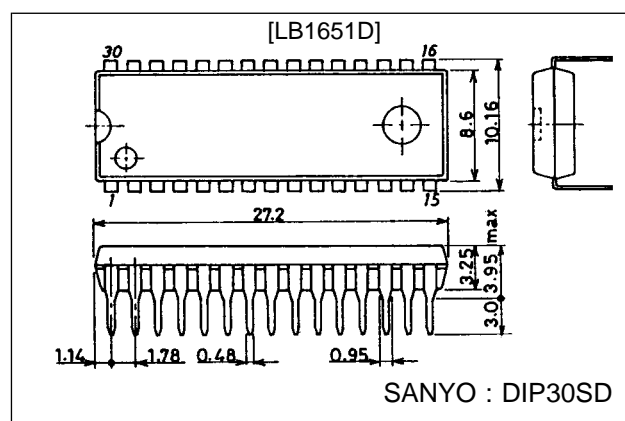
\* Specified board: 114 × 76 × 1.6 mm<sup>3</sup>

**Allowable Operating Conditions at Ta = 25°C**

Parameter	Symbol	Conditions	Rating	Unit
Supply voltage	V <sub>CC1</sub>		4.5 to 36	V
Logic supply voltage	V <sub>CC2</sub>		4.5 to 36	V

**Package Dimensions**

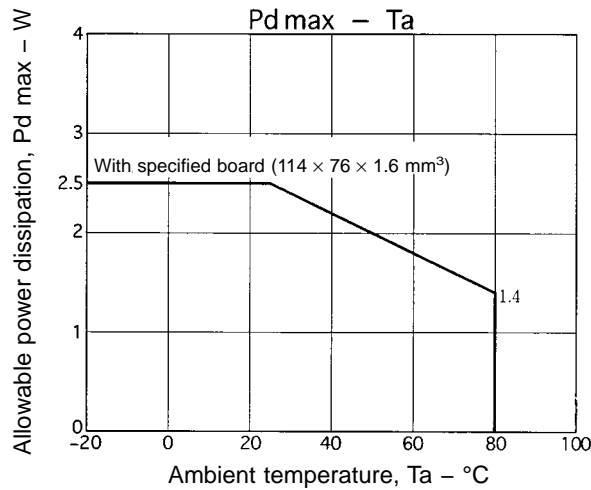
unit : mm

**3196-DIP30SD**

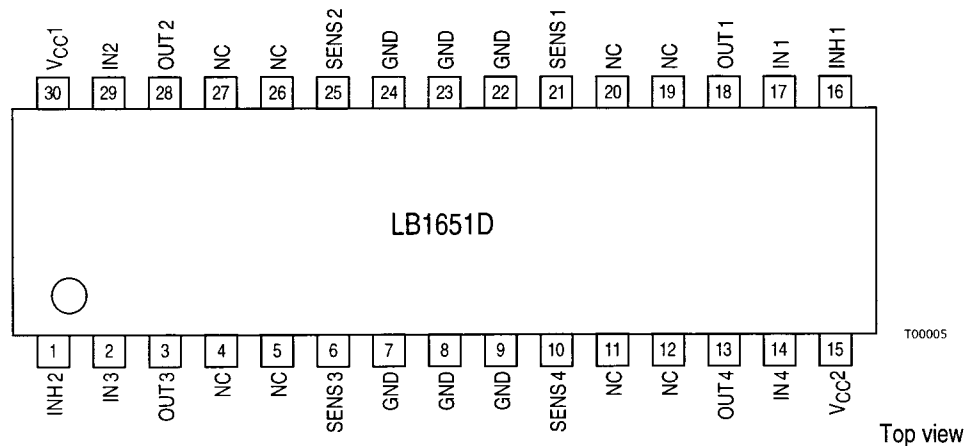
# LB1651D

## Electrical Characteristics at Ta = 25°C, VCC1 = 24 V, VCC2 = 5 V

Parameter	Symbol	Conditions	min	typ	max	Unit
Supply current (Per channel)	I <sub>CC1</sub>	V <sub>IN</sub> = L, I <sub>O</sub> = 0, V <sub>inh</sub> = H			1.5	mA
		V <sub>IN</sub> = H, I <sub>O</sub> = 0, V <sub>inh</sub> = H			6	mA
		V <sub>inh</sub> = L			1	mA
Logic supply current	I <sub>CC2</sub>	V <sub>IN</sub> = L, I <sub>O</sub> = 0, V <sub>inh</sub> = H		44	60	mA
		V <sub>IN</sub> = H, I <sub>O</sub> = 0, V <sub>inh</sub> = H			22	mA
		V <sub>inh</sub> = L			24	mA
Low-level input voltage	V <sub>IL</sub>		-0.3		+1.5	V
High-Level Input Voltage	V <sub>IH</sub>	V <sub>CC2</sub> ≤ 7 V	2.3		V <sub>CC2</sub>	V
		V <sub>CC2</sub> > 7 V	2.3		7	V
Low-level input current	I <sub>IL</sub>	V <sub>IN</sub> = L			±10	μA
High-level input current	I <sub>IH</sub>	V <sub>IN</sub> = H - 0.3 V		30	100	μA
Low-level inhibit voltage	V <sub>inhL</sub>		-0.3		+1.5	V
High-level inhibit voltage	V <sub>inhH</sub>	V <sub>CC2</sub> ≤ 7 V	2.3		V <sub>CC2</sub>	V
		V <sub>CC2</sub> > 7 V	2.3		7	V
Low-level inhibit current	I <sub>inhL</sub>		-100	-30		μA
High-level inhibit current	I <sub>inhH</sub>				±10	μA
Saturation voltage	V <sub>CE(sat)H</sub>	I <sub>O</sub> = -1 A		1.4	1.8	V
	V <sub>CE(sat)L</sub>	I <sub>O</sub> = 1 A		1.2	1.8	V
Sensing voltage	V <sub>SENS</sub>				2	V



## Pin Assignment



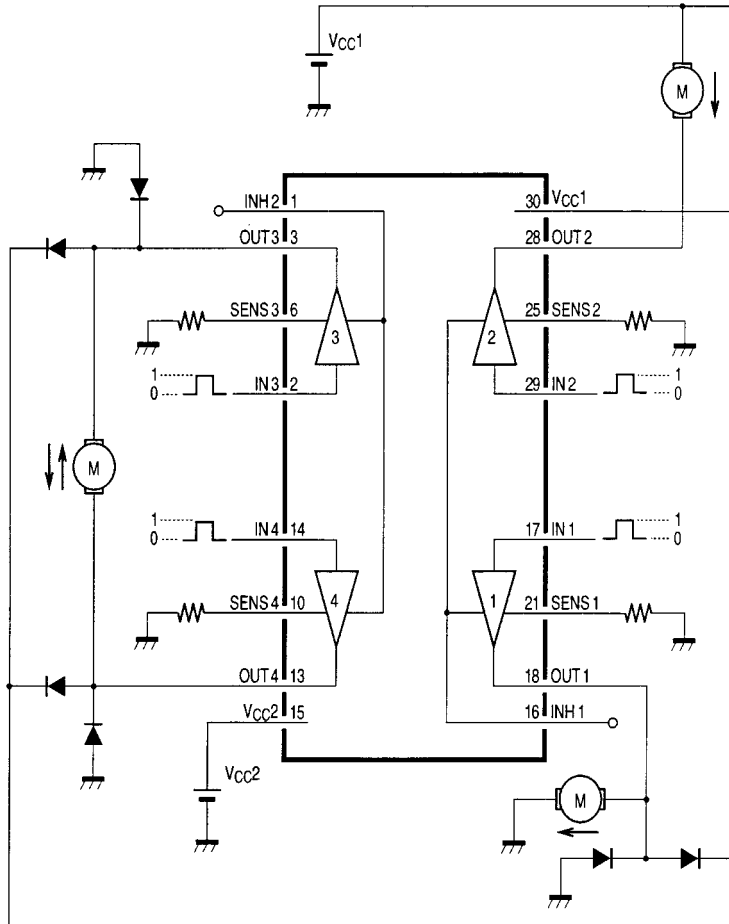
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## Truth Table

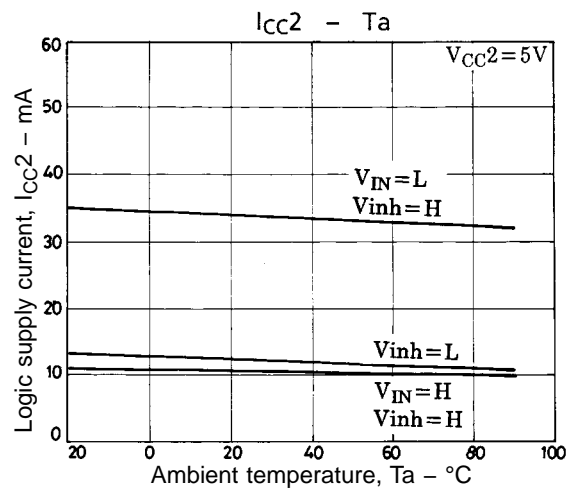
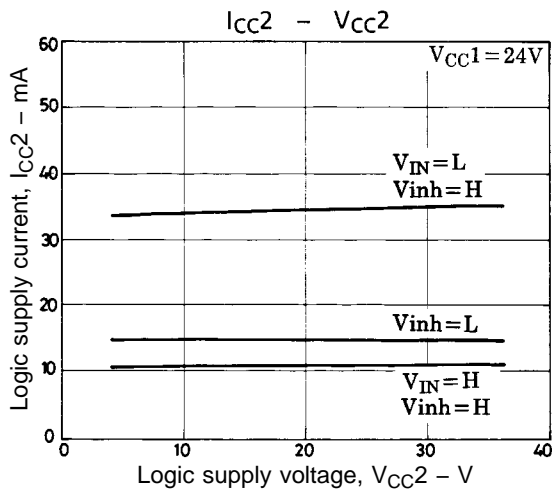
$V_{IN}$ (per CH)	$V_{inh}$	$V_O$
H	H	H
L	H	L
H	L	Open*
L	L	Open*

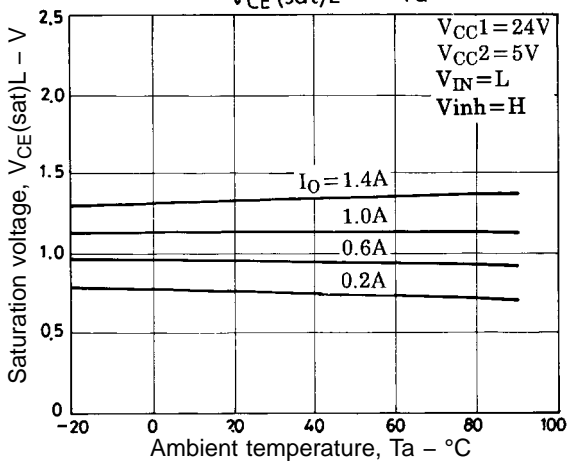
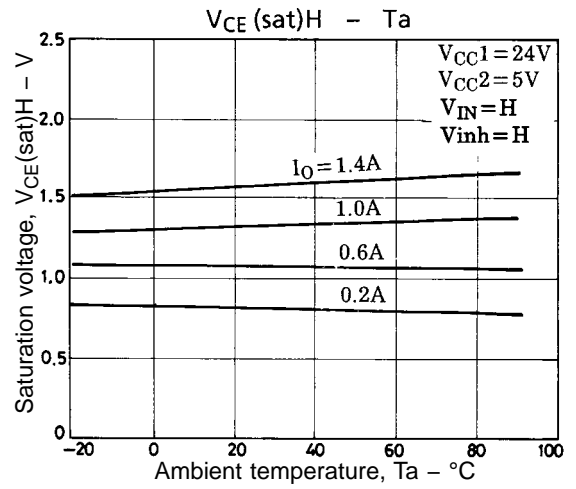
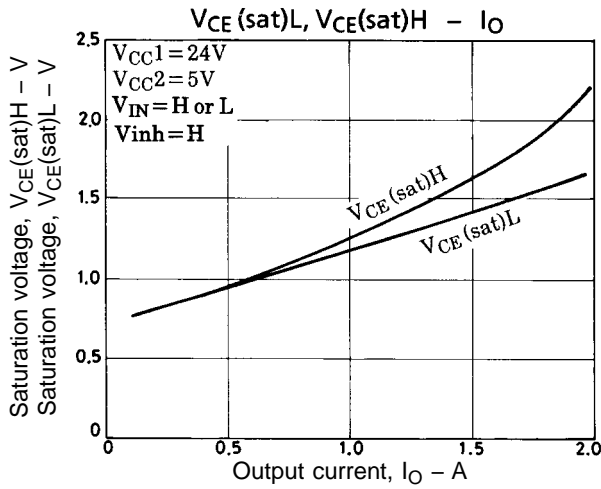
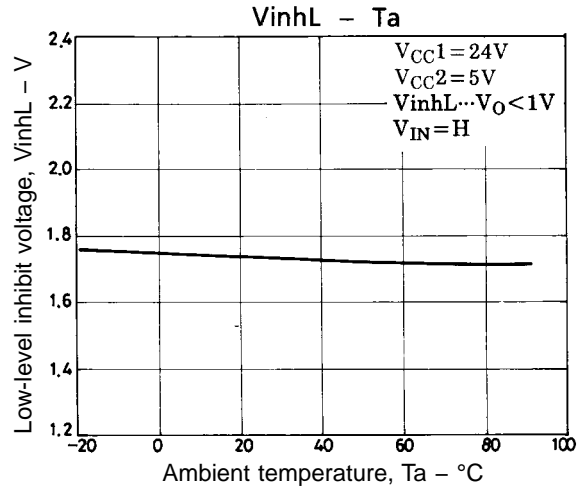
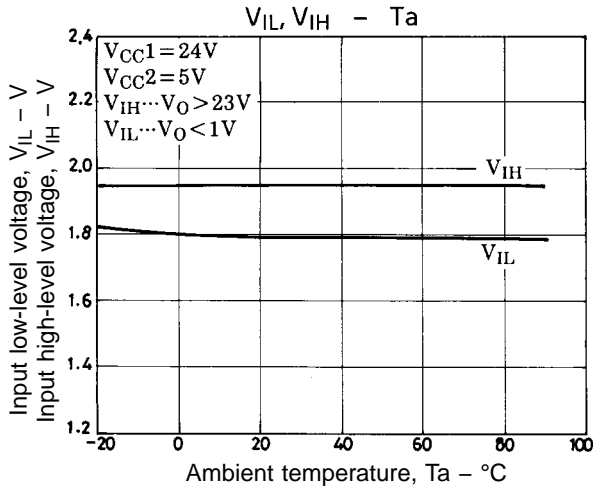
\*: High impedance

## Sample Application Circuit



T00006





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