



# FC805

Silicon Schottky Barrier Diode

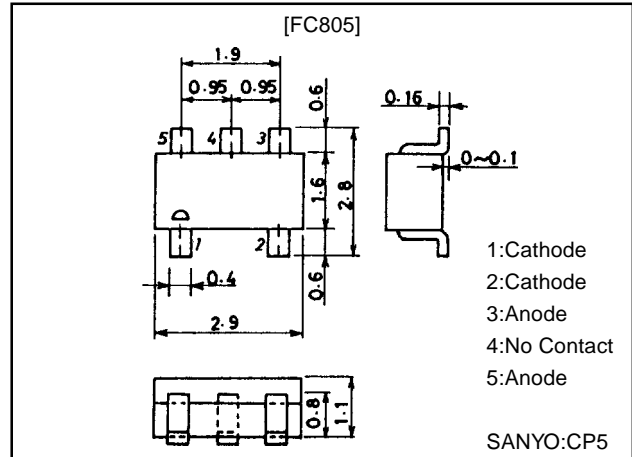
## 30V, 500mA Rectifier

### Features

- Low forward voltage ( $V_F \text{ max}=0.55\text{V}$ ).
- Fast reverse recovery time ( $t_{rr} \text{ max}=10\text{ns}$ ).
- Composite type with 2 diodes contained in the CP package currently in use, saving the mounting space greatly.
- The FC805 is formed with two chips, each being equivalent to the SB05-03C, placed in one package.

### Package Dimensions

unit:mm  
1236A



### Specifications

#### Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

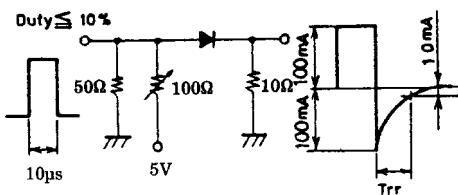
Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	$V_{RRM}$		30	V
Nonrepetitive Peak Reverse Surge Voltage	$V_{RSM}$		35	V
Average Output Current	$I_O$		500	mA
Surge Forward Current	$I_{FSM}$	50MHz sine wave, 1 cycle	5	A
Junction Temperature	$T_j$		-55 to +125	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +125	$^\circ\text{C}$

#### Electrical Characteristics at $T_a = 25^\circ\text{C}$ (Value per element)

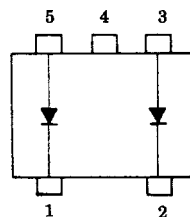
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Reverse Voltage	$V_R$	$I_R=150\mu\text{A}$	30			V
Forward Voltage	$V_F$	$I_F=500\text{mA}$			0.55	V
Reverse Current	$I_R$	$V_R=15\text{V}$			30	$\mu\text{A}$
Interteminal Capacitance	C	$V_R=10\text{V}$ , $f=1\text{MHz}$		16		pF
Reverse Recovery Time	$t_{rr}$	$I_F=I_R=100\text{mA}$ , See specified Test Circuit			10	ns
Thermal Resistance	$R_{th(j-a)}$			560		$^\circ\text{C/W}$

· Marking:805

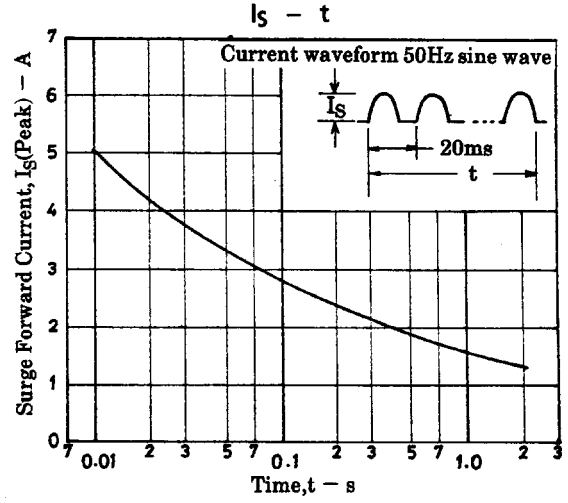
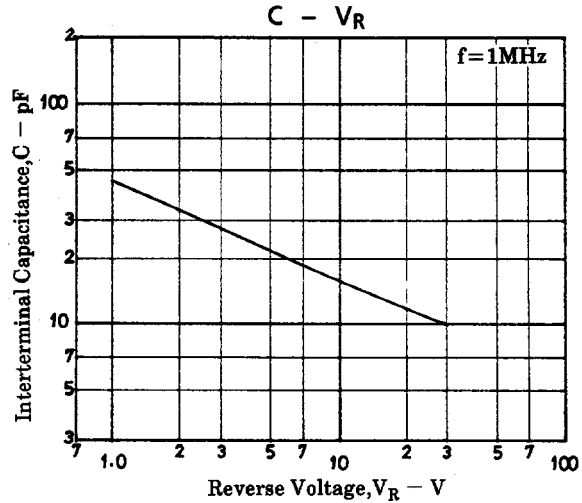
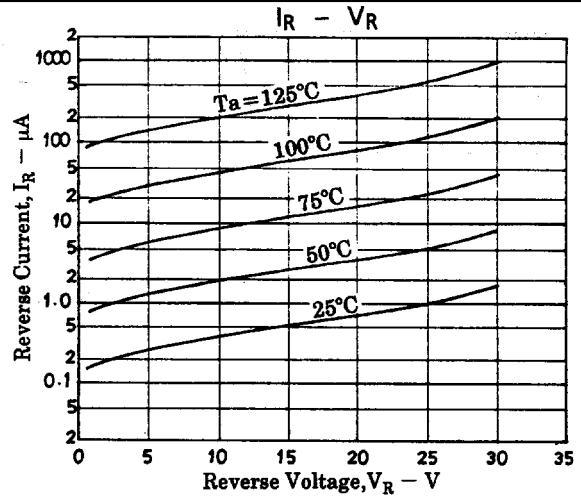
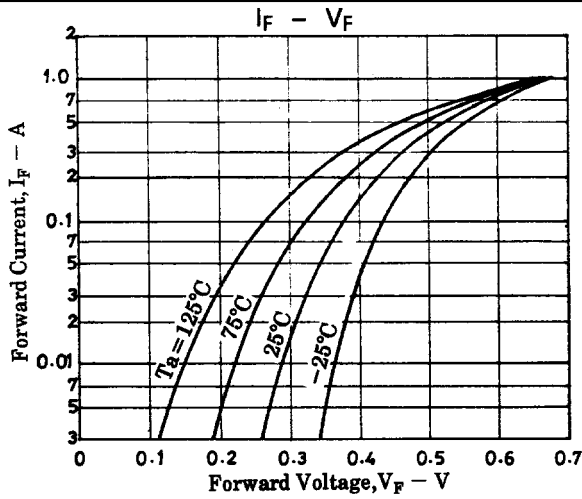
#### $t_{rr}$ Test Circuit



#### Electrical Connection



# FC805



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