NPN Triple Diffused Planar Silicon Transistor



2SC3637

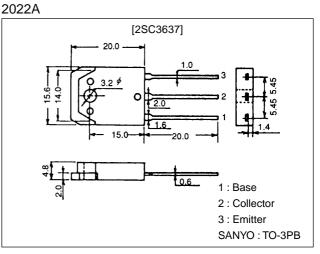
Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

Features

- · High reliability (Adoption of HVP process).
- · Fast speed.
- · High breakdown voltage.
- · Adoption of MBIT process.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		900	V
Collector-to-Emitter Voltage	VCEO		500	V
Emitter-to-Base Voltage	VEBO		7	V
Collector Current	ι _C		10	A
Collector Current (Pulse)	I _{CP}		20	A
Collector Dissipation	PC	Tc=25°C	90	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

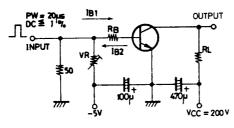
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ІСВО	V _{CB} =500V, I _E =0			10	μΑ
	ICES	V _{CE} =900V, R _{BE} =0			0.5	mA
Collector-to-Emitter Sastain Voltage	V _{CEO(sus)}	I _C =100mA, I _B =0	500			V
Emitter Cutoff Current	IEBO	V _{EB} =5V, I _C =0			1	mA
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =5A, I _B =1A			2	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =5A, I _B =1A			1.5	V
DC Current Gain	hFE	V _{CE} =5V, I _C =1A	8			
Storage Time	tstg	I _C =5A, I _{B1} =1A, I _{B2} =-2A			3.0	μs
Fall Time	t _f	I _C =5A, I _{B1} =1A, I _{B2} =-2A		0.1	0.2	μs

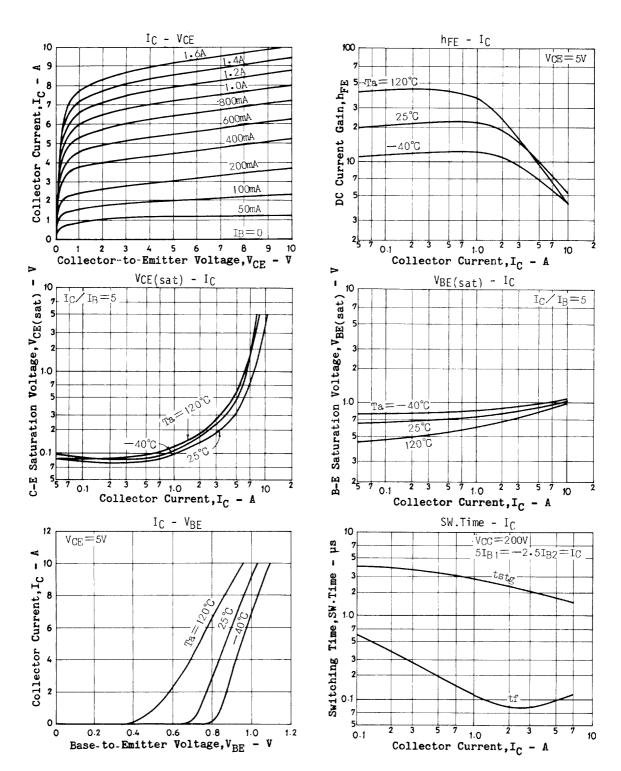
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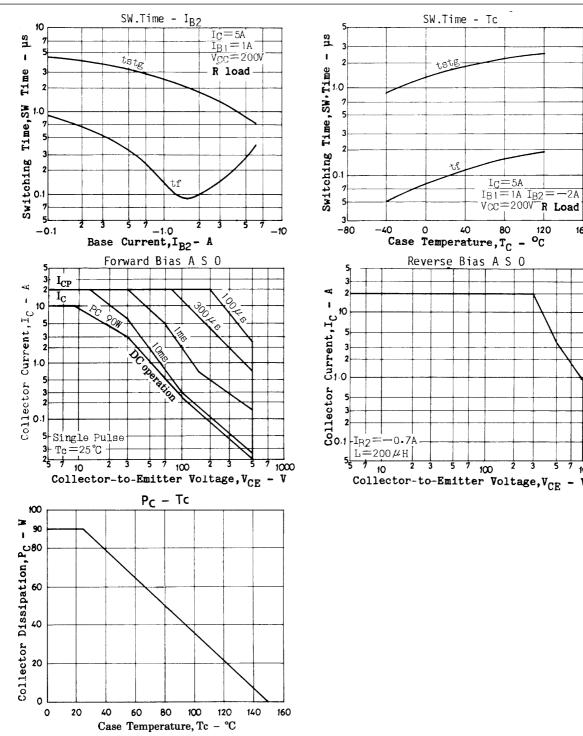
SANYO Electric Co., Ltd. Semiconductor Bussiness Headquaters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Switching Time Test Circuit



Unit (resistance: Ω , capacitance:F)





160

5

1000

- V

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