



FTD1011

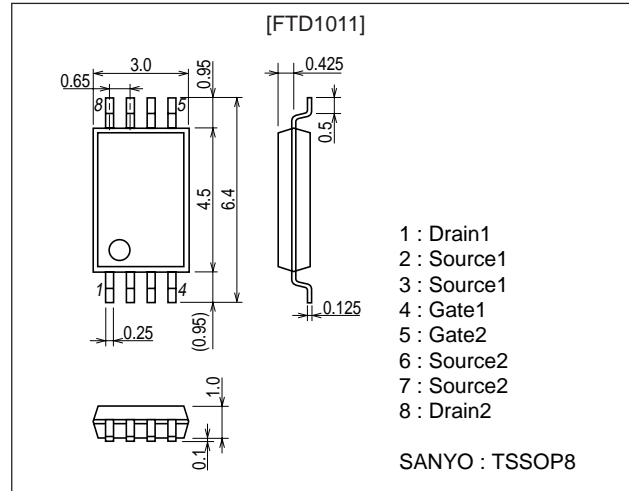
Ultrahigh-Speed Switching Applications

Features

- Low ON-resistance.
- 2.5V drive.
- Mount height of 1.1mm.
- Composite type, facilitating high-density mounting.

Package Dimensions

unit : mm
2155A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-20	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		-3	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	-15	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (1000mm²X0.8mm)1unit	0.8	W
Total Dissipation	P _T	Mounted on a ceramic board (1000mm²X0.8mm)	1.0	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-20			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0			-1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-10V, I _D =-1mA	-0.4		-1.4	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-10V, I _D =-3A	6	8.8		S
Static Drain-to-Source on-State Resistance	R _{DS(on)1}	I _D =-3A, V _{GS} =-4V		50	65	mΩ
	R _{DS(on)2}	I _D =-2A, V _{GS} =-2.5V		68	96	mΩ

Marking : D1011

Continued on next page.

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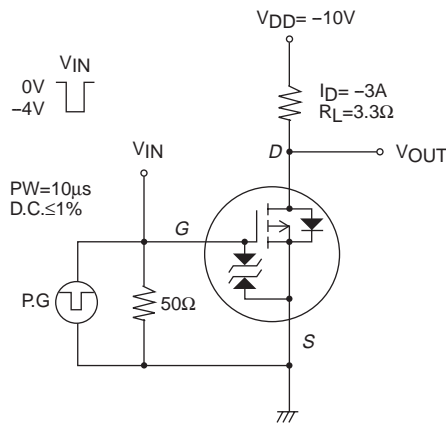
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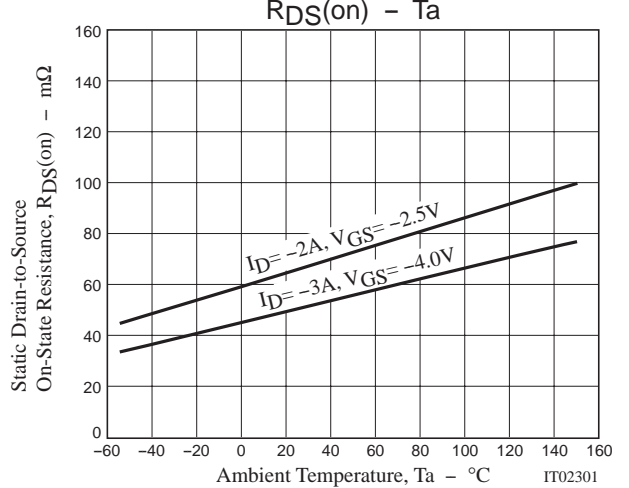
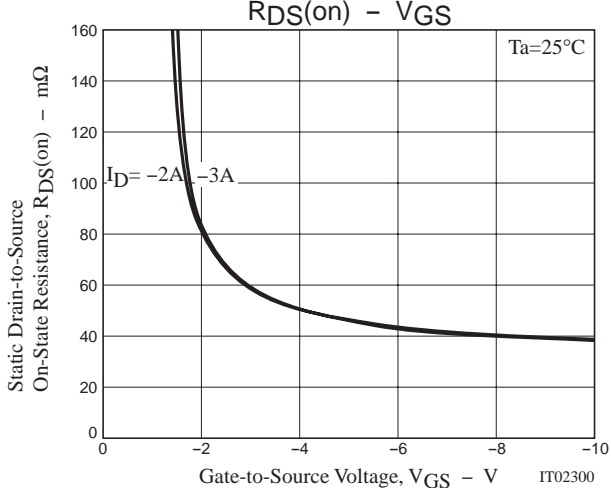
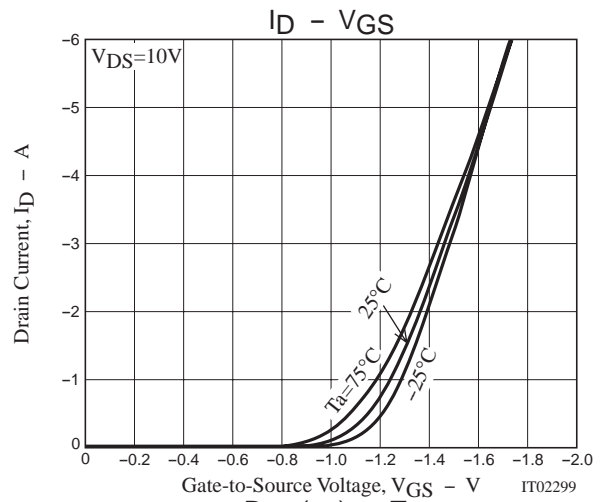
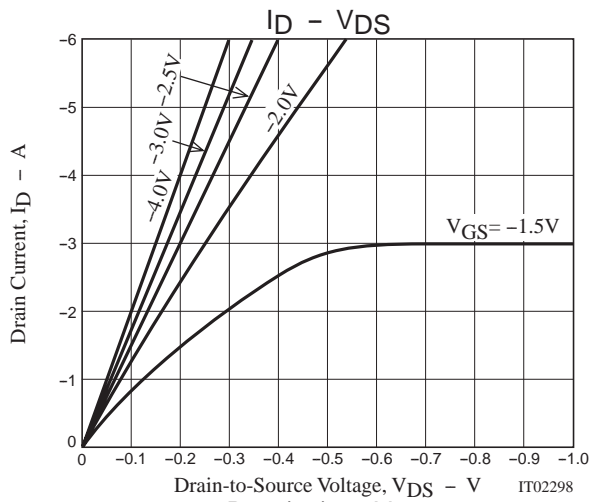
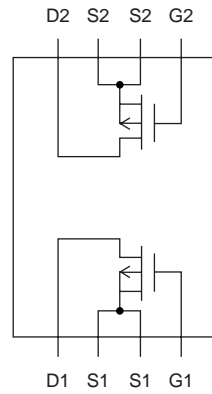
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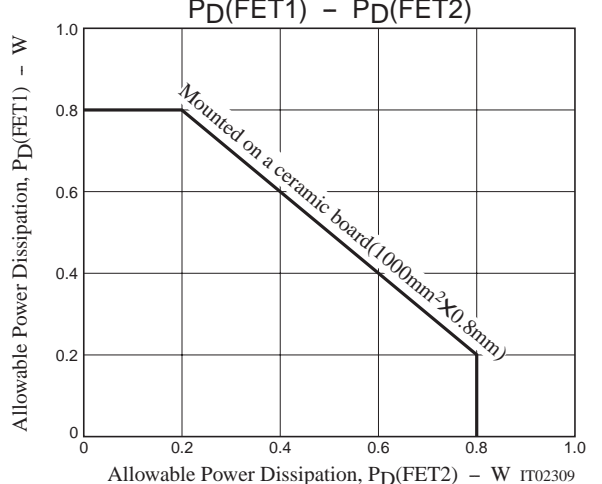
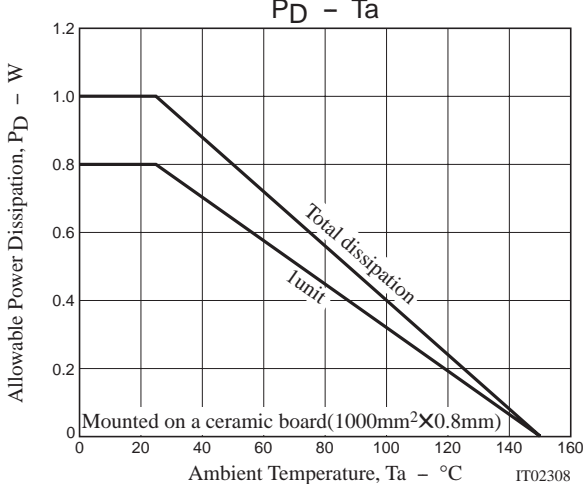
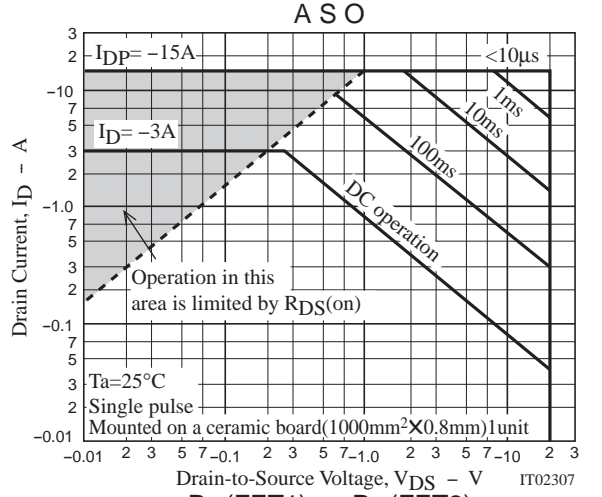
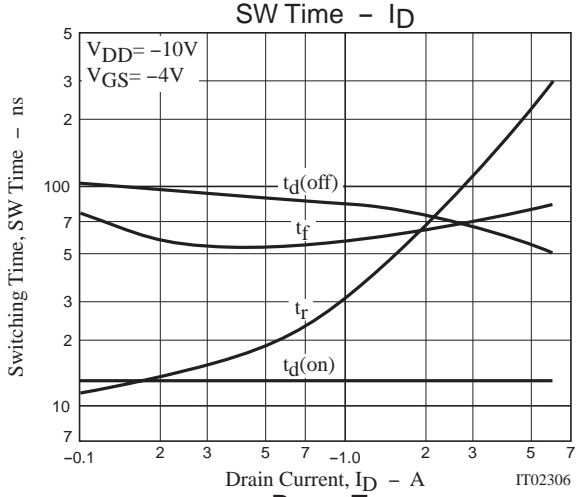
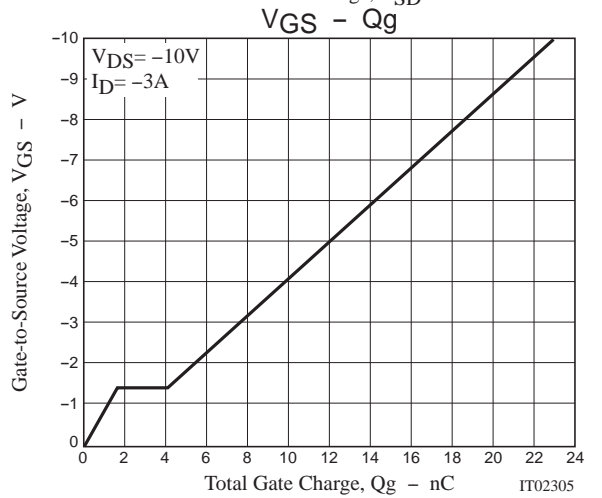
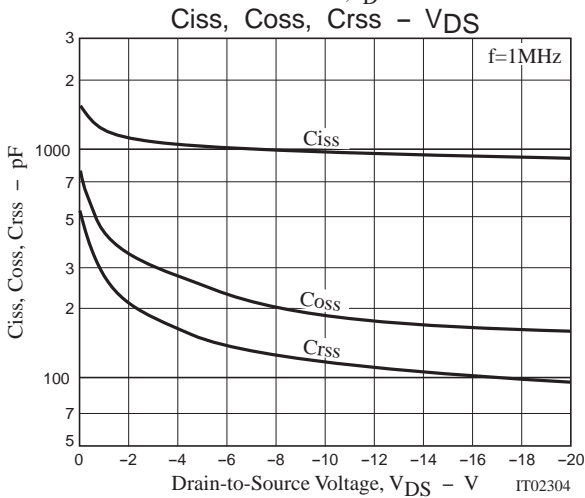
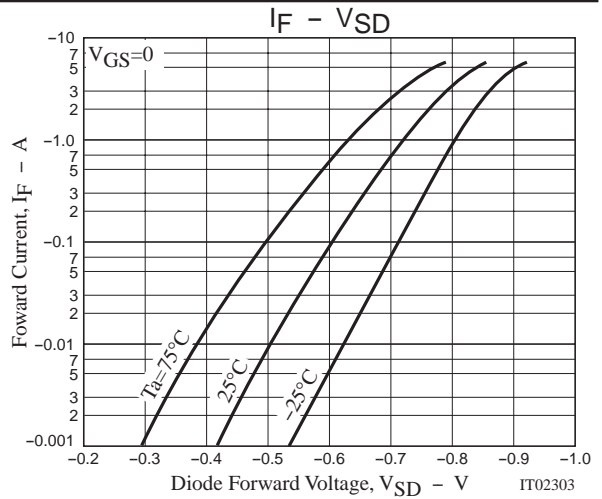
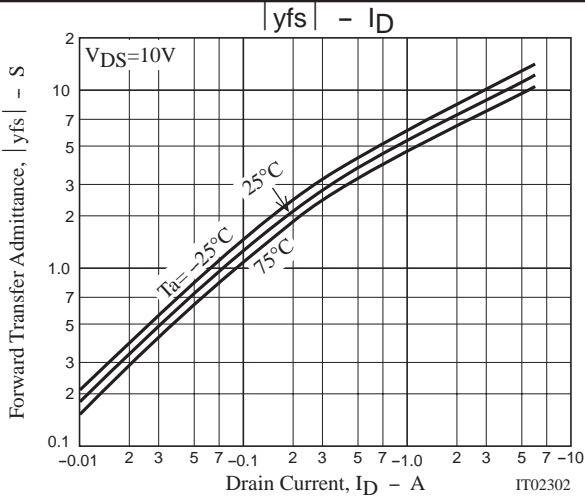
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		1000		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		190		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		120		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		13		ns
Rise Time	t _r	See specified Test Circuit		110		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit		65		ns
Fall Time	t _f	See specified Test Circuit		75		ns
Total Gate Charge	Q _g	V _{DS} =-10V, V _{GS} =-10V, I _D =-3A		23		nC
Gate-to-Source Charge	Q _{gs}	V _{DS} =-10V, V _{GS} =-10V, I _D =-3A		1.6		nC
Gate-to-Drain "Miller" Charge	Q _{gd}	V _{DS} =-10V, V _{GS} =-10V, I _D =-3A		2.5		nC
Diode Forward Voltage	V _{SD}	I _S =-3A, V _{GS} =0	-0.8		-1.5	V

Switching Time Test Circuit



Electrical Connection





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