



CPH3306

Ultrahigh-Speed Switching Applications

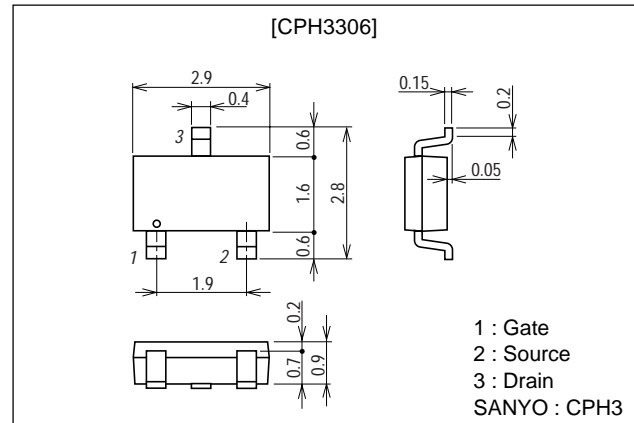
Features

- Low ON resistance.
- Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit:mm

2152A



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-60	V
Gate-to-Source Voltage	V_{GSS}		± 20	V
Drain Current (DC)	I_D		-1	A
Drain Current (pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-4	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm ² ×0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA$, $V_{GS} = 0$	-60			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -60V$, $V_{GS} = 0$			-10	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16V$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V$, $I_D = -1mA$	-1.0		-2.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V$, $I_D = -0.5A$	0.8	1.1		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -0.5A$, $V_{GS} = -10V$		620	800	m Ω
	$R_{DS(on)2}$	$I_D = -0.3A$, $V_{GS} = -4V$		800	1150	m Ω
Input Capacitance	C_{iss}	$V_{DS} = -20V$, $f = 1MHz$		130		pF
Output Capacitance	C_{oss}	$V_{DS} = -20V$, $f = 1MHz$		35		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -20V$, $f = 1MHz$		9		pF

Marking : JF

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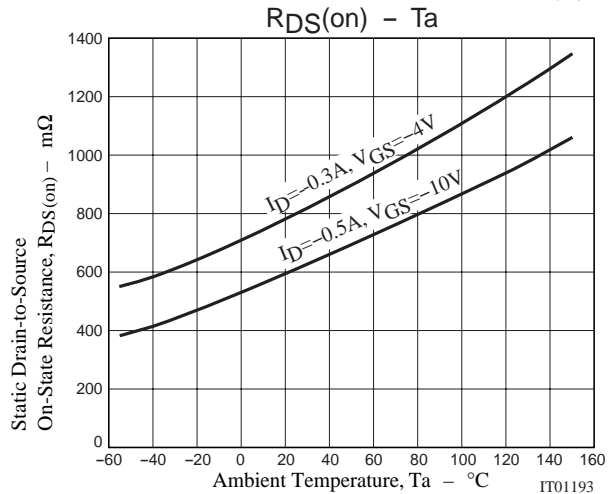
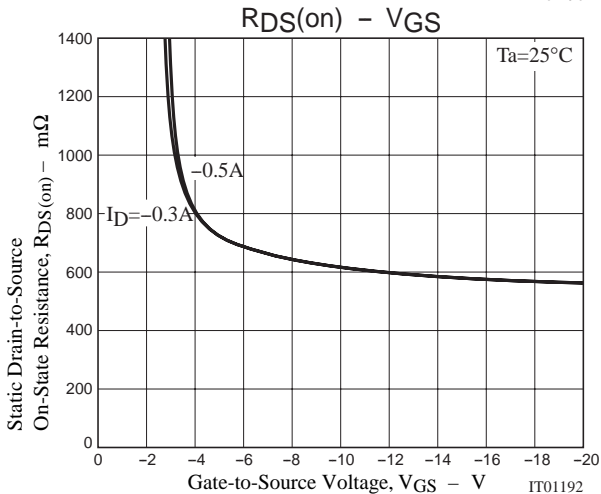
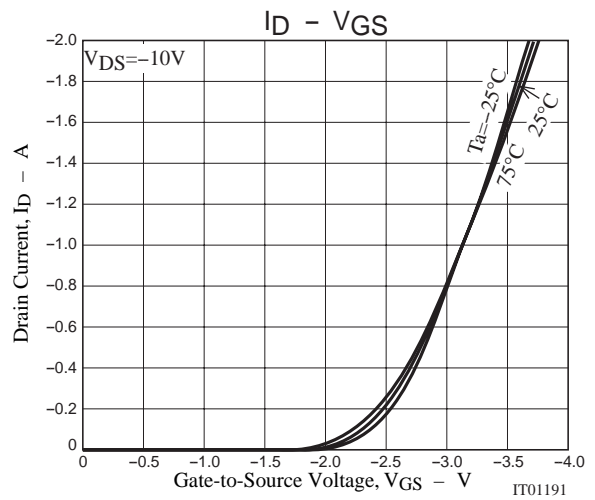
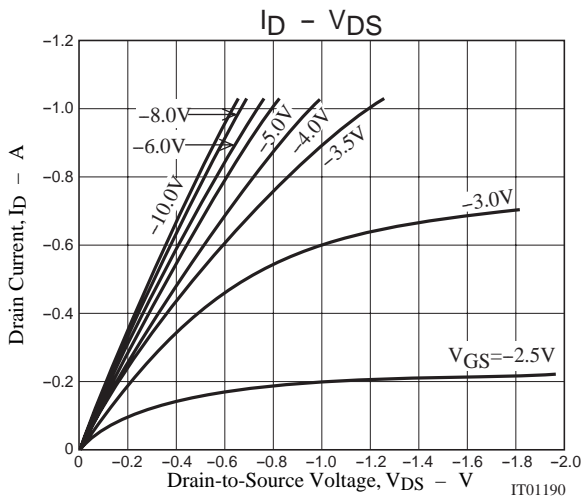
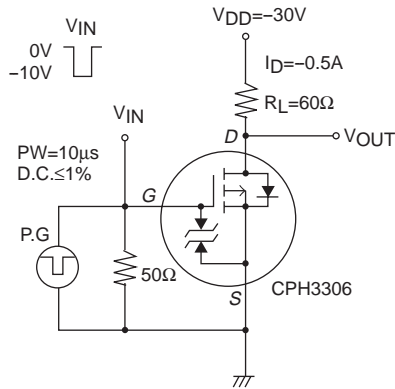
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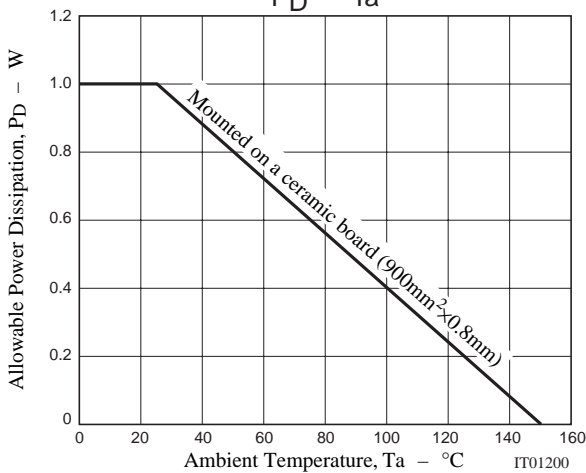
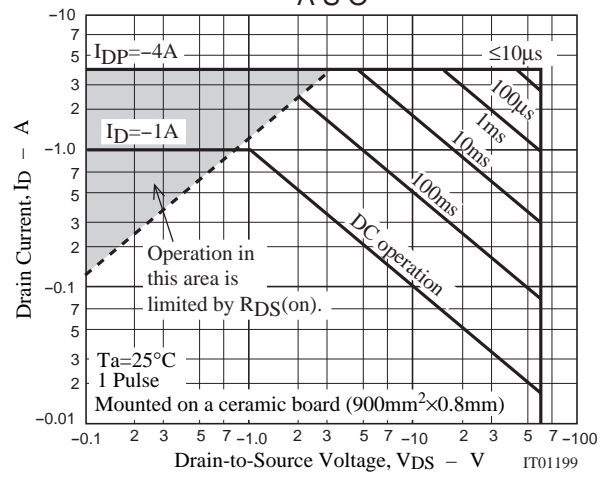
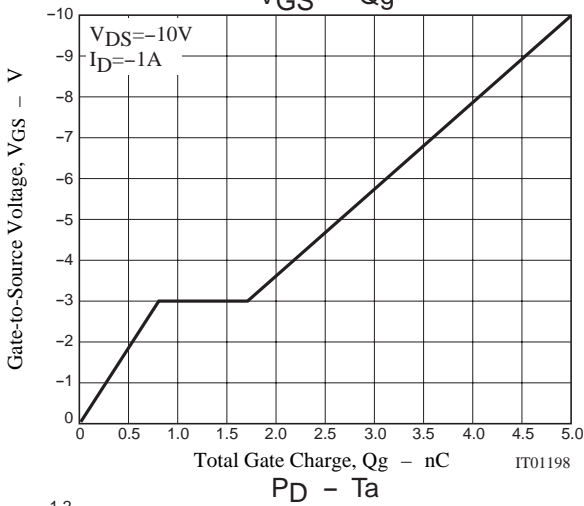
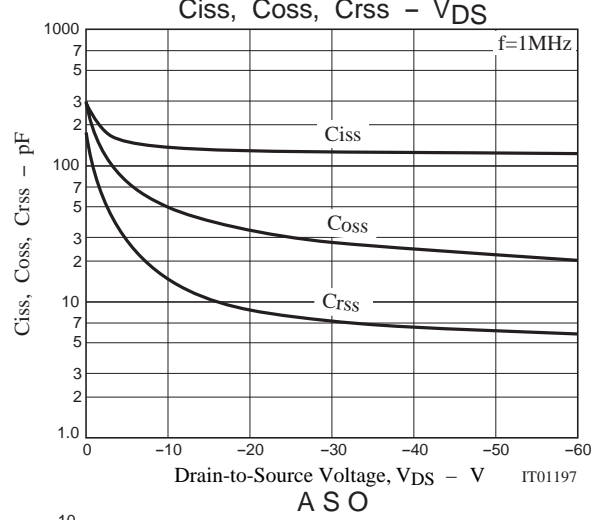
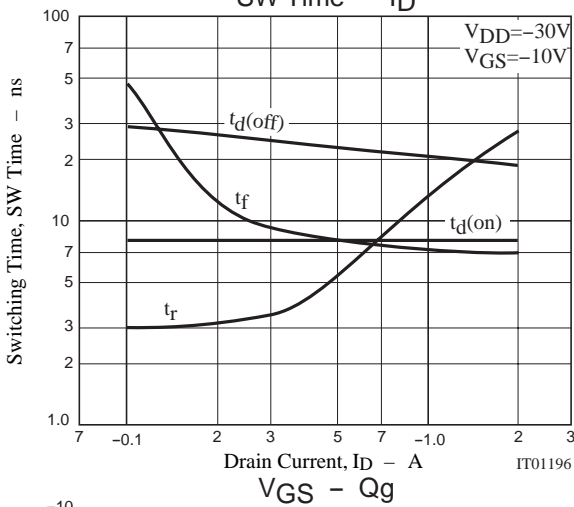
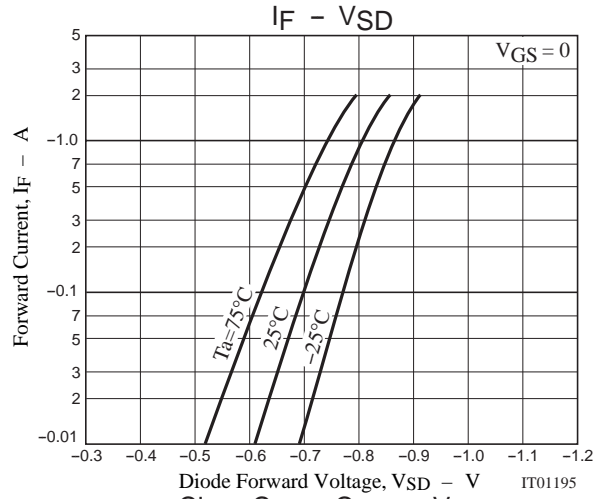
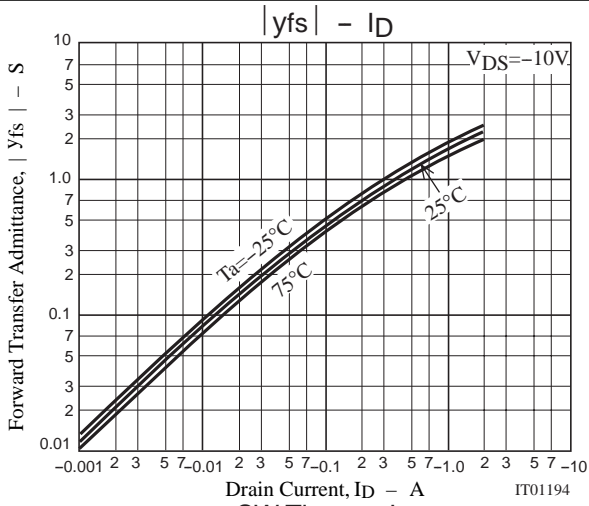
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		8		ns
Rise Time	t_r	See specified Test Circuit		6		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		23		ns
Fall Time	t_f	See specified Test Circuit		8		ns
Total Gate Charge	Qg	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1A$		5		nC
Gate-to-Source Charge	Qgs	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1A$		0.8		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=-10V, V_{GS}=-10V, I_D=-1A$		0.9		nC
Diode Forward Voltage	V_{SD}	$I_S=-1A, V_{GS}=0$		-0.81	-1.2	V

Switching Time Test Circuit



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