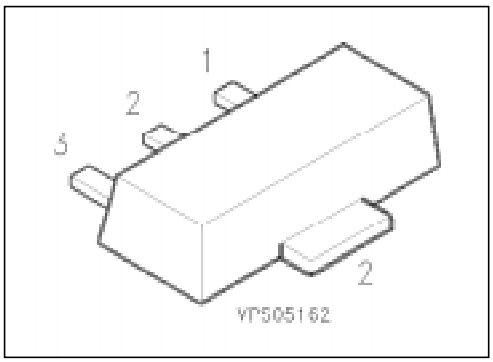
SIEMENS

NPN Silicon High-Voltage Transistor — BF 622

- Suitable for video output stages in TV sets
- High breakdown voltage
- Low collector-emitter saturation voltage
- Low capacitance
- Complementary type: BF 623 (PNP)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration 1	2	3	Package[1]
BF 622	DA	Q62702-F1052	B	C	E	SOT-89

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE0}	250	V
Collector-base voltage	V_{CB0}	250	
Collector-emitter voltage, R_{BE} = 2.7 kΩ	V_{CER}	250	
Emitter-base voltage	V_{EB0}	5	
Collector current	I_C	50	mA
Peak collector current	I_{CM}	100	
Total power dissipation, T_S = 120 °C	P_{tot}	1	W
Junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	– 65 … + 150	

Thermal Resistance

Junction - ambient[2]	$R_{th\ JA}$	≤ 90	K/W
Junction - soldering point	$R_{th\ JS}$	≤ 30	

1) For detailed information see chapter Package Outlines.

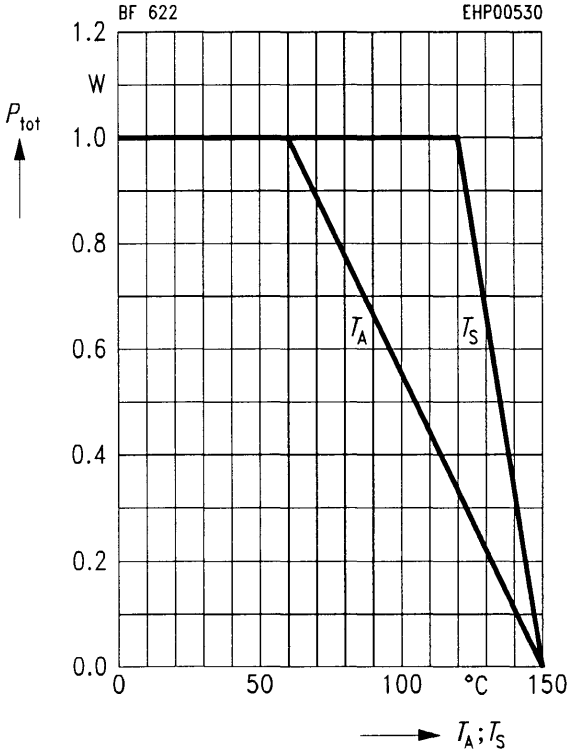
2) Package mounted on epoxy pcb 40 mm × 40 mm × 1.5 mm/6 cm² Cu.

Electrical Characteristics
at T_A = 25 °C, unless otherwise specified.

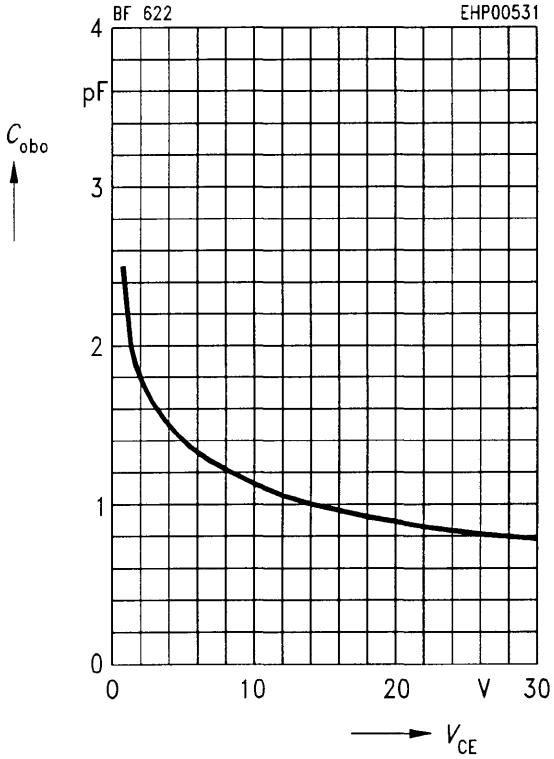
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC characteristics					
Collector-emitter breakdown voltage					V
I_C = 1 mA	$V_{(BR)CE0}$	250	–	–	
I_C = 10 µA, R_{BE} = 2.7 kΩ	$V_{(BR)CER}$	250	–	–	
Collector-base breakdown voltage I_C = 10 µA	$V_{(BR)CB0}$	250	–	–	
Emitter-base breakdown voltage I_E = 10 µA	$V_{(BR)EB0}$	5	–	–	
Collector cutoff current	I_{CB0}				
V_{CB} = 200 V		–	–	100	nA
V_{CB} = 200 V, T_A = 150 °C		–	–	20	µA
Collector cutoff current	I_{CER}				µA
V_{CE} = 200 V, R_{BE} = 2.7 kΩ		–	–	1	
V_{CE} = 200 V, R_{BE} = 2.7 kΩ, T_A = 150 °C		–	–	50	
Emitter cutoff current V_{EB} = 5 V	I_{EB0}	–	–	10	
DC current gain[1)] I_C = 25 mA, V_{CE} = 20 V	h_{FE}	50	–	–	–
Collector-emitter saturation voltage[1)] I_C = 10 mA, I_B = 1 mA	V_{CEsat}	–	–	0.5	V
Base-emitter saturation voltage[1)] I_C = 10 mA, I_B = 1 mA	V_{BEsat}	–	–	1	
AC characteristics					
Transition frequency I_C = 10 mA, V_{CE} = 10 V, f = 20 MHz	f_T	–	100	–	MHz
Output capacitance V_{CB} = 30 V, f = 1 MHz	C_{obo}	–	0.8	–	pF

1) Pulse test: $t \leq 300$ µs, D = 2 %.

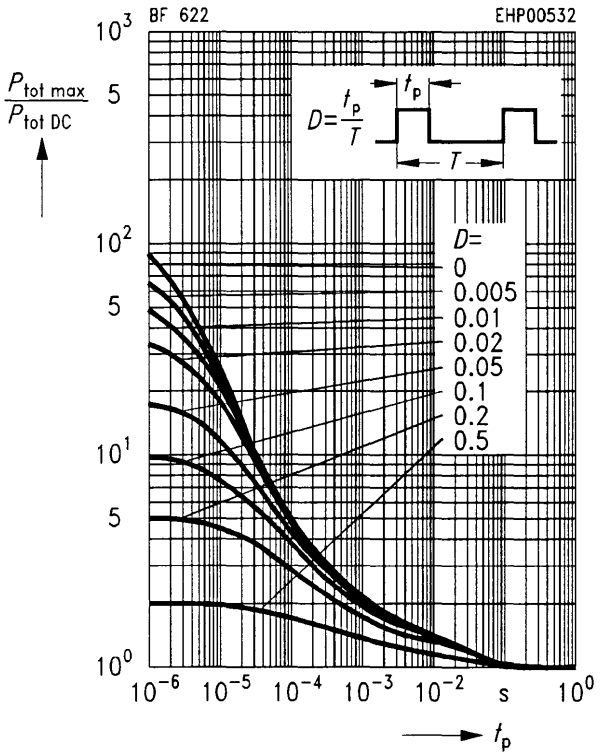
Total power dissipation $P_{tot} = f(T_A^*; T_S)$
* Package mounted on epoxy



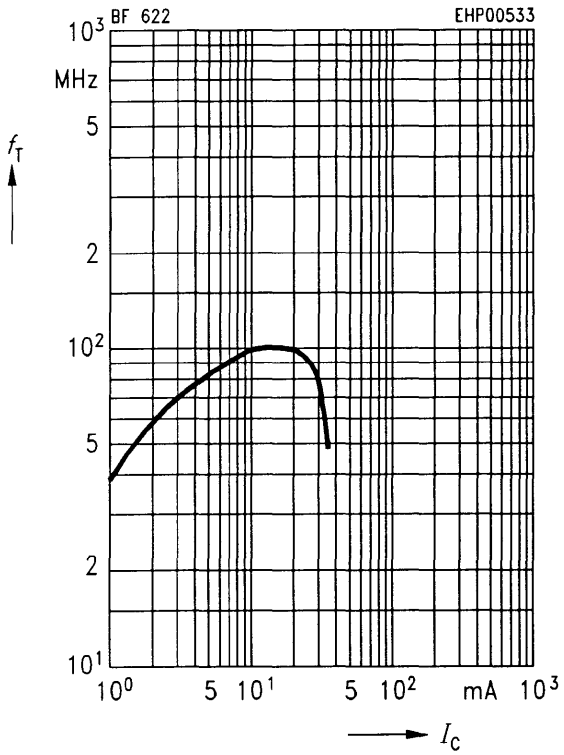
Output capacitance $C_{obo} = f(V_{CE})$
f= 1 MHz



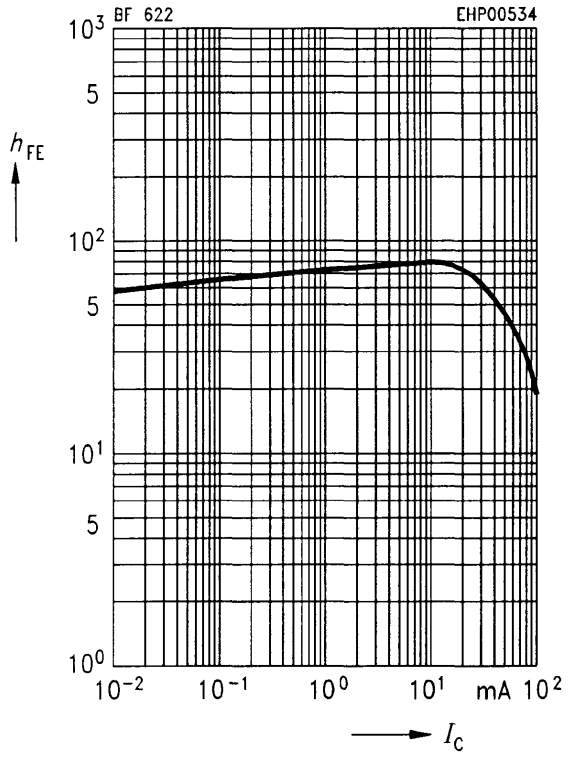
Permissible pulse load $P_{tot\,max}/P_{tot\,DC} = f(t_p)$



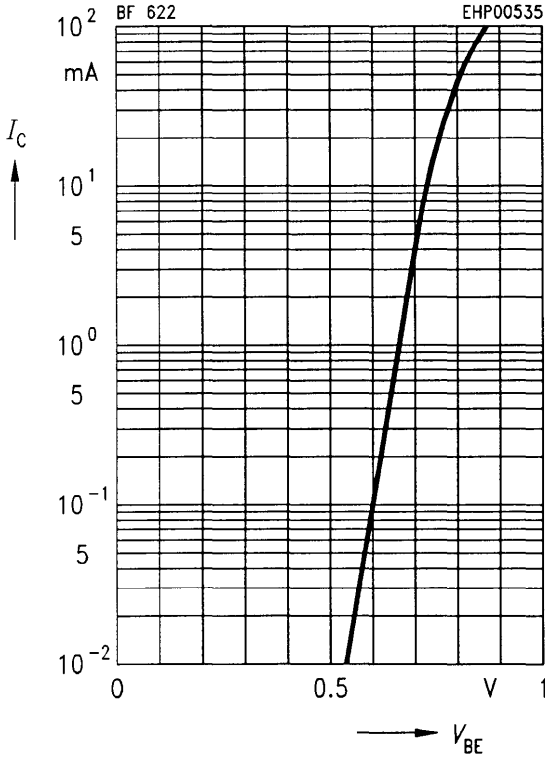
Transition frequency $f_T = f(I_C)$
V_{CE} = 10 V, f = 20 MHz



DC current gain $h_{FE} = f(I_C)$
V_{CE} = 20 V



Collector current $I_C = f(V_{BE})$
V_{CE} = 20 V



Collector cutoff current $I_{CB0} = f(T_A)$
V_{CB} = 200 V