

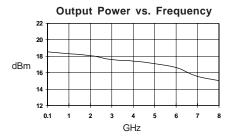
## **Product Description**

Stanford Microdevices' SNA-600 is a high-performance GaAs Heterojunction Bipolar Transistor (MMIC) in die form. A Darlington configuration is utilized for broadband performance to 6.5 GHz.

These unconditionally stable amplifiers provide 11dB of gain and +18dBm of P1dB when biased at 5.7V and 70mA. P1dB and TOIP may be improved by 2dB by biasing @ 100mA.

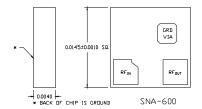
This MMIC requires only a single supply voltage. The use of an external resistor allows for bias flexibility and stability. Also available in packaged form (SNA-676, -686 & -687), its small size (0.4mm x 0.4mm) and gold metallization make it an ideal choice for use in hybrid circuits.

The SNA-600 is available in gel paks at 100 devices per container.



# **SNA-600**

# DC-6.5 GHz, Cascadable GaAs MMIC Amplifier



## **Product Features**

- Cascadable 50 Ohm Gain Block
- 11dB Gain, +18dBm P1dB
- High Linearity, +36dBm TOIP Typ.
- 1.5:1 Input and Output VSWR
- Chip Back Is Ground

# **Applications**

- Narrow and Broadband Linear Amplifiers
- Commercial and Industrial Applications

#### Electrical Specifications at Ta = 25C

Symbol	Param eters: Test Conditions: Id = 70 m A, Z <sub>0</sub> = 50 O h m s		Units	Min.	Тур.	Мах.
G <sub>P</sub>	Small Signal Gain	f = 0.1-4.0 G H z f = 4.0-6.5 G H z	d B d B	9.0	11.0 9.0	
B W 3 d B	3dB Bandwidth		G H z		6.5	
P <sub>1 d B</sub>	Output Power at 1dB Compression	f = 0.1-2.0 G H z f = 2.0-6.5 G H z	d B m		18.0 16.0	
N F	Noise Figure	f = 0.1-4.0 G H z f = 4.0-6.5 G H z	d B		7.5 8.5	
VSWR	Input / Output	f = 0.1-6.5 GHz			1.5:1	
IP <sub>3</sub>	Third Order Intercept Point	f = 0.1-2.0 G H z f = 2.0-6.5 G H z	d B m d B m		36.0 34.0	
T D	Group Delay	f = 2.0 GHz	psec		120	
IS O L	Reverse Isolation	f = 0.1-6.5 GHz	d B		17.0	
V D	Device Voltage		V	4.8	5.7	6.8
dG/dT	Device Gain Temperature Coefficient		dB/degC		-0.0023	
dV/dT	Device Voltage Temperature Coefficient		m V/degC		-5.0	

The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions.

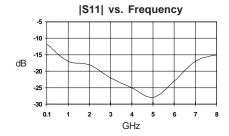
Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.

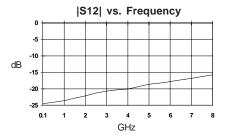
Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.

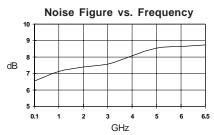


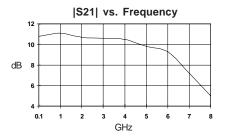
# SNA-600 DC-6.5 GHz Cascadable MMIC Amplifier

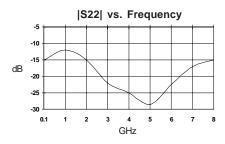
## Typical Performance at $25^{\circ}$ C (Vds = 5.7V, Ids = 70mA)

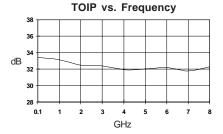














## SNA-600 DC-6.5 GHz Cascadable MMIC Amplifier

#### **Absolute Maximum Ratings**

Parameter	Absolute Maximum	
Device Current	150mA	
Power Dissipation	1000mW	
RF Input Power	200mW	
Junction Temperature	+200C	
Operating Temperature	-45C to +85C	
Storage Temperature	-65C to +150C	

#### Notes:

 Operation of this device above any one of these parameters may cause permanent damage.

### MTTF vs. Temperature @ Id = 70mA

Die Bottom Temperature	Junction Temperature	MTTF (hrs)				
+75C	+155C	1000000				
+110C	+190C	100000				
+140C	+220C	10000				

Thermal Resistance (Lead-Junction): 200° C/W

#### Die Attach

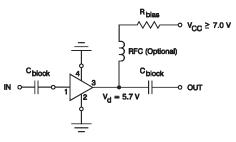
The die attach process mechanically attaches the die to the circuit substrate. In addition, it electrically connects the ground to the trace on which the die is mounted and establishes the thermal path by which heat can leave the die.

#### **Assembly Techniques**

Epoxy die attach is recommended. The top and bottom metallization is gold. Conductive silver-filled epoxies are recommended. This method involves the use of epoxy to form a joint between the backside gold of the chip and the metallized area of the substrate. A 150 C cure for 1 hour is necessary. Recommended epoxy is Ablebond 84-1LMIT1 from Ablestik.

#### Part Number Ordering Information

Part Number	Devices Per Pak
SNA-600	100



**Typical Biasing Configuration** 

#### Wire Bonding

Electrical connections to the die are through wire bonds. Stanford Microdevices recommends wedge bonding or ball bonding to the pads of these devices.

#### Recommended Wedge Bonding Procedure

- 1. Set the heater block temperature to 260C +/- 10C.
- 2. Use pre-stressed (annealed) gold wire between 0.0005 to 0.001 inches in diameter.
- 3. Tip bonding pressure should be between 15 and 20 grams and should not exceed 20 grams. The footprint that the wedge leaves on the gold wire should be between 1.5 and 2.5 wire diameters across for a good bond.

522 Almanor Ave., Sunnyvale, CA 94086

Phone: (800) SMI-MMIC