## Product Description

Stanford Microdevices' SSW-208 is a high perfomance Gallium Arsenide Field Effect Transistor MMIC switch housed in a low-cost surface-mountable small outline plastic package.

This single-pole, double-throw, non-reflective switch consumes less than 50 uA and operates at -5 V and 0 V for control bias. Its high isolation and low insertion loss makes it ideal for $T / R$ switching in analog and digital wireless communication systems.

The die is fabricated using 0.5 micron FET process with gold metallization and silicon nitride passivation to achieve excellent performance and reliability.


VControl =-5 V

Electrical Specifications at $\mathrm{Ta}=25 \mathrm{C}$

## SSW-208

## DC-4 GHz, High Isolation GaAs MMIC SPDT Switch



## Product Features

- High Isolation: 22dB at 2GHz
- Low DC Power Consumption
- Low Insertion Loss: 0.9dB at 2GHz
- Broad Performance - True DC Operation
- Low Cost Small Outline Plastic Package


## Applications

## - Analog/Digital Wireless System <br> - Spread Spectrum <br> - GPS

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## SSW-208 DC-6 GHz Absorptive SPDT GaAs Switch

## Absolute Maximum Ratings

| RF Input Power | $2 \mathrm{~W} \mathrm{Max}>500 \mathrm{MHz}$ |
| :--- | :--- |
| Control Voltage | -10 V |
| Operating <br> Temperature | -45 C to +85 C |
| Storage <br> Temperature | -65 C to +150 C |
| Thermal Resistance | 20 deg C/W |

Pin Out

| Pin | Function |
| :---: | :---: |
| 1 | GND |
| 2 | J1 |
| 3 | GND |
| 4 | GND |
| 5 | J2 |
| 6 | V 1 |
| 7 | V 2 |
| 8 | J 3 |

Truth Table

| V1 | V2 | J1-J2 | J1-J3 |
| :---: | :---: | :---: | :---: |
| 0 | -5 | Low Loss | Isolation |
| -5 | 0 | Isolation | Low Loss |

Switch Schematic

$\bigcirc \longrightarrow \mathrm{J} 3$


Pin numbers shown for reference only, not marked on part

On Port Input/Output VSWR vs. Frequency
VControl =-5 V



[^0]:    The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions.

