## L9907N

## MOTOR BRIDGE FOR HEADLIGHT ADJUSTMENT

- FULL BRIDGE OUTPUT CONFIGURATION WITH LOW SATURATION VOLTAGE LESS THAN 3.2V AT OUTPUT CURRENT 0.7A
- OPERATING SUPPLY VOLTAGE RANGE 7V TO 18V. SUPPLY OVERVOLTAGEUP TO 50V
- HIGH POSITIONING PRECISION AND HIGH NOISE IMMUNITY DUE TO TRANSFER CHARACTERISTICS WITH NEUTRAL ZONE AND STOP RANGE THRESHOLD
- FAST STOP THROUGH SHORT-CIRCUITING THE MOTOR
- MOTOR STOP STATUS IN CASE OF OPEN INPUT CONDITION
- SUPPLY OVERVOLTAGE PROTECTION FUNCTION FOR Vs MORE THAN 18V, UP TO 50V
- INPUT PROTECTION AGAINST TRANSIENTS ON THE BATTERY LINE AND THE REVERSE BATTERY CONDITION
- THERMAL OVERLOAD PROTECTION
- ESD PROTECTED ACCORDING TO MIL883C


DESCRIPTION
The L9907N is a monolithic integrated power comparator with full bridge output configuration, intended for driving DC motors in positioning systems, optimized for headlight adjustment application and respecting the automotive electronics environmental conditions.

## BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{SDC}}$ | DC Supply Voltage | 26 | V |
| $\mathrm{~V}_{\mathrm{SP}}$ | Supply Voltage Pulse ( $\mathrm{T} \leq 400 \mathrm{~ms}$ ) | 50 | V |
| $\mathrm{l}_{\text {OUT } \mathrm{DC}}$ | DC Output Current | $\pm 0.4$ | A |
| $\mathrm{I}_{\mathrm{OUT}} \mathrm{P}$ | Output Current Pulsed (100ms) | 0.8 | A |
| $\mathrm{I}_{\mathrm{N}}$ | DC Input Current | $\pm 10$ | mA |
| $\mathrm{I}_{\mathrm{N}}$ | Input Current Pulse (2ms) | $\pm 40$ | mA |
| $\mathrm{~T}_{\mathrm{S}}$ | Storage Shutdown Junction Temperature Range ( $\left.^{*}\right)$ | 150 | ${ }^{\circ} \mathrm{C}$ |

(*) Recommended maximal $\mathrm{T}_{\mathrm{amb}} \leq 105^{\circ} \mathrm{C}$

PIN CONNECTION


THERMAL DATA

| Symbol | Parameter | SO20L | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{R}_{\mathrm{th} j \text {-amb }}$ | Thermal Resistance Junction-ambient (1) | 50 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\mathrm{th} j \text {-pins }}$ | Thermal Resistance Junction-pins | 15 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

(1) with $6 \mathrm{~cm}^{2}$ on board heat sink area

ELECTRICAL CHARACTERISTICS ( $7 \mathrm{~V} \leq \mathrm{V}_{\mathrm{s}} \leq 18 \mathrm{~V},-40^{\circ} \mathrm{C}<\mathrm{T}_{\mathrm{j}}<150^{\circ} \mathrm{C}$; unless otherwise specified.)

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\square}$ | Quiescent Current | I Out $=0$, (Output Open) <br> $\left\|V_{\text {din }}\right\|<20 \mathrm{mV}$ (stop) <br> $\left\|V_{\text {din }}\right\|<200 \mathrm{mV}$ (L or R) |  | $\begin{aligned} & 7 \\ & 6 \\ & \hline \end{aligned}$ | $\begin{aligned} & 9 \\ & 7 \\ & \hline \end{aligned}$ | $\underset{\mathrm{mA}}{\mathrm{~mA}}$ |
| $-\mathrm{V}^{-}, \mathrm{V}^{+}+$ | Neutral Zone Threshold (2) | $\begin{aligned} & 1.5<V_{\text {INC }}<V_{S}-2 \mathrm{~V} ; \mathrm{V}_{\mathrm{S}}=12 \mathrm{~V} \\ & R_{P R}=\infty \\ & R_{P R}=0 \end{aligned}$ | $\begin{aligned} & 3 \times V_{S T} \\ & 3 \times V_{S T} \end{aligned}$ | $\begin{aligned} & 120 \\ & 240 \end{aligned}$ | $\begin{gathered} 4 \times \\ \mathrm{VST} \\ 4 \times \mathrm{V}_{\mathrm{ST}} \\ \hline \end{gathered}$ | $\begin{aligned} & \mathrm{mV} \\ & \mathrm{mV} \end{aligned}$ |
| $-^{-\mathrm{V}_{\text {ST }}}$, $\mathrm{V}_{\text {ST+ }}$ | Stop Range Threshold | $\begin{aligned} & 1.5<\mathrm{VINC}<\mathrm{V}_{\mathrm{S}}-2 \mathrm{~V} ; \mathrm{V}_{\mathrm{S}}=12 \mathrm{~V} \\ & \mathrm{R}_{P R}=\infty \\ & \mathrm{R}_{\mathrm{PR}}=0 \end{aligned}$ | $\begin{aligned} & 25 \\ & 50 \\ & \hline \end{aligned}$ | $\begin{aligned} & 38 \\ & 76 \\ & \hline \end{aligned}$ | $\begin{gathered} 60 \\ 100 \\ \hline \end{gathered}$ | $\begin{aligned} & \mathrm{mV} \\ & \mathrm{mV} \end{aligned}$ |
| $\mathrm{V}_{\text {INCL }}$ | Control Input LOW Disable Threshold | $\begin{aligned} & \mathrm{T}_{\mathrm{j}}=-40 \text { to }+25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{j}}=>25^{\circ} \mathrm{C} \end{aligned}$ | $\begin{aligned} & 0.8 \\ & 0.6 \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline 1.2 \\ & 1.2 \\ & \hline \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 1.5 \\ & \hline \end{aligned}$ | $\begin{aligned} & \hline \mathrm{V} \\ & \mathrm{~V} \end{aligned}$ |
| $\mathrm{V}_{\text {INCH }}$ <br> $\mathrm{V}_{\mathrm{CH}(3)}$ | Control Input HIGH Disable Threshold (4) | $\begin{aligned} & \text { Outputs }=\text { ON } \\ & \text { with RINC }=0 \Omega \\ & \text { with RINC }=5 \mathrm{~K} \Omega \\ & \text { with } \operatorname{RINC}=10 \mathrm{~K} \Omega \end{aligned}$ |  |  | $\begin{gathered} \mathrm{V}_{\mathrm{s}-2} \\ \mathrm{~V}_{\mathrm{s}}-1.5 \\ \mathrm{~V}_{\mathrm{s}-1.4} \\ \hline \end{gathered}$ | $\begin{aligned} & V \\ & V \\ & V \end{aligned}$ |
|  |  | $\begin{aligned} & \text { Outputs }=\text { OFF } \\ & \text { with } \mathrm{R}_{\text {INC }}=0 \Omega \\ & \text { with } \mathrm{R}_{\text {INC }}=5 \mathrm{~K} \Omega \\ & \text { with } \mathrm{R}_{\text {INC }}=10 \mathrm{~K} \Omega \end{aligned}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{S}}-0.8 \\ & \mathrm{~V}_{\mathrm{S}}-0.6 \\ & \mathrm{~V}_{\mathrm{s}-0.4} \\ & \hline \end{aligned}$ |  |  | V V |
| 1 N | Input Bias Current | $\begin{aligned} & 1.5<\mathrm{V}_{\text {INC }}<\mathrm{V}_{\mathrm{S}}-2 \mathrm{~V} ; \mathrm{V}_{\mathrm{S}}=12 \mathrm{~V} \\ & \mathrm{~V}_{\text {din }}=0 ; \mathrm{R}_{\mathrm{PR}}=\infty \\ & \mathrm{V}_{\text {din }}= \pm 200 \mathrm{mV} ; \mathrm{R}_{\mathrm{PR}}=\infty \\ & \mathrm{V}_{\text {din }}=0 ; \mathrm{R}_{\mathrm{PR}}=0 \\ & \mathrm{~V}_{\text {din }}= \pm 200 \mathrm{mV} ; \mathrm{R}_{\mathrm{PR}}=0 \end{aligned}$ |  | $\begin{gathered} 0.45 \\ 0.9 \\ 0.8 \\ 1.5 \\ \hline \end{gathered}$ | $\begin{aligned} & 2.0 \\ & 4.0 \\ & 3.6 \\ & 6.8 \\ & \hline \end{aligned}$ | $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ |
| Vosı | Output Saturation Voltage Sink Stage | $\begin{aligned} & \text { lout }=0.7 \mathrm{~A} \\ & \text { lout }=0.35 \mathrm{~A} \end{aligned}$ |  | $\begin{aligned} & \hline 1.1 \\ & 0.8 \\ & \hline \end{aligned}$ | $\begin{aligned} & 1.4 \\ & 1.1 \\ & \hline \end{aligned}$ | $\begin{aligned} & \mathrm{V} \\ & \mathrm{~V} \end{aligned}$ |
| Voso | Output Saturation Voltage Source Stage | $\begin{aligned} & \text { lout }=0.7 \mathrm{~A} \\ & \text { lout }=0.35 \mathrm{~A} \\ & \hline \end{aligned}$ |  | $\begin{aligned} & 1.2 \\ & 0.9 \\ & \hline \end{aligned}$ | $\begin{array}{r} 2 \\ 1.5 \\ \hline \end{array}$ | $\begin{aligned} & \mathrm{V} \\ & \mathrm{~V} \end{aligned}$ |

(2) With a programming resistor RPR between the PR pin and GND the N+ and N-thresholds can be adjusted from the nominal value ( $R_{P R}=\infty$ , pin PR open) up to two times the nominal value (RPR = 0, pin PR shorted to GND).
The formula defining $V_{N_{+}}, V_{N-}$ typical value as a function of $R_{P R}$ and $V_{S}$ is: $-V_{N-}=V_{N+}=\left(36 m V+0.017 \cdot V_{S}\right) \cdot \frac{1+\frac{R_{P R}}{9.5 K \Omega}}{1+2 \cdot \frac{R_{P R}}{9.5 K}}$.
for $R_{P R}=\infty$ this formula reduced to:
$-V_{N+}\left(R_{P R}=\infty\right)=V_{N_{+}}\left(R_{P R}=\infty\right)=18 \mathrm{mV}+0.0086 \cdot V_{\text {S }}$.
for $V_{s}$ in $V$ and $R_{P R}$ in $K \Omega$ these formulas result in $m V$
(3) $\mathrm{V}_{\text {CH }}$ is the control input voltage applied to the pin $\mathrm{I}_{\mathrm{NC}}$ through a serial resistor RINC
(4) OUTPUTS $=$ UNDEFINED for: $\mathrm{V}_{\mathrm{S}}-2 \mathrm{~V}<\mathrm{V}_{\mathrm{CH}}($ Rinc $=0 \Omega)<\mathrm{V}_{\mathrm{S}}-0.8 \mathrm{~V}$

$$
\begin{aligned}
& \mathrm{V}_{\mathrm{S}}-1.5 \mathrm{~V}<\mathrm{V}_{\mathrm{CH}}(\mathrm{RIINC}=5 \mathrm{~K} \Omega)<\mathrm{V}_{\mathrm{S}}-0.6 \mathrm{~V} \\
& \mathrm{~V}_{\mathrm{S}}-1.4 \mathrm{~V}<\mathrm{V}_{\mathrm{CH}}\left(\mathrm{R}_{\text {INC }}=10 \mathrm{~K} \Omega\right)<\mathrm{V}_{\mathrm{S}}-0.4 \mathrm{~V}
\end{aligned}
$$

Figure 1: L9907N Differential Input to Output Transfer Characteristics

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## FUNCTIONAL DESCRIPTION

The L9907N is a power comparator with fullbridge push-pull outputs, intended for driving a DC motor in the headling adjustment system.
The basic function of the device is shown in the input-output tranfer characteristic, Fig. 1.
For differential input voltage (VINC - VINF) lower than the negative NEUTRAL ZONE threshold, $\mathrm{V}_{\mathrm{N}}$ the output voltage is negative (corresponds to motor direction right), for increasing differential input voltage, this status changes to the zero output voltage (motor is actively braked), when the differential input voltage exceeds the $\mathrm{V}_{\text {ST }}$ - stop range threshold. The output status remains in this condition as long as the differential input voltage remains within the NEUTRAL ZONE. If the input voltage increases above the positive NEUTRAL ZONE threshold $\mathrm{V}_{\mathrm{N}_{+}}$, the output voltage become positive (motor direction left).
Similar behaviour is obtained for decreasing the input voltage. The output status transitions are marked with the arrows showing the corresponding direction of the output status variation. The active braking mode is got with both outputs in "HIGH" status
The above described behaviour assures high positioning precision, corresponding to the STOP RANGE and high noise immunity in the adjusted condition due to the NEUTRAL ZONE hysteresis thresholds.
Both the above described thresholds are defined referring to $I_{N C}, l_{N F}$ pins. The error due to voltage
drop over the input signal source resistances Rinc , RiNf is minimized with extremely low input bias currents linc, linf.
The above mentioned resistors (Rinc, Rinf) are necessary for the input current limitation during the transients on the VBAT line. The input source resistors must be dimensioned so that in case of a line transient the input current in the input pin, clamped with the internal input protection diodes do not increase over the specified absolute maximum value.

Figure 2: Control to feedbacktransfer characteristic for tracking regulation $\left(\mathrm{V}_{\mathrm{F}}=\mathrm{V}_{\mathrm{C}}\right.$ within the nominal Vinc operating range):


Figure 3: Recommended Application Circuit Diagram with L9907N for tracking regulation $\left(V_{F}=V_{C}\right.$ for $\left.1.5 \mathrm{~V}<\mathrm{V}_{\mathrm{C}}<\mathrm{V}_{\mathrm{S}}-2 \mathrm{~V}\right)$ :


Note:
Recommended value of $R_{I N C}$, RiNF (equivalent input resistance to $I_{N C}$ and $I_{N F}$ ) is $5 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$. Resistor $R_{C F}$ should assure that the differential input voltage |Vinc - Vinf| remains withinthe NEUTRAL ZONE, when the control signal wire become broken. When this condition is fulfiled the motor will not change its previous position.

An external resistor RCF is recommended between the slider of the control and feedback potentiometer.
This resistor assures that in the case of input control or input feedback wire interruption the input differential voltage will be within the NEUTRAL ZONE and the motor position remains frozen.
The circuit features an overvoltage disable function referred to the supply voltage $V_{s}$ higher than 18 V , both outputs are forced to tristate in this condition.
The thermal overload function disables the outputs (tristate) when the junction temperature increases above the thermal shutdown threshold temperature of $\mathrm{min} .150^{\circ} \mathrm{C}$.
For the start of a heavy loaded motor, if the motor current reaches the max. value it is necessary to respect the dynamical thermal resistance junction to ambient. The maximum output current is 0.8 A .
The maximum junction temperature in this phase should not increase above the thermal shutdown threshold. In case of output disable due to thermal overload the output remains disabled till the junction temperature decreases under the thermal enable threshold. This behaviour is assured with the thermal shutdown hysteresis threshold, which minimum value is $20^{\circ} \mathrm{C}$.
Figure 3,5 and 7 show typical application diagrams for headlight adjustment applications. To
assure the safety of the circuits in the reverse battery condition a reverse protection diode $D_{1}$, is necessary.
The input currents in this condition are limited by the resistors Rinc and Rinf. The transient protection diode $D_{2}$ must assure that the maximum rating for $\mathrm{V}_{S}$ during the transients at $\mathrm{V}_{\mathrm{BAT}}$ line will be limited to a value lower than absolute maximum

Figure 4: Control to feedbacktransfer characteristic for proportional regulation with extended $V_{C}$ operating range:


Figure 5: Recommended Application Circuit Diagram with L9907N for proportional regulation with extended
$\mathrm{V}_{\mathrm{C}}$ operating range. For ideal adjusted condition $\quad \mathrm{V}_{\mathrm{F}}=\frac{\mathrm{R}_{\operatorname{INC} 2}}{\mathrm{RINC1}+\mathrm{RINC2}} \cdot \mathrm{~V}_{\mathrm{C}}$ :


[^0]Figure 6: Control to feedback transfer characteristic for proportional regulation with reduced Vc operating range:

rating for $V_{\mathrm{S}}$. The device features an output disable function in case of control input voltage overdrive.
When the control input voltage increases above the HIGH control input disable threshold VINC $\geq$ $\mathrm{V}_{\text {INCH }}$, typically $\mathrm{V}_{\mathrm{S}}-1.2 \mathrm{~V}$ or decreases below the LOW control input disable threshold $\mathrm{V}_{\text {INC }} \geq \mathrm{V}_{\text {INCH }}$, typically 1.2 V , then both outputs will be forced to tristate.
The potential of the Inc pin is clamped at the Control Input HIGH disable threshold potential with a series resistor of $5 \mathrm{~K} \Omega$ typ. To activate the HIGH disable comparator an input current of $35 \mu \mathrm{~A}$ typ. is needed. To respect this behaviour in the application $V_{C R}$ is specified for different RINC.

Figure 7: Recommended Application Circuit Diagram with L9907N for proportional regulation with reduced $\mathrm{V}_{\mathrm{C}}$ operating range. For ideal adjusted condition $\mathrm{V}_{\mathrm{F}}=\frac{\mathrm{R}_{\text {INF2 }}}{\mathrm{RINF2}+\text { RINF2 }} \cdot \mathrm{V}_{\mathrm{C}}$ :


Note:
Recommended value of RINC, RINF (equivalent input resistance to $I_{N C}$ and $I_{N F}$ ) is $5 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$. Resistor R $_{\text {CF }}$ should assure that the differential input voltage |VINC - VINF| remains within the NEUTRAL ZONE, when the control signal wire become broken. When this condition is fulfilled the motor will not change its previous position.

## SO20 PACKAGE MECHANICAL DATA

| DIM. | mm |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A |  |  | 2.65 |  |  | 0.104 |
| a1 | 0.1 |  | 0.3 | 0.004 |  | 0.012 |
| a2 |  |  | 2.45 |  |  | 0.096 |
| b | 0.35 |  | 0.49 | 0.014 |  | 0.019 |
| b1 | 0.23 |  | 0.32 | 0.009 |  | 0.013 |
| C |  | 0.5 |  |  | 0.020 |  |
| c1 | $45^{\circ}$ (typ.) |  |  |  |  |  |
| D | 12.6 |  | 13.0 | 0.496 |  | 0.512 |
| E | 10 |  | 10.65 | 0.394 |  | 0.419 |
| e |  | 1.27 |  |  | 0.050 |  |
| e3 |  | 11.43 |  |  | 0.450 |  |
| F | 7.4 |  | 7.6 | 0.291 |  | 0.299 |
| L | 0.5 |  | 1.27 | 0.020 |  | 0.050 |
| M |  |  | 0.75 |  |  | 0.030 |
| S | $8^{\circ}$ (max.) |  |  |  |  |  |

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[^0]:    Note:
    Recommended value of RiNc, $\operatorname{RiNF}$ (equivalent input resistance to $I_{N C}$ and $I_{N F}$ ) is $5 \mathrm{~K} \Omega$ to $10 \mathrm{~K} \Omega$. If the motor should not change its position, when the control signal wire become open, RiNC1 and RiNC2 should be rated so that $\mathrm{V}_{\mathrm{INC}} \leq 0.4 \mathrm{~V}$ in this condition.

