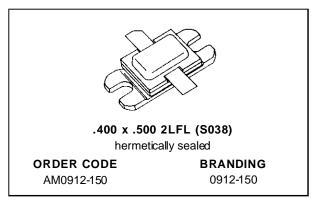


AM0912-150

RF & MICROWAVE TRANSISTORS AVIONICS APPLICATIONS

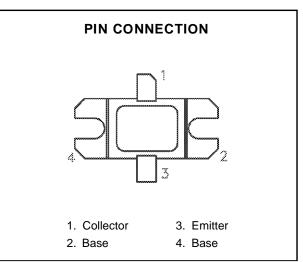
- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 150 W MIN. WITH 7.5 dB GAIN
- BANDWIDTH = 255MHz



DESCRIPTION

The AM0912-150 is designed for specialized avionics applications including Mode-S, TCAS and JTIDS, where power is provided under pulse formats utilizing short pulse widths and high burst or overall duty cycles.

The AM0912-150 is housed in the unique BIG-PACTM Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS (T_{case} = 25°C)

Symbol	Parameter	Value	Unit
PDISS	Power Dissipation* (T _C ≤ 100°C)	300	W
Ic	Device Current*	16.5	А
Vcc	Collector-Supply Voltage*	35	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)}	Junction-Case Thermal Resistance*	0.57	°C/W
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^{*}Applies only to rated RF amplifier operation

September 1992

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

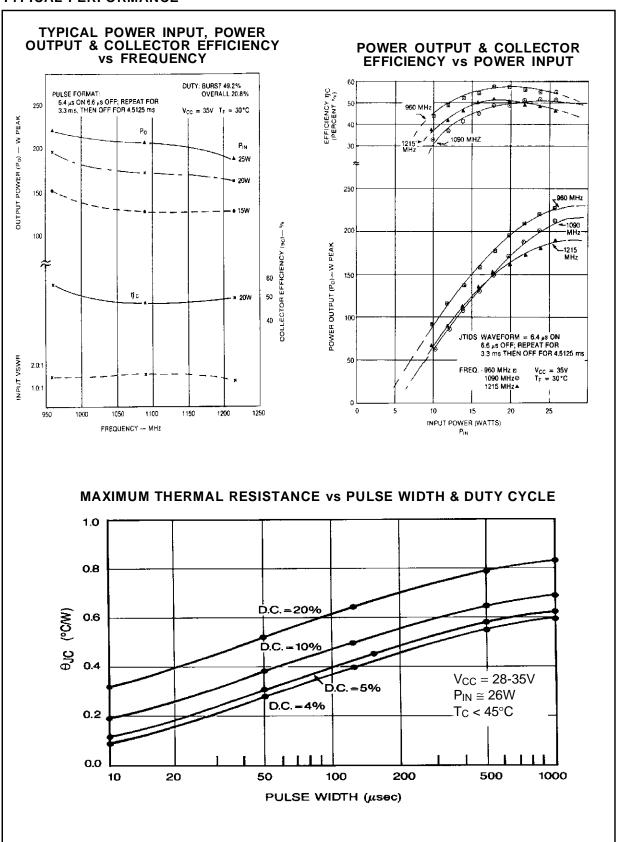
Symbol	Test Conditions		Value		
		Min.	Тур.	Max.	Unit
BV _{CBO}	$I_C = 60 \text{mA}$ $I_E = 0 \text{mA}$	55	65	_	V
BV _{EBO}	$I_E = 10mA$ $I_C = 0mA$	3.5	_	_	V
BVces	IC = 100mA	55	_	_	V
ICES	V _{CE} = 35V		_	25	mA
h _{FE}	$V_{CE} = 5V$ $I_C = 5A$	20	_	_	_

DYNAMIC

Symbol	Took Conditions		Value			l lm:4	
Symbol		Test Conditions			Тур.	Max.	Unit
Pout	f = 960 — 1215MHz	$P_{IN}=26.7W$	$V_{CC} = 35V$	150	_	_	W
ης	f = 960 — 1215MHz	$P_{IN}=26.7W$	$V_{CC} = 35V$	45	_	_	%
GP	f = 960 — 1215MHz	$P_{IN} = 26.7W$	Vcc = 35V	7.5	_		dB

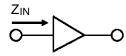
Note: Pulse Format: $6.4~\mu\text{S}$ on $6.6~\mu\text{S}$ off; repeat for 3.3 ms, then off for 4.5125 ms Duty Cycle: Burst 49.2% overall 20.8%

TYPICAL PERFORMANCE

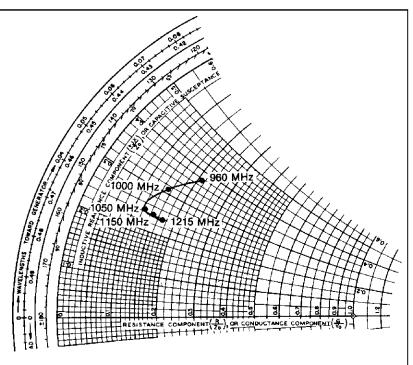


IMPEDANCE DATA



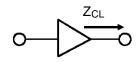


 $\begin{aligned} P_{IN} &= 26.7 \text{ W} \\ V_{CC} &= 35 \text{ V} \\ Z_{O}^{\star} &= 10 \text{ ohms} \end{aligned}$



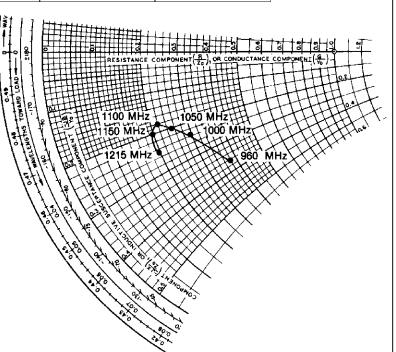
FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
L = 960 MHz	2.1 + j 3.8	3.8 – j 3.6
• = 1000 MHz	1.5 + j 3.1	3.0 – j 2.4
M = 1050 MHz	1.2 + j 2.5	2.5 – j 2.0
• = 1150 MHz	1.5 + j 2.4	2.0 – j 2.0
H = 1215 MHz	1.7 + j 2.4	2.0 – j 2.5

TYPICAL COLLECTOR LOAD IMPEDANCE

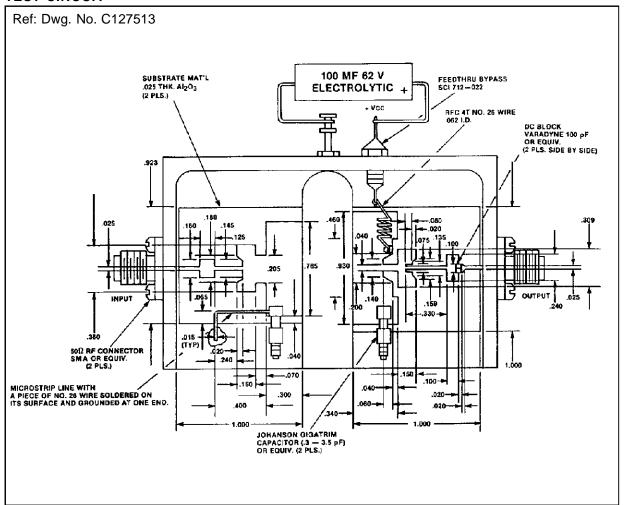


 $\begin{aligned} P_{IN} &= 26.7 \text{ W} \\ V_{CC} &= 35 \text{ V} \\ Z_{O}^* &= 10 \text{ ohms} \end{aligned}$

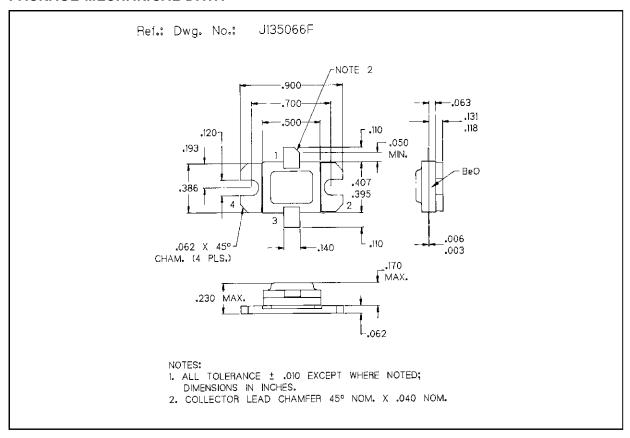
*Normalized Impedance



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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