

# **LET21030C**

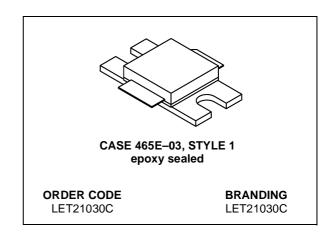
# RF POWER TRANSISTORS

# Ldmos Enhanced Technology

TARGET DATA

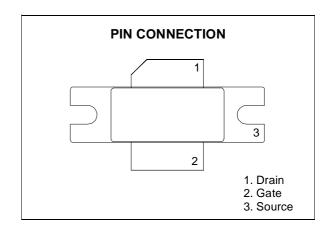
Designed for GSM / EDGE / IS-97 / WCDMA applications

- EXCELLENT THERMAL STABILITY
- P<sub>OUT</sub> = 30 W with 11 dB gain @ 2170 MHz
- BeO FREE PACKAGE
- INTERNAL INPUT MATCHING
- ESD PROTECTION



#### **DESCRIPTION**

The LET21030C is a common source N-Channel enhancement-mode lateral Field-Effect RF power transistor designed for broadband commercial and industrial applications at frequencies up to 2.1 GHz. The LET21030C is designed for high gain and broadband performance operating in common source mode at 26 V. Its internal matching makes it ideal for base station applications requiring high linearity.



#### **ABSOLUTE MAXIMUM RATINGS** (T<sub>CASE</sub> = 25 °C)

| Symbol               | Parameter                           | Value       | Unit |
|----------------------|-------------------------------------|-------------|------|
| V <sub>(BR)DSS</sub> | Drain-Source Voltage                | 65          | V    |
| V <sub>GS</sub>      | Gate-Source Voltage                 | -0.5 to +15 | V    |
| I <sub>D</sub>       | Drain Current                       | 4           | Α    |
| P <sub>DISS</sub>    | Power Dissipation (@ Tc = 70 °C)    | 65          | W    |
| Tj                   | Max. Operating Junction Temperature | 200         | °C   |
| T <sub>STG</sub>     | Storage Temperature                 | -65 to +200 | °C   |

#### THERMAL DATA

| $R_{th(j-c)}$ | Junction -Case Thermal Resistance | 2 | °C/W |
|---------------|-----------------------------------|---|------|

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## **ELECTRICAL SPECIFICATION** (T<sub>CASE</sub> = 25 °C)

### STATIC (Per Section)

| Symbol               | Test Conditions  | Min. | Тур. | Max. | Unit |
|----------------------|--|------|------|------|------|
| V <sub>(BR)DSS</sub> | $V_{GS} = 0 \text{ V}$ $I_{D} = 20 \mu\text{A}$        | 65   |      |      | V    |
| I <sub>DSS</sub>     | V <sub>GS</sub> = 0 V V <sub>DS</sub> = 26 V           |      |      | 1    | μΑ   |
| I <sub>GSS</sub>     | V <sub>GS</sub> = 5 V V <sub>DS</sub> = 0 V            |      |      | 1    | μΑ   |
| V <sub>GS(Q)</sub>   | $V_{DS} = 28 \text{ V}$ $I_D = TBD$                    | 2    |      | 4.5  | V    |
| V <sub>DS(ON)</sub>  | V <sub>GS</sub> = 10 V I <sub>D</sub> = 1 A            |      | 0.29 | 0.4  | V    |
| G <sub>FS</sub>      | V <sub>DS</sub> = 10 V I <sub>D</sub> = 1 A            |      | 2    |      | mho  |
| C <sub>ISS</sub> *   | V <sub>GS</sub> = 0 V V <sub>DD</sub> = 26 V f = 1 MHz |      | TBD  |      | pF   |
| Coss                 | V <sub>GS</sub> = 0 V V <sub>DD</sub> = 26 V f = 1 MHz |      | TBD  |      | pF   |
| C <sub>RSS</sub>     | V <sub>GS</sub> = 0 V V <sub>DS</sub> = 26 V f = 1 MHz |      | TBD  |      | pF   |

<sup>\*</sup> Including input matching capacitor in package ?

| Symbol                                  | Test Conditions   | Min. | Тур. | Max. | Unit |
|---|---|------|------|------|------|
| <b>DYNAMIC</b> ( <i>f</i> = 21)         | 70 MHz)   |      |      |      |      |
| P <sub>OUT</sub> <sup>(1)</sup>         | $V_{DD} = 26 \text{ V}$ $I_{DQ} = TBD$                                  | 30   | 35   |      | W    |
| η <sub>D</sub> <sup>(1)</sup>           | $V_{DD} = 26 \text{ V}$ $I_{DQ} = TBD$                                  | 45   | 50   |      | %    |
| Load<br>mismatch                        | V <sub>DD</sub> = 26 V P <sub>OUT</sub> = 30 W<br>ALL PHASE ANGLES      |      |      | 10:1 | VSWR |
| DYNAMIC (f = 21                         | 10 - 2170 MHz)  |      |      |      |      |
| Pour <sup>(1)</sup>                     | $V_{DD} = 26 \text{ V}$ $I_{DQ} = TBD$                                  | 25   | 30   |      | W    |
| η <sub>D</sub> <sup>(1)</sup>           | $V_{DD} = 26 \text{ V}$ $I_{DQ} = TBD$                                  | 40   | 45   |      | %    |
| G <sub>P</sub>                          | V <sub>DD</sub> = 26 V I <sub>DQ</sub> = TBD mA P <sub>OUT</sub> = 30 W |      | 11   |      | dB   |
| P <sub>OUT(W-CDMA)</sub> <sup>(2)</sup> | ACPR -45 dBc  |      | 5    |      | W    |
| η <sub>D(W-CDMA)</sub> <sup>(2)</sup>   | ACPR -45 dBc  |      | 20   |      | %    |

<sup>(1) 1</sup> dB Compression point

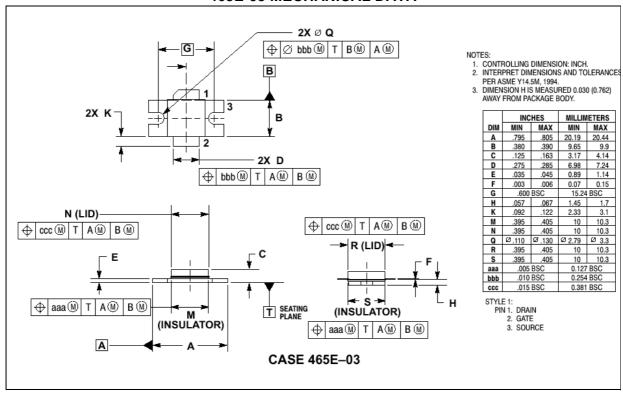
#### **ESD PROTECTION CHARACTERISTICS**

| Test Conditions  | Class |  |
|------------------|-------|--|
| Human Body Model | 2     |  |
| Machine Model    | M3    |  |

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<sup>(2) +/- 5</sup> MHz offset; 3.84 MHz Bandwitdh

#### **465E-03 MECHANICAL DATA**



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